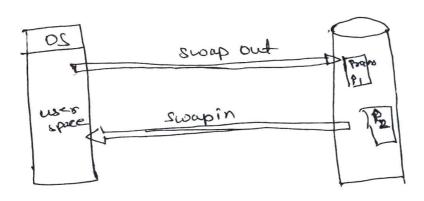
That coupping must be loaded into memory to execute.

. If there is not enough memory available to keep alternating process in memory at the same process who are not using the CPU may have their memory swapping out to a first local disk called backing store.



This is a Technique of temporarily semoving a process from the memory of a computer system.

more component need to exchange data and the component perform transfers at differeng speeds chackes solve the transfer problem by providing a buffer of intermediate speed between the component. If the first device finds the data it needs in the catche finds the data it needs in the catche device. The device cache must be device. The device cache must be kept consistent with the data in the kept consistent with the data in the value change and the datam is also ratue change and the chacke must be

on multiprocessor system coher more than one process may be eleminated, by an equal sized cache but only if i) The cache and the component have equivalent state capacity. ii) The cache is affordable because faster storage tends to be more expensive Solutin(3) yes it is possible to developa new Command interpreter using the System call interpreter is not where tighty integrated into the System. solution (4) Monolithic Kernal - All parts of a kernal like the Scheduler, file System, memory management, Networking stacks, Device Drivers octe, are mainted in one Unit whithin the Kernal in molithic > It is faster processing. > Crash Insecure porting Inflexibility. Kernal Size explosion. Ex:- MS-DOS, UNIX, LINUX. Micro Kernal & Ony the very important

parts like IPC linter process communication)

basis scheduler basic memory handling. basis I/O permitive etc. are put into happen via message the kernal communication happen via message possing other are mandedned as server process in user space

> Crash Resistant, Postable, Smaller size -> Slower processing due to additional message passing Ex windows NT & Every Program assigned a propority. The Epu is always allocated to the helgihest priority program that wishes - A low bejority beodern executing on the CPU passempted if a heigher-priority program wishes to we the CPU. CPU always executing the highest Pristrity program that needs it. The high priority program completed the Kernel would immediately switch the CPU to the hight priority program. CPU acidivity Progios I/O action copy activity I lo activity to to the the Buy Timmly chart when Busy Commilative Busy I/O bound Pragram I/O activity TIM has priority.