$\ensuremath{\mathsf{EE}} 2016$: Microprocessor Theory and Lab

$\begin{array}{c} \textbf{Lab Report} \ \# \ \textbf{4} \\ \textbf{INTERRUPTS IN ATMEL AVR ATMEGA THROUGH} \\ \textbf{ASSEMBLY PROGRAMMING} \end{array}$

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1 Aim of the Experiment

Using Atmel AVR assembly language programming, implement interrupts and DIP switches control in Atmel Atmega microprocessor. Aims of this experiment are:

- Generate an external (logical) hardware interrupt using an emulation of a push button switch.
- Write an ISR to switch ON an LED for a few seconds (10 secs) and then switch OFF. (The lighting of the LED could be verified by monitoring the signal to switch it ON).
- If there is time, you could try this also: Use the 16 bit timer to make an LED blink with a duration of 1 second.

2 Problems

- 1. Fill in the blanks in the assembly code.
- 2. Use int0 to redo the same in the demo program (duly filled in). Once the switch is pressed the LED should blink 10 times (ON (or OFF) 1 sec, duty cycle could be 50 %). Demonstrate both the cases.
- 3. Rewrite the program in 'C' (int1). Rewrite the C program for int0.
- 4. Demonstrate both the cases (of assembly and C).

3 INT 0

3.1 Code in Assembly Language

```
//ASM program to implement external input INTO in AVR
  //switch to PD2
  //LED to PBO
  .nolist
  .include "m8def.inc"
   .list
   .org 0
  rjmp reset
                   ; on reset, program starts here
10
11
   .org 0x0002
                   ; Interrupt vector address for INTO. Put your ISR here or
12

    jump

  rjmp INTO_ISR
                  ; to the ISR
13
15
   .org 0x0100
16
   reset:
17
        ldi R16,0x70; Setup the stack pointer to point to address 0x0070
18
            out spl,R16
19
            ldi R16,0x00
20
            out sph,R16
21
22
            ldi R16,0x01
                              ;make PBO output
23
            out DDRB,R16
24
25
            ldi R16,0x00
                             ; make PORTD as input
26
            out DDRD, R16
27
28
            ldi R16,0x04
                             ; use pull up resistor for PD2
29
            out PORTD, R16
30
31
            in R16, MCUCR
            ori R16,0x02
                              ; set INTO to falling edge sensitive
33
            out MCUCR,R16
                              ; use OR so that other bits are not affected
34
35
            in R16,GICR
36
            ori R16, 0x40
                              ; enable INTO interrupt
37
            out GICR, R16
38
39
            ldi R16,0x00
                              ; Turn off LED
40
            out PORTB, R16
41
42
            sei
                               ; enable interrupts
43
44
            indefiniteloop: rjmp indefiniteloop
45
46
```

```
; INTO Interrupt handler or ISR
             INTO_ISR:
47
                    in R16, SREG
                                  ; save status register
48
                             push R16
49
50
                             ldi R16,0x0a ; blink LED 10 times
                             mov RO,R16
52
53
                                 ; Turn ON LED
           c1: ldi R16,0x01
54
                out PORTB,R16
55
56
                 LDI R16,0xFF
                                 ; delay
57
           a1: LDI R17,0xFF
58
            a2: DEC R17
59
                 BRNE a2
60
                     DEC R16
61
                     BRNE a1
62
63
                      ldi R16,0x00 ; Turn OFF LED
64
                      out PORTB, R16
65
66
                 LDI R16,0xFF
                                ;delay
67
                LDI R17,0xFF
           b1:
68
           b2:
                 DEC R17
                 BRNE b2
70
                     DEC<sub>R16</sub>
71
                     BRNE b1
72
73
                     DEC RO
                                 ; check if LED has blinked 10 times
                     BRNE c1
76
                                    ; retrive status register
                      pop R16
77
                      out SREG, R16
78
79
                      RETI
                                     ; go back to main program
```

Listing 1: Code for INT0 in ASM

3.2 Code in C Language

```
//C program for INTO
#define F_CPU 1000000
#include <aur/io.h>
#include <util/delay.h>
#include <aur/interrupt.h>

ISR (INTO_vect)

{
    // Write your ISR here to blink the LED 10 times
    // with ON and OFF interval of 1 second each
for(int i=0; i<10; i=i+1)</pre>
```

```
{
12
                     //PBO is set to 1 for 1 sec.
13
                     PORTB = 0x01;
14
                     _delay_ms(1000);
15
                     //PBO is set to 0 for 1 sec.
                     PORTB = 0x00;
17
                     _delay_ms(1000);
18
            }
19
   }
20
21
   int main (void)
22
   {
23
            //port i/o declarations
24
            DDRD = 0x00;
25
            DDRB = 0x01;
26
            MCUCR = 0x02; //check
27
28
            GICR = 0x40;
            PORTB = 0x00;
29
30
            //set interrupt flag of SREG
31
            sei();
32
33
            while (1)
            {
35
                     //To keep the program running forever.
36
            }
37
  }
38
```

Listing 2: Code for INT0 in C

3.3 Outputs

Video for INTO ASM:

https://drive.google.com/file/d/1GmlwjWGwj5UysEHzt0Q6WA6dIoC6APzz/view?usp=drivesdk

Video for INT0 C:

https://drive.google.com/file/d/1GePhhQhB-NP2EgTQnhWwiN2enPLHKmlh/view?usp=drivesdk

3.4 Explanation

- The INT0 interrupt, which is associated with pin 4 on an ATmega8, detects various levels and level changes on the INT0 input.
- The interrupt branches to the INT0 vector if the corresponding interrupt is enabled. The states or changes that cause this interrupt can be chosen using the bits ISC01 and ISC00.

- Determining whether the counter is at zero is the main goal. The T flag is checked if it does. If set, the counter is reset to six and a new cycle begins.
- The bTo flag is checked if the cycle counter is not zero (it is set by the CTC ISR when a time-out occurs). If this is set, the next stage is handled. The flow returns to the sleep instruction in any case.

4 INT 1

4.1 Code in Assembly Language

```
//ASM program to implement external input INT1 in AVR
  //switch to PD3
  //LED to PBO
  .nolist
  .include "m8def.inc"
  .list
   .org 0
                   ; on reset, program starts here
  rjmp reset
10
                   ; Interrupt vector address for INT1. Put your ISR here or
   .org 0x0002
   \hookrightarrow jump
  rjmp INT1_ISR
                  ; to the ISR
12
13
   .org 0x0100
14
15
   reset:
16
        ldi R16,0x70; Setup the stack pointer to point to address 0x0070
17
            out spl,R16
18
            ldi R16,0x00
19
20
            out sph,R16
21
            ldi R16,0x01
                              ;make PBO output
22
            out DDRB,R16
23
24
            ldi R16,0x00
                             ; make PORTD as input
25
            out DDRD, R16
26
27
            ldi R16,0x08
                             ; use pull up resistor for PD3
28
            out PORTD, R16
29
30
            in R16, MCUCR
31
            ori R16,0x08
                              ; set INT1 to falling edge sensitive
            out MCUCR,R16
                              ; use OR so that other bits are not affected
33
34
            in R16,GICR
35
                              ; enable INT1 interrupt
            ori R16, 0x80
36
            out GICR, R16
37
            ldi R16,0x00
                              ; Turn off LED
39
            out PORTB, R16
40
41
            sei
                               ; enable interrupts
42
43
            indefiniteloop: rjmp indefiniteloop
44
45
            INT1_ISR:
                                   ; INT1 Interrupt handler or ISR
46
```

```
in R16, SREG
                                     ; save status register
47
                              push R16
48
49
                              ldi R16,0x0a ; blink LED 10 times
50
                              mov RO,R16
52
            c1: ldi R16,0x01
                                  ; Turn ON LED
53
                out PORTB,R16
54
55
                  LDI R16, OxFF
                                  ; delay
56
                 LDI R17, OxFF
            a1:
57
            a2: DEC R17
58
                  BRNE a2
59
                      DEC<sub>R16</sub>
60
                      BRNE a1
61
62
                      ldi R16,0x00 ; Turn OFF LED
63
                      out PORTB,R16
64
65
                  LDI R16,0xFF
                                  ; delay
66
            b1:
                 LDI R17, OxFF
67
            b2:
                 DEC R17
68
                  BRNE b2
                      DEC<sub>R16</sub>
70
                      BRNE b1
71
72
                      DEC RO
73
                      BRNE c1
                                  ; check if LED has blinked 10 times
                                    ; retrive status register
                      pop R16
76
                      out SREG, R16
77
78
                      RETI
79
                                       ; go back to main program
```

Listing 3: Code for INT1 in ASM

4.2 Code in C Language

```
#define F_CPU 1000000
  #include <aur/io.h>
  #include <util/delay.h>
  #include <avr/interrupt.h>
  ISR (INT1_vect)
6
  {
7
           // Write your ISR here to blink the LED 10 times
           // with ON and OFF interval of 1 second each
           for(int i=0; i<10; i=i+1)</pre>
10
           {
11
                   //PBO is set to 1 for 1 sec.
12
```

```
PORTB = Ox01;
13
                     _delay_ms(1000);
14
                     //PBO is set to 0 for 1 sec.
15
                     PORTB = OxOO;
16
                     _delay_ms(1000);
            }
   }
19
20
   int main (void)
21
22
            //port i/o declarations
            DDRD = 0x00;
^{24}
            DDRB = 0x01;
25
            MCUCR = 0x02;
26
            GICR = 0x80;
27
            PORTB = OxOO;
28
29
            //set interrupt flag of SREG
30
            sei();
31
32
            while (1)
33
            {
34
                     //To keep the program running forever.
            }
36
  }
37
```

Listing 4: Code for INT1 in C

4.3 Outputs

Video for INT1 ASM:

https://drive.google.com/file/d/1GkrKFJhulHYAP-31yp1crPQSms0TnDoy/view?usp=drivesdk

Video for INT1 C:

https://drive.google.com/file/d/1GmHhMud-DTHZ62_u5e-77DaNdOpoouPf/view?usp=drivesdk

4.4 Explanation

- The ATmega 8 IC's pin 5 houses interrupt number 1, and the rest of the circuit is connected similarly.
- INT1's interrupt vector address. Jump to the ISR or place your ISR here.
- Set the stack pointer to point at address 0x0070, set PB0 to be an output, PORTD to be an input, and PD3 to be pulled up using a resistor.
- To ensure that other bits are not impacted, set INT1 to falling edge sensitive use OR.
- Save the status register and blink the LED ten times in the INT1 Interrupt handler or ISR. Interrupt.

5 Learning Outcomes

By doing this experiment we were able to:

- Learn how to program in assembly language and how to burn code into board.
- Get familiar with the software Atmel Studio and AVR Burn-OMAT.
- \bullet By using interrupts 0 and 1 with a 50% duty cycle, we learned how to make an LED blink using ASM and the C language.