JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET

CMM. No. : C 0170151

BACHELOR OF TECHNOLOGY- ELECTRONICS & COMMUNICATION ENGINEERING

Serial No. : 21046011115

Name : N SHIVALINGAM

Name of the College : AG-ACEEC,GHATKESAR

Month & Year of Final Exam : April,2012

Hall Ticket No. : 08AG1A0440

Year of Admission : 2008-2009

Class Awarded : FIRST CLASS

S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
	Maximum Marks in Theory	20	80	100			Maximum Marks in Lab	25	50	75	
	I YEAR										
1	ENGLISH	16	57	73	4	2	MATHEMATICS - I	15	48	63	6
3	MATHEMATICAL METHODS	18	33	51	6	4	APPLIED PHYSICS	15	55	70	4
5	C PRG.& DATA STRUCTURES	18	43	61	6	6	NETWORK ANALYSIS	13	43	56	4
7	ELECTRONIC DEVICES & CKTS.	15	33	48	6	8	E4-ENGINEERING DRAWING	20	63	83	4
9	COMPUTER PROGRAMMING LAB	23	44	67	4	10	IT WORKSHOP	21	43	64	4
11	ELECTRONIC DEV.&CKTS.LAB	22	42	64	4	12	ENGLISH LANG.COMM.SKILLS LAB	22	42	64	4
	I SEMESTER						**II YEAR**				**II SEMESTER**
1	MATHEMATICS - III	14	36	50	4	1	PULSE & DIGITAL CIRICUITS	14	49	63	4
2	PROBABILITY THEORY & STO. PROCESSES	15	34	49	4	2	CONTROL SYSTEMS	15	47	62	4
3	ENVIRONMENTAL STUDIES	14	65	79	4	3	OBJECT ORIENTED PROGRAMMING	10	55	65	4
4	SIGNALS & SYSTEMS	12	37	49	4	4	SWITCHING THEORY & LOGIC DESIGN	17	53	70	4
5	ELECTRICAL TEHCNOLOGY	12	33	45	4	5	EM WAVES & TRANSMISSION LINES	11	66	77	4
6	ELECTRONICS CIRCUIT ANALYSIS	15	34	49	4	6	ANALOG COMMUNICATIONS	14	50	64	4
7	ELECTRONIC CIRCUITS (LAB)	19	40	59	2	7	ANALOG COMMUNICATIONS (LAB)	21	40	61	2
8	ELECTRICAL TECHNOLOGY (LAB)	23	30	53	2	8	PULSE & DIGITAL CIRCUITS (LAB)	21	40	61	2
	I SEMESTER						**III YEAR**				**II SEMESTER**
1	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	13	43	56	4	1	DIGITAL SIGNAL PROCESSING	12	46	58	4
2	COMPUTER ORGANIZATION	12	37	49	4	2	MICROPROCESSORS & INTERFACING	14	43	57	4
3	LINEAR IC APPLICATIONS	13	28	41*	4	3	MANAGEMENT SCIENCE	16	48	64	4
4	DIGITAL IC APPLICATIONS	14	29	43*	4	4	VLSI DESIGN	15	53	68	4
5	ANTENNAS AND WAVE PROPAGATION	16	45	61	4	5	MICROWAVE ENGINEERING	14	39	53	4
6	DIGITAL COMMUNICATIONS	13	63	76	4	6	TELECOMMUNICATION SWITCHING SYSTEMS	12	42	54	4
7	DIGITAL COMMUNICATIONS LAB	22	41	63	2	7	MICROPROCESSORS & INTERFACING (LAB)	22	44	66	2
8	IC APPLICATIONS AND ECAD LAB	22	43	65	2	8	ADVANCED ENGLISH COMMN. SKILLS (LAB)	21	46	67	2
	I SEMESTER						**IV YEAR**				**II SEMESTER**
1	COMPUTER NETWORKS	15	41	56	4	1	RADAR SYSTEMS	16	49	65	4
2	ELECTRONIC MEASUREMENTS AND INSTRUMENTATION	15	72	87	4	2	DIGITAL DESIGN THROUGH VERILOG	16	58	74	4
3	CELLULAR AND MOBILE COMMUNICATIONS	16	52	68	4	3	ARTIFICIAL NEURAL NETWORKS	18	30	48	4
4	OPTICAL COMMUNICATIONS	14	70	84	4	4	INDUSTRY ORIENTED MINI PROJECT	-	43	43	2
5	OPERATING SYSTEMS	15	35	50	4	5	SEMINAR	41	-	41	2
6	DIGITAL IMAGE PROCESSING	12	41	53	4	6	PROJECT WORK#	33	136	169	10
7	MICROWAVE AND OPTICAL COMMUNICATIONS (LAB)	23	42	65	2	7	COMPREHENSIVE VIVA	-	91	91	2
8	DIGITAL SIGNAL PROCESSING (LAB)	20	40	60	2						
							(# Project Internal= 40 , External=160)				

Number of Credits registered for : 224 Aggregate Marks Secured for best: 216

Aggregate Marks Secured : 3631 OUT OF 5350 (67.87%)

Date of Issue : June 15, 2012

(see overleaf for Rules concerned to award of class)

A indicates 'ABSENT'

(*Courses registered but not counted for calculation of aggregate)

CONTROLLER OF EXAMINATIONS