

Tutorial 1 Introduction to Embedded Systems

1. Buses are: a) Address b) Control c) Address d) Data e) Address f) Data.
2. (a) Embedded systems may need a powerful processor not just for the GUI, but for sophisticated control algorithms using Fuzzy logic, neural networks and statistical signal processing. Also, process sound and video data.

(b) Older desktop versions will be used for embedded systems. So as desktops increase in performance, so will embedded.

(c) Provide real mode for compatibility and protected mode for advanced features.

(d) Speed, data size, address size, architecture (sec 1.3.2)
3. Differences between embedded and other computers (See section 1.4) Fixed use / small size / limited resource / failure tolerance / real time / response time / reliability / simple I/O / low power / low cost.
4. i) Program Design / Power Supply - types and sources / Processor hardware features
ii) Power supply - use SMPS/LDO / consider battery types (charge/noncharge) / alternative types - sun / movement / “stray” energy - TV/radio signals, etc
- 5.

9V -->0.05A--> 5V

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|-----|
| 7805   |
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Input power = 9V * 0.05A = 0.45W
Output power = 5V * 0.05A = 0.25W
Efficiency = 0.25/0.45 = 55% OR (9-5)V*0.05A wasted as heat!!!

9V -->0.05A--> 5.5V ----- 5V

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|-----|
| SMPS   |
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| LDO   |
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Input power = 9V * 0.05A = 0.45W
SMPS power loss = 9V * 0.05A * (1-0.85) = 0.0675W
LDO power loss = (5.5-5)*0.05=0.025W
Efficiency = (0.45-0.0675-0.025)/0.45 = 0.3575/0.45 = %79

Switched Mode Power Supply

Low Drop Out

6. Sect 1.5.2

Software loops	Hardware
Processor tied up	dedicated h/w
multitasking causes timing variation	timing is accurate
timing varies due to prefetch	can be finer resolution than software