

SINGAPORE POLYTECHNIC

2017/2018 Semester 2 Mid semester Test

FULL MODULE NAME: Embedded Computer Systems

module no.: ET0104

Set by: Tracey Lee

Course: DCPE/DEEE

Year : 3

FT

Q No.	SOLUTION	Marks	Total															
A	1 - a / 2 - a / 3 - c / 4 - d / 5 - c 6 - d / 7 - a / 8 - b / 9 - a / 10 - d	3 marks each	30															
B1.a)	Processor speed / Address bus size / Data bus size / Architectural changes.	4 marks																
b)	Main programming changes: 1) Go into low power mode as soon as possible: standby/hibernate modes 2) Programs are just interrupts, wake up from low power. Similar answers accepted	6 marks																
c)	Health condition, sleep/wake times, driving habits (any 2-others possible)	2 marks	12															
B2.	<table><tr><th>Type of memory</th><th>Use</th><th>Reason</th></tr><tr><td>ROM</td><td>program messages, tables</td><td>non volatile</td></tr><tr><td>RAM</td><td>data, temporary store of recorded images</td><td>read/write</td></tr><tr><td>Flash Memory</td><td>Storage of images</td><td>non volatile, erasable</td></tr><tr><td>Serial EEPROM</td><td>user settings, compute history</td><td>non volatile, erasable</td></tr></table>	Type of memory	Use	Reason	ROM	program messages, tables	non volatile	RAM	data, temporary store of recorded images	read/write	Flash Memory	Storage of images	non volatile, erasable	Serial EEPROM	user settings, compute history	non volatile, erasable	4 marks 3 marks 3 marks 3 marks	13
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B3.	<table><tr><td>North1=0xED</td><td>North2=0xDD</td><td>North3=0xBD</td><td>North4=0x7D</td></tr><tr><td>South1=0xEE</td><td>South2=0xDE</td><td>South3=0xBE</td><td>South4=0x7E</td></tr></table>	North1=0xED	North2=0xDD	North3=0xBD	North4=0x7D	South1=0xEE	South2=0xDE	South3=0xBE	South4=0x7E	8 mks								
North1=0xED	North2=0xDD	North3=0xBD	North4=0x7D															
South1=0xEE	South2=0xDE	South3=0xBE	South4=0x7E															
b)	For 90 keys, consider L is latch, B is buffer 45*2: 6L/1B, 30*3: 4L/1B, 15*6: 2L/1B, 10*9: 2L/2B ∴ 15*6 - least h/w	4 marks	12															
B4.	Since we need 64K, each chip is 16K, # chips needed: 64K /16K = 4 A0 to A13- (2 ¹⁴ = 16384=3FFFh. From D0000H Chip 1 - D0000H to D3FFFH Chip 2 - D4000H to D7FFFH Chip 3 - D8000H to DBFFFH Chip 4 - DC000H to DFFFFH A19 A18 A17 A16 A15 A14 A13 - - - - - A0 1 1 0 1 0 0 x-----x D0000-D3FFFH 1 1 0 1 0 1 x-----x D4000-D7FFFH 1 1 0 1 1 0 x-----x D8000-DBFFFH 1 1 0 1 1 1 x-----x DC000-DFFFFH A17-19 enabled by 74688 Nonvolatile, writable - Flash	2 mk 2 mks 3 mks 2 mks																

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		4 mks	13
B5.			
a)	<p>Goals: Excessive water detection system</p> <ul style="list-style-type: none"> - use water sensor, every minute, motor moves to detect motor - display on water level <p>Constraints: water proof, vertical motion, battery operation (similar answers accepted)</p>	2 mks	
b)	<p>Sub-systems for the design:</p> <p>water sensor / motor / display / timer / μp</p>	2 mks	
c)		4 mks	
		<p>1 mk (user)</p> <p>2 mk (balloon)</p> <p>1 mk 'use arrows'</p>	

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Q No.	SOLUTION	Marks	Total
d)	<pre> sequenceDiagram participant Start participant Timer participant microprocessor participant Motor participant Display Start->>Timer: Timer task start activate Timer Timer->>microprocessor: Start measurement deactivate Timer activate microprocessor microprocessor->>Motor: Move motor activate Motor Motor->>microprocessor: Time's up deactivate Motor microprocessor->>Timer: Load timer deactivate microprocessor activate Timer Timer->>Display: Update display deactivate Timer deactivate Display </pre>	<p>3 marks (arrows) 3 marks (activities) (other solutions possible-as long as reasonable)</p>	
v)	<p>Should display on active light like LED. Definitely not LCD.!!!</p>	2 mks	20