



ANALYSIS OF ASSEMBLY CODE OF BASIC C PROGRAM (GCC 9.3.0)

Concepts of Programming Languages
Assignment -4

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*** C Program used for demonstration ***

* Key features included in Program: For and While loop, Conditional Statement
Switch Statement and assignment

</> Below is the Code, it is just for demonstration purpose, switch case 1 calculates occurrences of 5 in array using linear search and case 2 finds sum of all array elements.

```
1  #include <stdio.h>
2  int main() {
3      int key=2;
4      int i=0;
5      int arr[10]={5,1,2,3,5,9,6,7,5,5};
6      switch(key) {
7          case 1 :
8              {
9                  int count=0;
10                 for(i=0;i<10;i++){
11                     if(arr[i]==5){
12                         count++;
13                     }
14                 }
15                 if(count>0)
16                 {
17                     printf("5 is found %d times.\n",count);
18                 }
19                 else
20                 {
21                     printf("5 not Found in array\n");
22                 }
23                 break;
24             }
25         case 2 :
26             {
27                 int sum=0;
28                 while(i<10) {
29                     sum=sum+arr[i];
30                     i++;
31                 }
32                 printf("Sum of all array elements : %d\n",sum);
33                 break;
34             }
35         default :
36             printf("Enter correct choice\n");
37             break;
38     }
39 }
```


This part of code is the crux of the Main function's control flow, as mentioned in comments frame pointer (%rbp) is pushed onto program stack activation record and required offsets are also set, stack pointer (%rsp) is set to frame pointer.

After that required size according to data is allocated on stack which is 64 in this case, essential data like return address are stored.

Data initialising starts then, starting with integer key used for toggling switch statement which is set to value to 2 (just for demonstration) initially.

Key is at -52(%rbp) 52 here denotes offset and negative sign is because stack grows downwards.

Next integer variable initialised is i, which is used as loop counter is set to value 0, it is present at -64(%rbp), and finally our array of 10 integers is stored as continuous blocks starting from -48(%rbp). Then key is compared to 1, if it is equal to 1 then program control jumps to .L2, else if key is equal to 2, program control jumps to case 2 at .L3, else default is executed in switch and jumps to .L15 to print a statement.

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```
46 .L2:                                ; Code under switch case 1
47     movl    $0, -60(%rbp)          ; Initialising count integer variable with value 0
48     movl    $0, -64(%rbp)          ; Setting our predefined variable i to value 0
49     jmp     .L5                    ; Jump to .L5 , it contains For loop
50 .L7:                                ; For loop body in switch case 1
51     movl    -64(%rbp), %eax         ; Temporarily storing i in register %eax
52     cltq                                ; Converting %eax to sign extended quad word into %rax
53     movl    -48(%rbp,%rax,4), %eax  ; Using frame pointer to access arr by offset,
54     ; by i in %rax takes to arr[i] (by array internal offset 4) and moving to %eax
55     cmpl    $5, %eax               ; Comparing value 5 with arr[i] i.e present in %eax
56     jne     .L6                    ; JUMP to .L6 if (jne) not equal i.e 5!=%eax
57     addl    $1, -60(%rbp)          ; if arr[i] (value in %eax) == 5, then we add 1 to predefined count integer
58 .L6:
59     addl    $1, -64(%rbp)          ; incrementing i
```

When switch case 1 is used, this part of code works namely labels with L2,L7,L6,L5,L8
As mentioned in comments first it initialises a new count integer to 0 at -60(%rbp)
and starts our loop. wherein iterator integer i is set to 0 again and control is transferred to L5 which is discussed below.

L7: it stores i in a temporary register and then sign extends that to a quad word word in register %rax, now frame pointer and offset it access array first element and then takes i from %rax and increments i*4 to address to obtain arr[i] value.

This is stored in register %eax again, now as per algorithm it compares arr[i] in %eax with \$5 and then based on result jump is conditionally called

i.e. if jne (jump if not equal) is called is arr[i]!=5, this transfers control to L6 which increments i. Else i.e. if jne is not called count is incremented.

Variables offset :	name	offset
	i	-64(%rbp)
	count	-60(%rbp)
	sum	-56(%rbp)
	key	-52(%rbp)
	arr (arr[0])	-48(%rbp)

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```
60 .L5: ; For loop in Switch case 1
61     cmpl    $9, -64(%rbp) ; inside for loop, checking for termination condition i.e i==9
62     jle .L7 ; if i<=9, loop iterates, which is jle
63     ; jle (JUMP Less than or eq) to .L7 which has loop body
64     cmpl    $0, -60(%rbp) ; after for loop ended, checking if count is 0
65     jle .L8 ; now if count<=0 (jle) JUMP to .L8, which prints our staement
66     movl    -60(%rbp), %eax ; else , moving value in varaible i to %eax, parameter passed to printf
67     movl    %eax, %esi ; moving value in register %eax to %esi
68     leaq    .LC0(%rip), %rdi ; loading address from .LC0 (String is stored) to %rdi
69     movl    $0, %eax ; moving value 0 to register %eax
70     call    printf@PLT ; Calls printf standard function
71     jmp .L10 ; Unconditional JUMP to .L10 (break;)
72 .L8:
73     leaq    .LC1(%rip), %rdi ; loading address from .LC1 (String is stored) to %rdi
74     call    puts@PLT ; Calls printf standard function
75     jmp .L10 ; Unconditional JUMP to .L10 (break;)
```

As discussed above here we see the code under label L5, where comparison for loop is taking place, at start it compares i with 9, if i<=9 then control is passed on to loop body at .L7.

After loop terminates, count is compared to 0, if count<=0 then control is shifted to .L8 where string from .LC1 is loaded into %rdi and puts (works as printf) is called and then unconditionally jump to .L10 is called indicating label where break statement is executed. If count is not <=0 then, count is stored in %eax and is sent as parameter to printf next along with string at LC0.

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```
72 .L8:
73     leaq    .LC1(%rip), %rdi ; loading address from .LC1 (String is stored) to %rdi
74     call    puts@PLT ; Calls printf standard function
75     jmp .L10 ; Unconditional JUMP to .L10 (break;)
76 .L3:
77     movl    $0, -56(%rbp) ; Initialising integer variable sum to value 0
78     jmp .L11 ; Unconditional JUMP to .L11
79 .L12:
80     movl    -64(%rbp), %eax ; Temporarily storing i in register %eax
81     cltq ; Converting %eax to sign extended quad word into %rax
82     movl    -48(%rbp,%rax,4), %eax ; Using frame pointer to access arr by offset
83     ; by i in %rax takes to arr[i] (by array internal offset 4) and moving to %eax
84     addl    %eax, -56(%rbp) ; adding %eax (arr[i]) to predefined sum
85     addl    $1, -64(%rbp) ; incrementing i in while loop
86 .L11:
87     cmpl    $9, -64(%rbp) ; inside while loop, checking for termination condition i.e i==9
88     jle .L12 ; if i<=9, loop iterates, which is jle
89     ; (JUMP Less than or eq) to .L12 which has loop body
90     movl    -56(%rbp), %eax ; moving variable sum value to %eax, parameter to printf
91     movl    %eax, %esi ; moving %eax to %esi, passing as parameter to print
92     leaq    .LC2(%rip), %rdi ; loading address from .LC2 (String is stored) to %rdi
93     movl    $0, %eax ; moving value 0 to register %eax
94     call    printf@PLT ; Calls printf standard function with parameters
95     jmp .L10 ; Unconditional JUMP to .L10 (break;)
```

Here, L3 is shown which is first called when switch is under case 2, and it initialises integer sum to 0 at -56(%rbp) and unconditional transfer happens to L11 where while loop body is present.

In While loop (L11) first 'i' is compared with 9, if i<=9 loop is executed else terminated. Inside loop body L12 where our main algorithm works, in L12 'i' is temporarily stored in %eax and again sign extended to quad word in %rax, and now as we have seen in for loop similar access to array element is made using offset and frame pointer and arr[i] is stored into %eax, now arr[i] is added to sum and i is incremented.

After loop terminates sum is stored in %eax ad sent as parameter for printf along with string from .LC2 (obtained from address: leaq) and then printf is called and control transferred to L10 which is break statement.

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```
96 .L15:
97     leaq    .LC3(%rip), %rdi        ; loading address from .LC3 (String is stored) to %rdi
98     call    puts@PLT               ; No Parameter print hence calling puts standard function
99     nop                             ; No operation
100 .L10:
101     movl    $0, %eax               ; Break execution
102     movq    -8(%rbp), %rdx          ; moving value 0 to %eax
103     xorq    %fs:40, %rdx           ; moving return address to %rdx
104     je      .L14                   ; JUMP if equal to .L14 to return from Main function
105     call    __stack_chk_fail@PLT    ; calls for function which checks Program Stack Fail
106 .L14:
107     leave
108     .cfi_def_cfa 7, 8
109     ret                                ; returns control to return address
110     .cfi_endproc
```

Now, L15 here is for default case in switch which prints a statement stored in LC3 and then puts function is called which works like printf and at the end nop indicates no operation. In L10, which is our break statement gets the return address from activation record and compares using XOR and then conditionally L14 is called which returns control to address pointed by return address and ends program process. Else if jump L14 doesn't take place call to function is made which checks program stacks failure.

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```
111 .LFE0:                                ; Compiler Data
112     .size    main, .-main
113     .ident    "GCC: (Ubuntu 9.3.0-17ubuntu1~20.04) 9.3.0"
114     .section    .note.GNU-stack,"",@progbits
115     .section    .note.gnu.property,"a"
116     .align 8
117     .long    1f - 0f
118     .long    4f - 1f
119     .long    5
120 0:
121     .string    "GNU"
122 1:
123     .align 8
124     .long    0xc0000002
125     .long    3f - 2f
126 2:
127     .long    0x3
128 3:
129     .align 8
130 4:
```

This data is regarding the compiler options and its details.

----- THE END -----