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Subject Name	High Performance	Date of Exam	08/06/2021
Subject Code	52753	Semester	VIII
Invigilator Signature	Total No. of Pages	Student Signature	Airean
- They are us	chniques are used to a sed to solve major soc alance, IPC que for load balanung 2 types	sign task	to processes.
- Before exec processes.	Les are unknown a stable de imbalance.	ks are dist	nbuted among
lenous loa	d imbalance.	11 0	
- State ma	pping is applicable for to	tasks that	are generated
stahically ar	la advance unform com	putational 1	iquixements are
known in ad	rance	•	
	•		
(ii) Dynamic	Magping:	,	
- Alon refer	ed to as dynamic load	balanuna	) .
- Pasks are	ed to as dynamic load distributed at run him e Page No. (01/08)	Ö	

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	e for tasks that			
1, 100	enerated dunan	ocally.		
b. )	encrated dynaminate non-unifor	mi computation	d requirements.	
			V	
- Two diff	erent claves of a	lynamic mappi	ng.	
(i) Cookers	·	Mana 6 a		
Ci) (eiirae	ized dynamic -	Mapping.		
- All exer	cutable tasks are	maintained in	a coma comman	
	nichite.		ca c	
	e maintained by	special processes	,.	
• M	laster: Manage	the aroup of	avaîlabictasks.	
ي .	<u>laster: Manage</u> <u>lave:</u> Depend on	master to obtain	in work.	
- when a	slave process h	as no task, if	take a portion of	
available	work from mas	ster; and a new	task is generated,	
it is ado	led to the pool of	of tasks in the 1	naster process.	
- Main i	erni ir munu u	rany process ar	e used, the master	
becomes bottleneck.				
اع مان ماه مان	- 2010: Chunk scheduling, where when a process mens out of			
work it gets tasks from master. In other words chank of group work will be assigned at once.				
	- / (DOY)	- will be assign	IKO WA VIICO	
(ii) Distributed Dynamic mapping				
- Taske are distributed among processes.				
- Taske are dismibuted among processes.  - They exchange task while runhime to dismibute load.				
- Each process can send or receive work from other processes.				
- Main issue is now sending & recieving processes are pained				
- Each process can send or receive work from other processes.  - Main issue is how sending is receiving processes are paired together and who will initiall the work transfer.  Page No. (02/08)				
	I age		,	

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B. Dieus in	detail priperine	nazards with	its lype	
-> Pipeline +	fazards occur w	hen instructions	read or write	
maisters +	nat are used bu	other insmuho	na.	
- The Wife	of conflicts that	anise are divid	led into 8 main	
categories	y ,	e e	,	
ci) Sm	chural Hazards	C. Resource con	fucts).	
- These	hazards are caus	sed toy access to	nemony by 400	
instruction-	at a same hime	These conflicts	can be solved by	
using sepe	rate instruction	s in the pipuline	2	
- Ost o	ceurs when the p	procusor is not ca	pable af executing	
au instru	thons simultaneou	usly.	,	
- Not so 1	prevalent in mo	dern processors	as the Onstruction	
set archit	ecuture is design	ed to support	pipelining.	
		$T^{\dagger}$	, ,	
(ii) Data t	tazards Coata	Dependency)		
- Stocur	s when the aux	ment instructions	depends on the	
	e previous instruc			
available.	)			
- Divided in 4 Categorius:				
(a) RAR Hazard.				
- Occurs when a instructions read from same register.				
- Downot cause a major problem as reading downot				
means changing value in register.				
- Therefore, two instructions that have RAR Hazard can				
execute in successive cycles.				
ADD n 82 Y3 F Both instructions read				
Sun	84 85 83		creating RAK	
			J	
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b. RAW Hazard.					
D. Filtra					
- occurs when an instruction reads register that was written					
bu C	by previous instruction				
- ALSO	- Also called data dependencies or mic dependencies.				
eg: ADD	Y1 72 Y3	Submact	reads the olp of ADD		
U SUB	rurg 81 4	creating	RAW Mazara		
	C WAR & WAW				
- Also c	alled as Name d	ependencies.			
- These	occurs when a	olo of register	af an instruction has		
been re	ead or vontren to	oy previous insti	MCIPITS:		
- of pro	cessor executes	instructions in T	he sequence They		
oceny a	ind uses the sam	re preline for	all instruenons not		
L WAW	nazards donot	cause any pr	oblem in execution		
process					
eg:		·			
ADI	n 82 43	` \			
SUB 82 Y3 Y6 WAR Hazard.					
A-D.	400 m. Y2 Y3				
Suc	1 7 758	<b>~</b> 6			
SUB TI YS Y6					
WAW Hazard.					
(1900) Branch Hazards.					
-Branch ins	muchons, partice	uarty condinor	nal branch inst <sup>n</sup>		
create da	-Branch instructions, particularly conditional branch instr.  create data dependencies blo branch instr & previous its  fetch stage of the pipeline.  Page No. ( D4 / D8 )				
fetch stage of the pipeline.					
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- since branch computes address of next instructions.				
that the	the metion sho	ruld be fetched from.		
- ot cons	umo time & also	hime is aquired to flush the		
pipulina	& fetch instruction	on from the calculated target		
uaani.				
- A Lot of	hime is wasted i	nit lis called as Branch		
penalty	•			
83 Solve an	y two out of H	hree.		
B. State & e	rplain the per	formance metrics.		
e ovaa	<u>up</u>			
- The	speed increase	because of the parallel system.		
TO THE TANK I	ALLINE, VUITADOMIALA	No distriction is called		
9 ( 13 ) n	e ranoof the s	pied of paravel system to that of		
L				
excust a	1 also be given	as the ratio of the nine taken to		
Daraled to	weton.	sequential system to that on a		
Represent	ed at SCn) 1	Programme and the second secon		
Sin	)=T(1)	e a givai as.		
execute a program on sequential system to that on a parallel mystem.  Represented as SCn) & is given as.  SCn) = T(1)  T(n).				
b. Efficience	u·			
- Ethiciency in parallel auchen in the minh				
speed-up obtained by a system to the ideal speedup that should be achieved according to the				
that should be arnieved according to the no of				
processors wed in a parallel system.				
- Sold time unactioned by an in-				
processor should be T(1)/n  Page No. (05/08)				
1	Page No. (	105/08)		

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	Thus, efficiency (n) can be given as					
	M or	2(n) = Actual	Speed-up =	Ta)		
	76	2(n) = Adual - Odeal	Speed-up	n (Tin).		
	b) b eve.					
	acli	Tal speedup = 1	(Tin) 2 9d	eal speedup= n/(1)		
	c. Throug	hput				
	<b>→</b>					
	- Through	part of a syste	m is defined as T	he number of is represented as W.		
	program	s executed per u	nithme, this	is represented as W.		
	and is	given as:				
_	Ws =	: Number of pr Time in sea	99 rams			
		Time in seu	onds.			
	d. Salabil	9/	<del>-</del>			
	a. Samabil	141				
	A paralla	L Alltera ?	h h	or 4-		
	- H Paralle	eystem is said	W De scalable	if the same		
	efficiency is obtained by increasing the number of processor.					
	- since efficiency is dependent upon The number of processors					
	- Since efficiency is dependent upon the number of processors.  2 it keeps on decreasing as we increase the no of processor.  - We have $\eta = T(y)$					
	$\frac{-\text{ Ne have } \eta = \frac{1(\eta)}{n(T(n))}$					
	• C • • • • • • • • • • • • • • •			- <b>A</b> L		
	if we increase in them it will decrease, so to have a stable or same efficiency T(n) should be reduced in the same proportion as that of increase in 'n'.					
	SUBJEOR	same expany	1cm & nowa	be realized in		
	THE same	proportion us tina	it of increase in	D ·		
		·				
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A. write a	parallel MPI	program to b	roadcast a dala	
from a r	pot process to 4	other processes.	roadcast a dalā	
· <b>-</b> ) '		•		
The follow	ing program is	withten in py	thon programming	
language	using the nah	ie MPI librari	called mpi4py	
0 0 /	0			
Code:		· · · · · ·		
		·	,	
from mpi	4py Pomport MF	L		
J .		,		
committee	= MPI. CO	mm_world.		
#using co	omm-world as	communicator.		
	mm. Get_rank		·····	
# To get r				
	nm · Get_size ()			
# To get siz	9			
J				
data = 0.			·	
	:			
def oxint	message ( da	ita_, rank		
def print_message ( data_, rank) print (f' Data : { data_4 Reviewed at process Ed: { rank_4'})				
oxecu ed: Synk & 1)				
er i I-n. H checking for slave process.				
if rank!=0: # checking for slave process.				
data = comm. recv (source = rank-1, tag = 11)				
print message (data, oank)  data += 1				
dala+=1				
comm send (data, dest = (rank+1)% size, lag=11)				
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1	_			

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if rank	==0; # Mas	der process.			
data	==0; # Mas = comm.recv (	source - size -1	tag = 11).		
	alā   = 0 :		, J		
)	not message Cd	lala rank).			
Com	n'nt message Cd m: send ( dalā, de	ote (rank+1)%	size, fag=11)		
Alternativ	celu: we an als	O USC MPI BOOM	t cammand as		
follows:	rely, we can als	primitive			
()0110002					
from mo	14 py import M	IPI .			
J	/3/				
comm = m	npl. comm_work	bu			
	mm. Get.rank()				
	mm. Get size ()				
if rank = :	=0:		·		
davā =	[i for Pin ra	nge (size)]			
eve:					
dala = None.					
dala = comm b cast L dala, root=0)					
print (f' para: { data y on the process i rank y').					

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