Smit Shah - 201701009

Q3. Adjacency to Incidence Matrix

```
void adj2inc(int **adj, int **inc, int n, int m)
    // Converts a given adjacency matrix to incidence matrix.
    // Uses lower-half matrix (symmetric) to calculate the adjacency
matrix
    int i, j, edge id = 1, no edges = 0;
    // outer loop parallelizable. We can also parallelize inner loop
but that will involve usage of critical sections
    // We use dynamic scheduling which means that the compiler will
schedule the given process during runtime.
    // We do this because each iteration does not have equal amount
of load (iterations closer to n will be more loaded)
    //#pragma omp parallel for private(j, dist) schedule(dynamic)
    for (i = 1; i <= n; i++)
        for (j = 1; j \le i; j++)
        {
            if (adj[i][j] != 0)
                i == j? no edges = adj[i][j] / 2 : no edges =
adj[i][j];
                for (int k = 0; k < n edges; k++)
                    inc[i][edge id] = 1;
                    inc[j][edge id] = 1;
                    edge id++;
                }
            }
        }
    }
Inner Loop Parallelization
void adj2inc(int **adj, int **inc, int n, int m)
```

```
{
    // Converts a given adjacency matrix to incidence matrix.
    // Uses lower-half matrix (symmetric) to calculate the adjacency
matrix
    int i, j, edge id = 1, no edges = 0;
    for (i = 1; i <= n; i++)
        //#pragma omp parallel for private(j, dist)
        for (j = 1; j \le i; j++)
            if (adj[i][j] != 0)
                i == j ? no_edges = adj[i][j] / 2 : no_edges =
adj[i][j];
                for (int k = 0; k < n edges; k++)
                    //Critical section. Use locks or omp critical.
                    mutex lock();
                    inc[i][edge id] = 1;
                    inc[j][edge id] = 1;
                    edge id++;
                    mutex unlock();
                }
            }
        }
    }
}
```