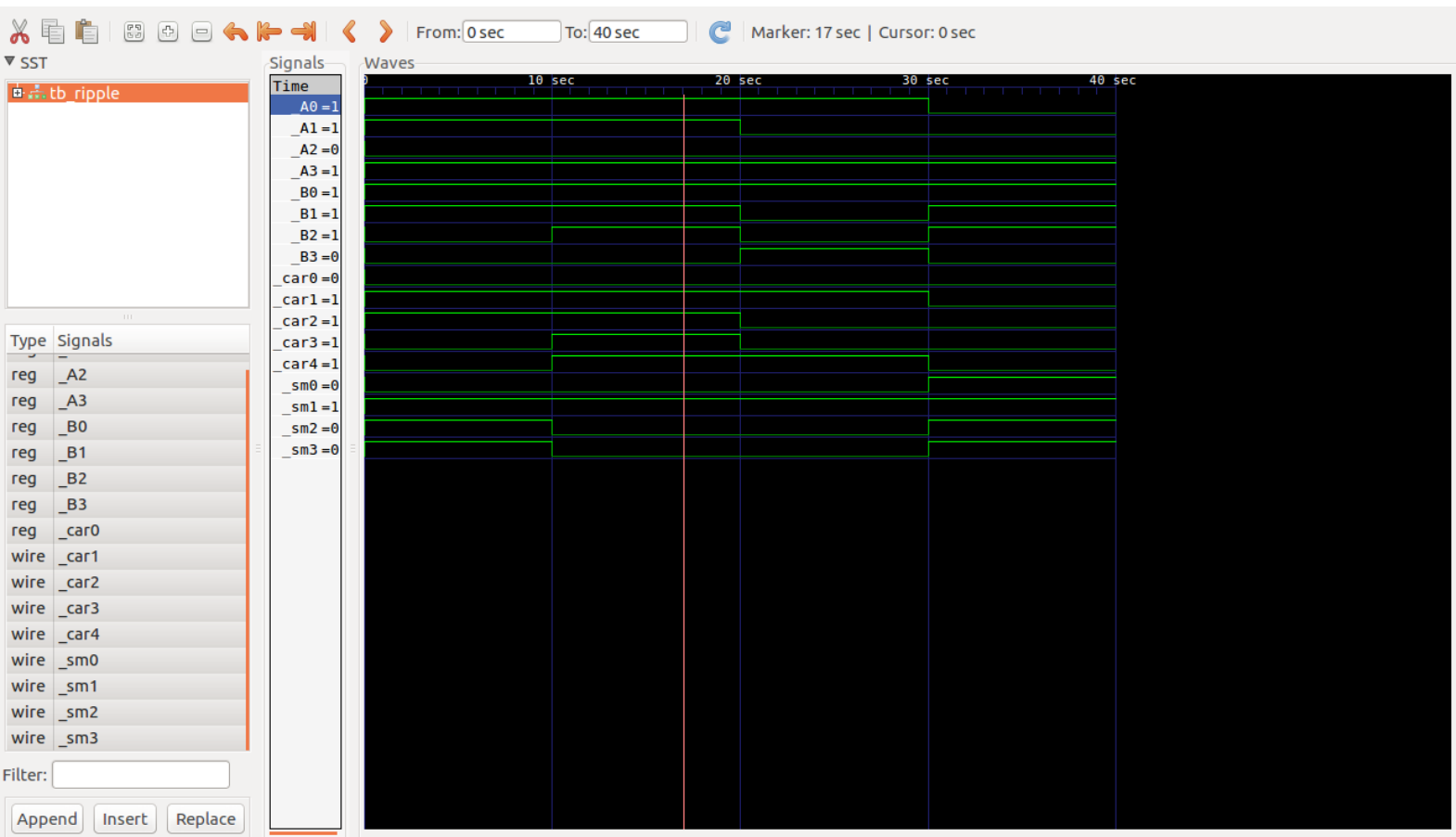


EHD_Report for Lab_1

Shivani Chepuri
Roll: 2018122004
UG2 – ECD



The above image shows the output waveform of the ripple carry adder for 4 different inputs, holding each of the inputs for 10 delay units(seconds).

_sm3 _sm2 _sm1 _sm0 respectively gives the msb to lsb bits of the sum output of the ripple carry adder.

_car4 _car3 _car2 _car1 respectively gives the msb to lsb bits of the carry output of the ripple carry adder.

A3,A2,A1,A0 are the bitwise inputs of A input,from msb to lsb
B3,B2,B1,B0 are the bitwise inputs of B input,from msb to lsb