SHIVA PRASAD DAS

E-Mail: shivaprasad767@gmail.com **Contact#: +91-750-428-7322**

Career Objective:

To pursue a challenge career and be a part of progressive organization that gives a scope to enhance my knowledge and utilizing my skills towards the growth of the organization

Academic Qualifications:

- B.Tech (ECE) : Silicon Institute Of Technology, Bhubaneswar, passed out on 2019, with **7.88 CGPA**

ACADEMIC GPA BY SEMESTER								
GPA								**CGPA**
1st Sem	2nd Sem	3rd Sem	4th Sem	5th Sem	6th Sem	7th Sem	8th Sem	
6.96	7.6	7.78	7.68	8.33	8.29	8.11	8.78	**7.88**

- XII: Kendriya Vidyalaya No-1,Bhubaneswar (CBSE), Passed out in the year 2015, with 62.8%
- X : Kendriya Vidyalaya No-1,Bhubaneswar (CBSE), Passed out in the year 2013, with 9.2 CGPA

Internships & Trainings:

- Done an Internship based Training in Eduvance & Karkhana Makerspace, Specializing in IOT & Embeeded Systems on June 2018
- Done an Internship based Training in CTTC on June 2017

Technical Skills:

Programming Languages : Computer languages: C/C++, JAVA, Python,HTML/CSS, JavaScript
Verilog, VHDL

Project related Activities

A Low Power SPI/I2C Serial SRAM Suited for IoT Based Embedded System (XC06M3-18/A0)

This project describes the design and implementation of a low-power SPI/I2C accessible SRAM implemented in a 0.6um CMOS technology. The innovation of this project lies in creating and controlling all the signals required for the SRAM without use of internal clocks, deriving all control signals from the SPI/ I2C signals themselves instead, thereby making it a simple and scalable circuit.

Home Automation in Verilog HDL

This project is implemented to introduce an efficient home automation system. This design is stimulated in Verilog HDL using Xilinx and Altera. This system helps to monitor the security and

comfort of a home. Security system includes detection of fire, intruders through doors, windows and garage protection. The comfort system is designed to control the temperature and luminosity. User can easily control the device by employing a central Field Programmable Gate Array (FPGA) controller to which the devices and sensors are interfaced.

Music Operated Dancing LEDs

This project is designed to create a circuit in which LEDs will flash according to music.

Developing a Website for Keeping Lyrics of AOL Bhajans

I have taken this Initiative to design a Simple Website for hosting the Art of Living Bhajans with them

Extra-Curricular Activities

Diploma in 'Sangeet Visharad'(Tabla)
Volunteer in Social Projects of The Art of Living
Felicitated as a Super Scholar by LectureNotes for having 200k views on my notes.

Workshops and Seminars

- Attended two day workshop on "Digital Image & Video Processing".
- Attended two day workshop on "Recent trends in VLSI Devices and Circuits".
- Attended seminar on "Artificial Intelligence and Machine Learning".
- Attended seminar on "LTE Overview".
- Attended workshop on Hands on Python from Udemy.

Personal Details

Name	: SHIVA PRASAD DAS
Date of Birth	: 04-Oct-1997
Gender	: Male
Nationality	: Indian
Contact Address	: PLOT NO -624/625(C), BEHERA SAHI, NAYAPALLI, BHUBANESWAR-751012

Place: Bhubaneswar

Date: 5.12.2019 **Shiva Prasad Das**