

1. Description

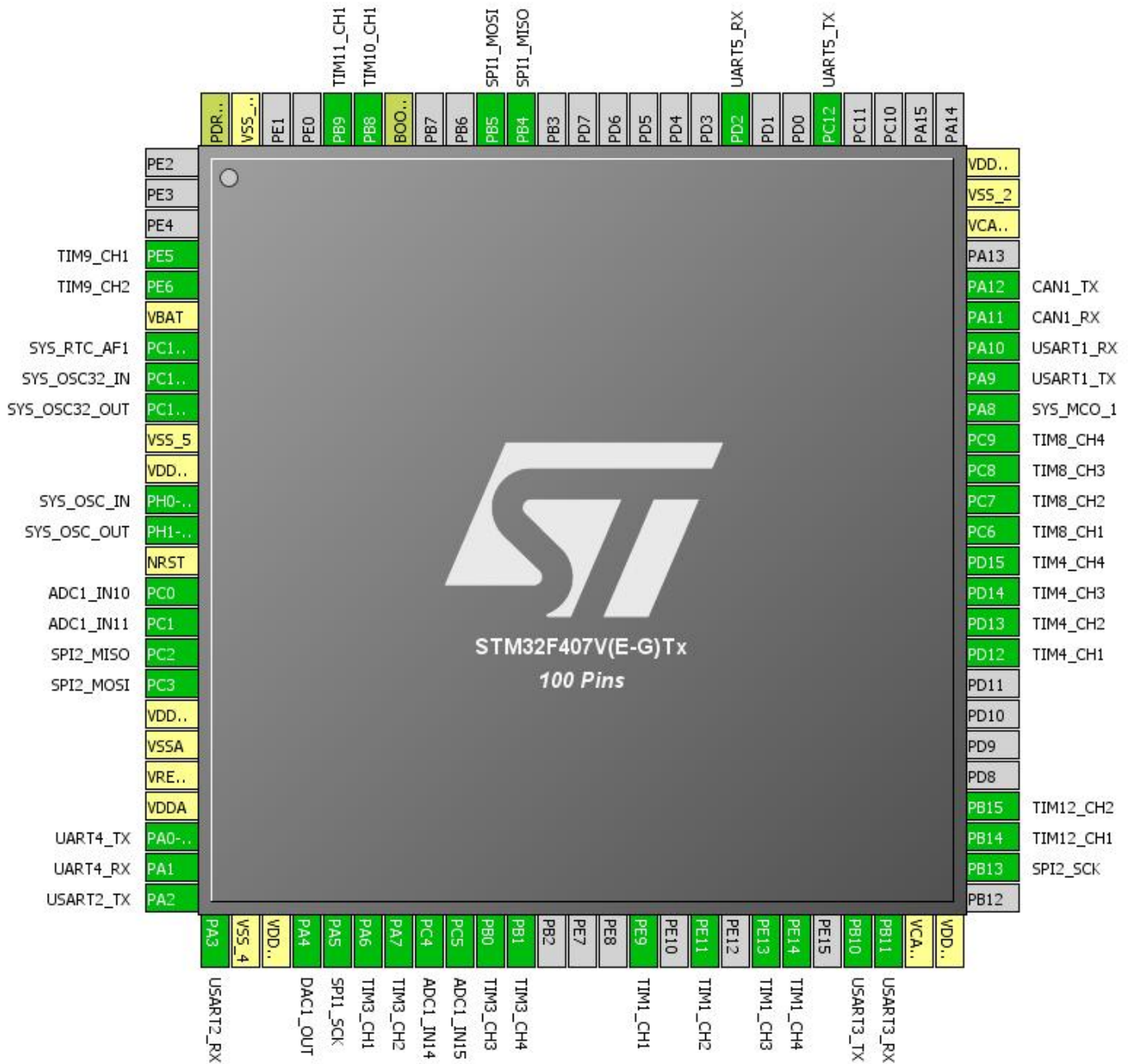
1.1. Project

Project Name	Tilt-MCU
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1.2. MCU

MCU family	STM32
MCU name	STM32F407V(E-G)Tx
MCU Package	LQFP100
MCU Pin number	100

2. Pinout Configuration



3. Peripherals Configuration

Peripheral	Mode	Remap	Fonction	Pin
SPI1	Full-Duplex-Master	1	SPI1_MISO	PB4
		1	SPI1_MOSI	PB5
		0	SPI1_SCK	PA5
SPI2	Full-Duplex-Master	0	SPI2_MISO	PC2
		0	SPI2_MOSI	PC3
		1	SPI2_SCK	PB13
USART1	Multiprocessor-communication	0	USART1_RX	PA10
			USART1_TX	PA9
USART2	Multiprocessor-communication	0	USART2_RX	PA3
			USART2_TX	PA2
USART3	Multiprocessor-communication	0	USART3_RX	PB11
			USART3_TX	PB10
UART4	Multiprocessor-communication	0	UART4_RX	PA1
			UART4_TX	PA0-WKUP
UART5	Multiprocessor-communication	0	UART5_RX	PD2
			UART5_TX	PC12
CAN1	CAN	0	CAN1_RX	PA11
			CAN1_TX	PA12
ADC1	IN10	0	ADC1_IN10	PC0
	IN11	0	ADC1_IN11	PC1
	IN14	0	ADC1_IN14	PC4
	IN15	0	ADC1_IN15	PC5
TIM1	PWM-Generation	0	TIM1_CH1	PE9
			TIM1_CH2	PE11
			TIM1_CH3	PE13
			TIM1_CH4	PE14
TIM8	PWM-Generation	0	TIM8_CH1	PC6
			TIM8_CH2	PC7
			TIM8_CH3	PC8
			TIM8_CH4	PC9
TIM3	PWM-Generation	0	TIM3_CH1	PA6
			TIM3_CH2	PA7
			TIM3_CH3	PB0
			TIM3_CH4	PB1
TIM4	PWM-Generation	0	TIM4_CH1	PD12
			TIM4_CH2	PD13
			TIM4_CH3	PD14
			TIM4_CH4	PD15
TIM9	PWM-Input	0	TIM9_CH1	PE5

Tilt-MCU Project

Peripheral	Mode	Remap	Fonction	Pin
			TIM9_CH2	PE6
TIM12	PWM-Input	0	TIM12_CH1	PB14
			TIM12_CH2	PB15
TIM10	PWM-Generation	0	TIM10_CH1	PB8
TIM11	PWM-Generation	0	TIM11_CH1	PB9
DAC1	Dac	0	DAC1_OUT	PA4
SYS	SYS-OSC	0	SYS_OSC_IN	PH0-OSC_IN
			SYS_OSC_OUT	PH1-OSC_OUT
	SYS-OSC32	0	SYS_OSC32_IN	PC14-OSC32_IN
			SYS_OSC32_OUT	PC15-OSC32_OUT
	TAMPER1/TIMERSTAMP/ALARM_OUT/512Hz	0	SYS_RTC_AF1	PC13-ANTI_TAMP
	Clock-out-1	0	SYS_MCO_1	PA8

4. Pins configuration

Pins	Pos	Functions
PE5	4	TIM9_CH1
PE6	5	TIM9_CH2
PC13-ANTI_TAMP	7	SYS_RTC_AF1
PC14-OSC32_IN	8	SYS_OSC32_IN
PC15-OSC32_OUT	9	SYS_OSC32_OUT
PH0-OSC_IN	12	SYS_OSC_IN
PH1-OSC_OUT	13	SYS_OSC_OUT
PC0	15	ADC1_IN10
PC1	16	ADC1_IN11
PC2	17	SPI2_MISO
PC3	18	SPI2_MOSI
PA0-WKUP	23	UART4_TX
PA1	24	UART4_RX
PA2	25	USART2_TX
PA3	26	USART2_RX
PA4	29	DAC1_OUT
PA5	30	SPI1_SCK
PA6	31	TIM3_CH1
PA7	32	TIM3_CH2
PC4	33	ADC1_IN14
PC5	34	ADC1_IN15
PB0	35	TIM3_CH3
PB1	36	TIM3_CH4
PE9	40	TIM1_CH1
PE11	42	TIM1_CH2
PE13	44	TIM1_CH3
PE14	45	TIM1_CH4
PB10	47	USART3_TX
PB11	48	USART3_RX
PB13	52	SPI2_SCK
PB14	53	TIM12_CH1
PB15	54	TIM12_CH2
PD12	59	TIM4_CH1
PD13	60	TIM4_CH2
PD14	61	TIM4_CH3
PD15	62	TIM4_CH4
PC6	63	TIM8_CH1
PC7	64	TIM8_CH2
PC8	65	TIM8_CH3

Pins	Pos	Functions
PC9	66	TIM8_CH4
PA8	67	SYS_MCO_1
PA9	68	USART1_TX
PA10	69	USART1_RX
PA11	70	CAN1_RX
PA12	71	CAN1_TX
PC12	80	UART5_TX
PD2	83	UART5_RX
PB4	90	SPI1_MISO
PB5	91	SPI1_MOSI
PB8	95	TIM10_CH1
PB9	96	TIM11_CH1