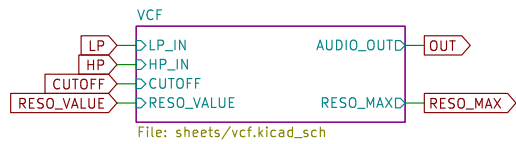
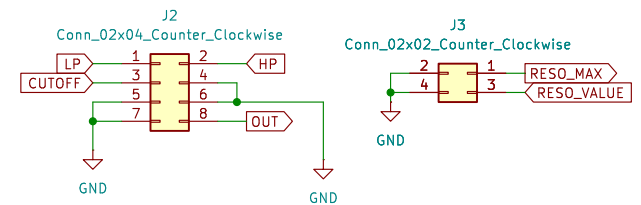
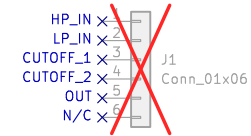


Filter

I/O



Default patch connector



Shielding pairs
DO NOT GROUND ON UI BOARD!

Power

Power

File: sheets/power.kicad_sch

H1 MountingHole H2 MountingHole H3 MountingHole H4 MountingHole

Shmøergh

Sheet: /
File: vcf-core.kicad_sch

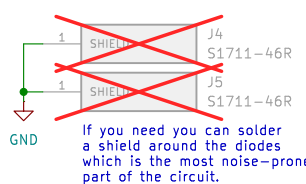
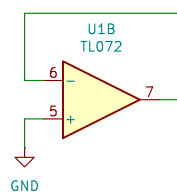
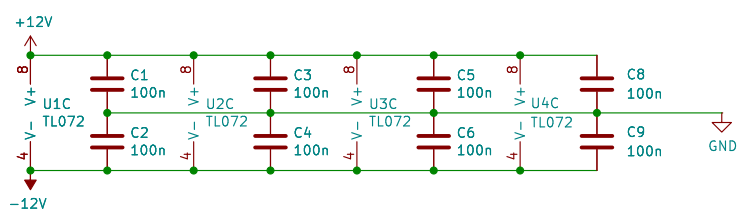
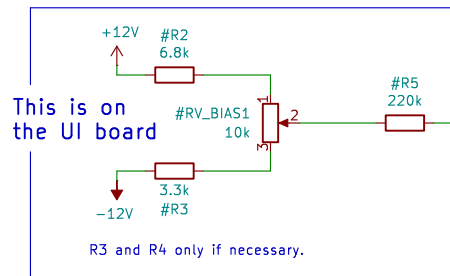
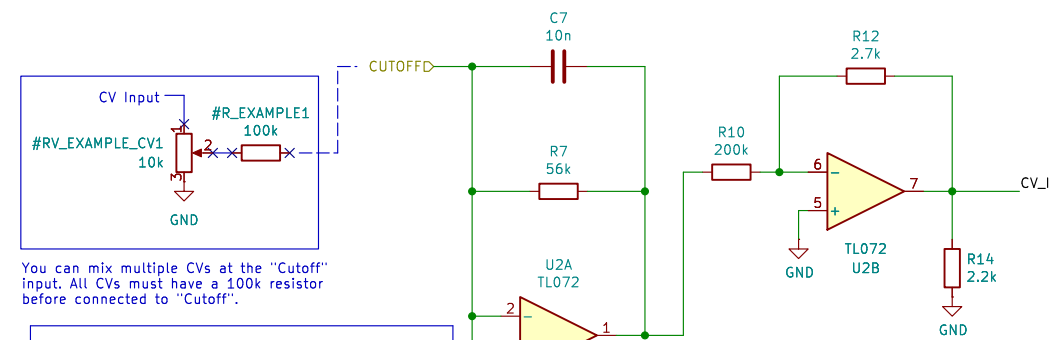
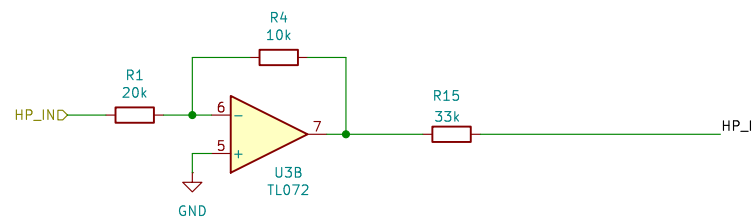
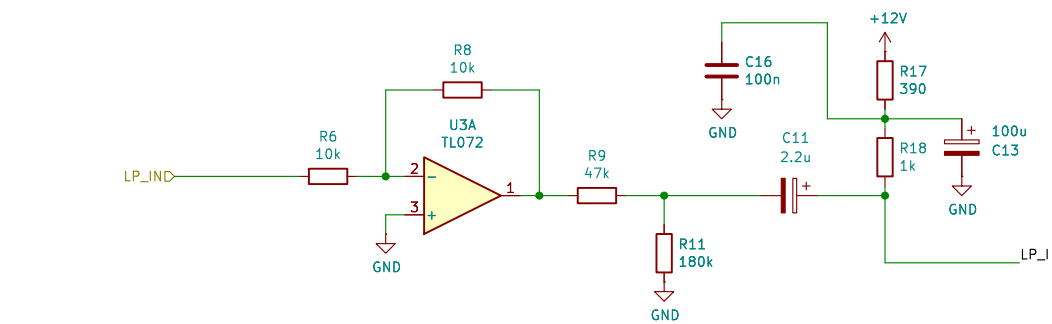
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Size: A4 Date: 2026-02-21

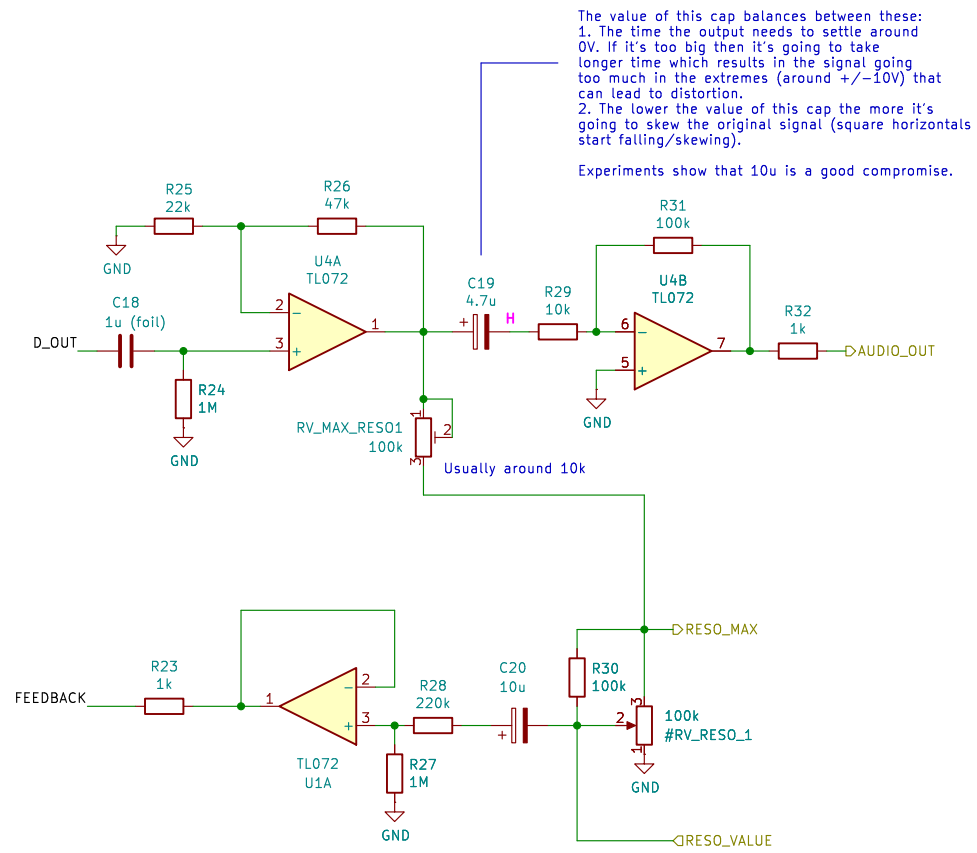
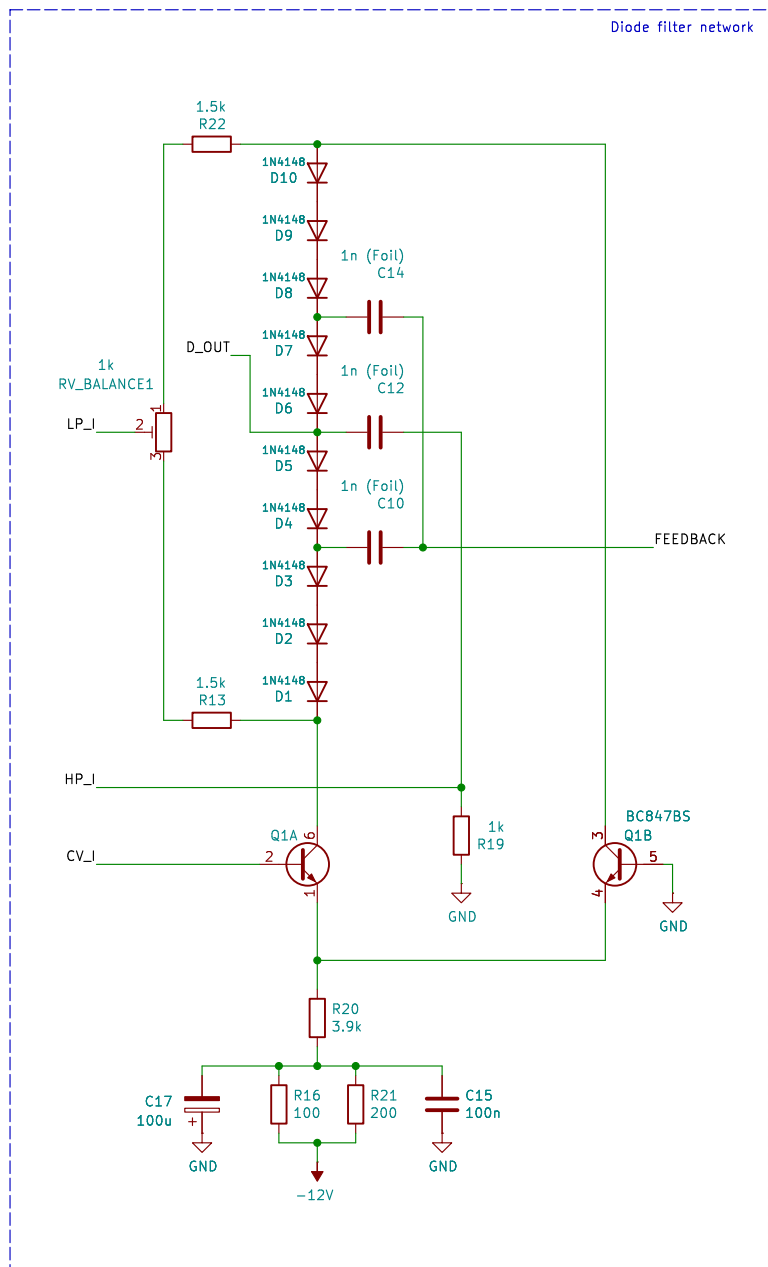
KiCad E.D.A. 9.0.6

Rev: v1.0

Id: 1/3



H = Hand soldered



The value of this cap balances between these:
1. The time the output needs to settle around 0V. If it's too big then it's going to take longer time which results in the signal going too much in the extremes (around +/-10V) that can lead to distortion.
2. The lower the value of this cap the more it's going to skew the original signal (square horizontals start falling/skewing).
Experiments show that 10u is a good compromise.

