

Digital Electronics

Unit 3 Flip flop

1. What is the difference between combination circuit and sequential circuit?
2. Explain following flip flop with truth table and logic diagram.
 - i. Positive edge triggered S-R flip flop
 - ii. Positive edge triggered J-K flip flop
 - iii. Positive edge triggered D flip flop
 - iv. Positive edge triggered T flip flop
 - v. Negative edge triggered S-R flip flop
 - vi. Negative edge triggered J-K flip flop
 - vii. Negative edge triggered T flip flop
 - viii. Negative edge triggered D flip flop
 - ix. Master-Slave (Pulse Triggered) S-R Flip flop
 - x. Master-Slave (Pulse Triggered) J-K Flip flop
 - xi. Master-Slave (Pulse Triggered) D Flip flop .
3. Convert following flip flop
 - S-R flip flop TO J-K flip flop
 - J-K flip flop TO S-R flip flop
 - S-R flip flop TO D flip flop
 - D flip flop TO S-R flip flop
 - J-K flip flop TO T flip flop
 - T flip flop to J-K Flip flop
 - J-K flip flop TO D flip flop
 - D flip flop TO J-K flip flop