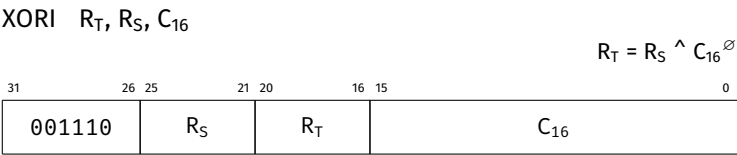
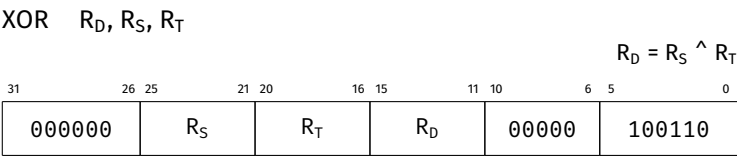
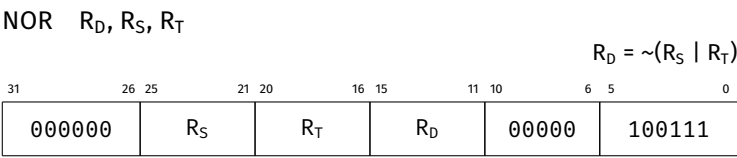
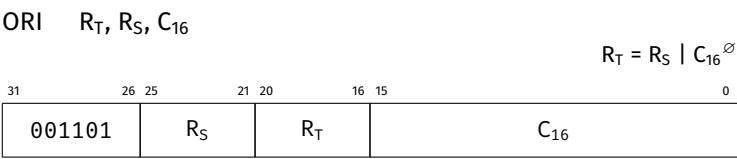
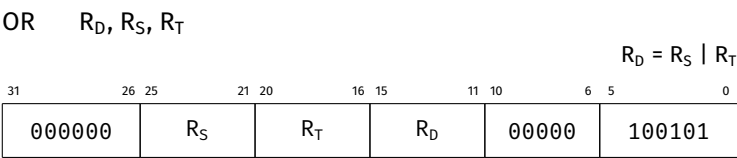
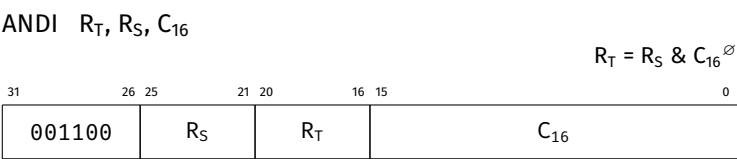
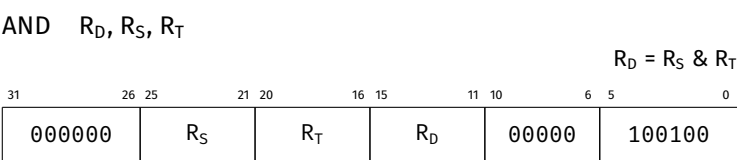
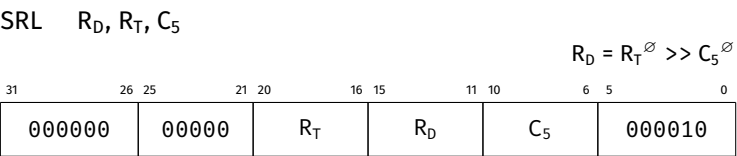
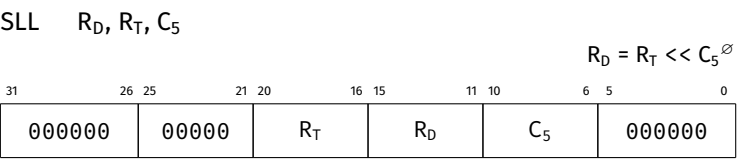
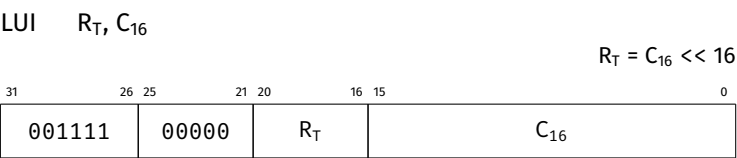
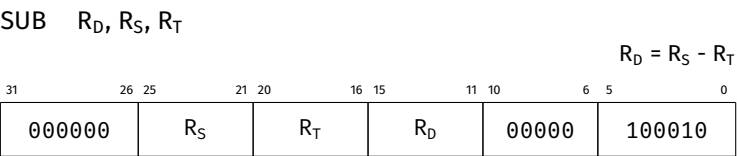
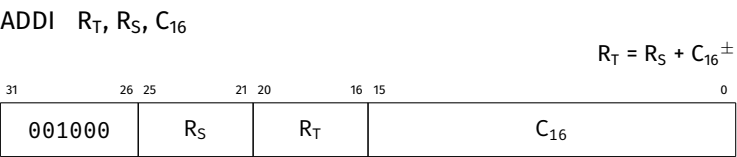
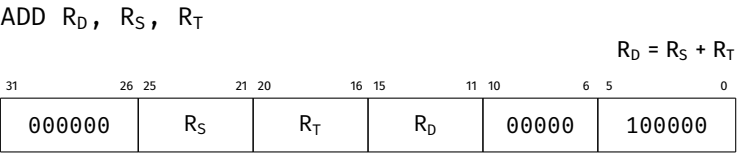
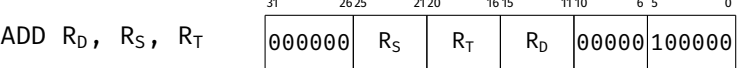
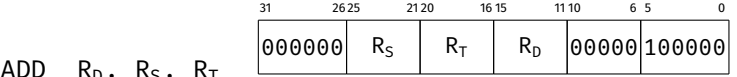


1 MIPS Instructions



SLT R_D, R_S, R_T

312625212016151110650

0000000

R_S

R_T

R_D

000000

101010

$R_D = (R_S < R_T)$

J C_{26}^{\emptyset}

312625212016151110650

0000000

R_S

R_T

R_D

000000

100000

SLTI R_T, R_S, C_{16}^{\pm}

312625212016151110650

0010100

R_S

R_T

C_{16}

$R_T = (R_S < C_{16}^{\pm})$

J C_{26}^{\emptyset}

312625212016151110650

0000100

R_S

R_T

C_{16}

BEQ R_S, R_T, C_{16}^{\pm}

312625212016151110650

0001000

R_S

R_T

C_{16}

IF $R_S = R_T, PC += C_{16}^{\pm} << 2$

J C_{26}^{\emptyset}

312625212016151110650

0000100

R_S

R_T

C_{16}

BNE R_S, R_T, C_{16}^{\pm}

312625212016151110650

0001010

R_S

R_T

C_{16}

IF $R_S \neq R_T, PC += C_{16}^{\pm} << 2$

J C_{26}^{\emptyset}

312625212016151110650

0001010

R_S

R_T

C_{16}

LW $R_T, C_{16}^{\pm}(R_S)$

312625212016151110650

0000100

R_S

R_T

C_{16}

$R_T = \text{Mem}[R_S + C_{16}^{\pm}]$

J C_{26}^{\emptyset}

312625212016151110650

0000100

R_S

R_T

C_{16}

SW $R_T, C_{16}^{\pm}(R_S)$

312625212016151110650

1000110

R_S

R_T

C_{16}

$\text{Mem}[R_S + C_{16}^{\pm}] = R_T$

J C_{26}^{\emptyset}

312625212016151110650

1000110

R_S

R_T

C_{16}

2 Digital Logics

NOT

$a \longrightarrow \neg a$

a	\bar{a}
0	1
1	0

AND

$a, b \longrightarrow a \cdot b$

a	b	$a \cdot b$
0	0	0
0	1	0
1	0	0
1	1	1

OR

$a, b \longrightarrow a + b$

a	b	$a + b$
0	0	0
0	1	1
1	0	1
1	1	1

XOR

$a, b \longrightarrow a \oplus b$

a	b	$a \oplus b$
0	0	0
0	1	1
1	0	1
1	1	0

NAND

$a, b \longrightarrow \overline{a \cdot b}$

a	b	$\overline{a \cdot b}$
0	0	1
0	1	1
1	0	1
1	1	0

NOR

$a, b \longrightarrow \overline{a + b}$

a	b	$\overline{a + b}$
0	0	1
0	1	0
1	0	0
1	1	0

XNOR

$a, b \longrightarrow \overline{a \oplus b}$

a	b	$\overline{a \oplus b}$
0	0	1
0	1	0
1	0	0
1	1	1