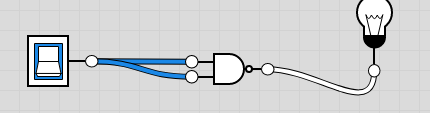
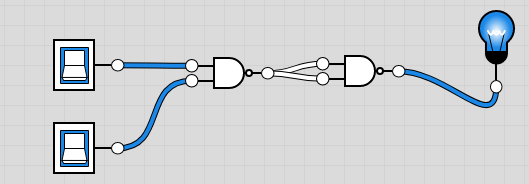
TASK-1

All logic gates using nand gate

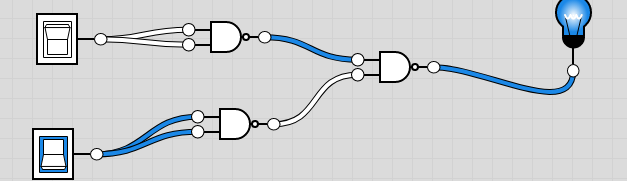
1-not\_gate\_using\_nand\_gate



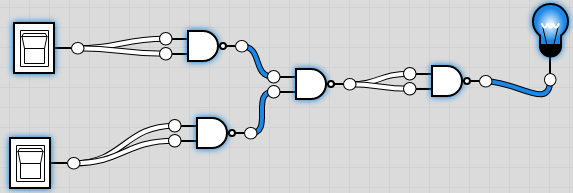
2\_And\_gate with\_nand\_gate



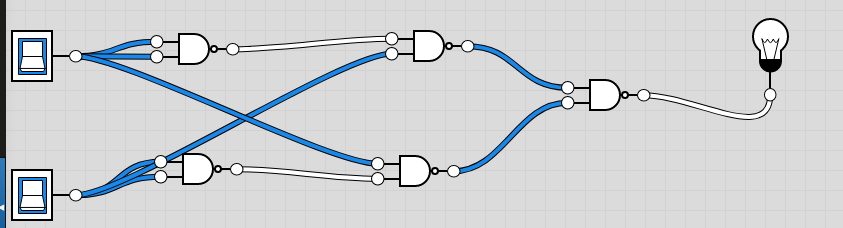
3\_or\_gate\_using\_nand



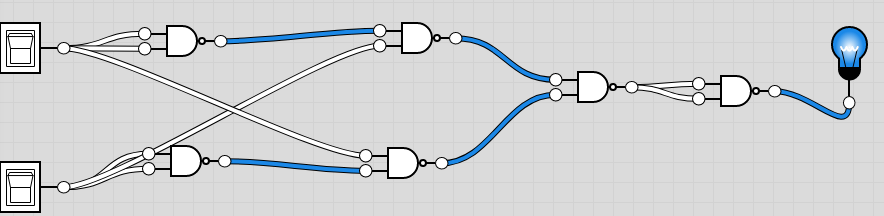
4\_nor\_gate\_using\_nand\_gate



5\_ex\_OR\_using\_nand\_gate



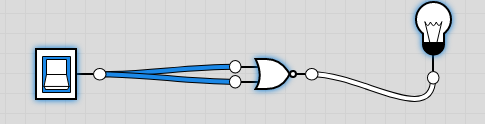
6\_ex\_Nor\_using\_nand Gate



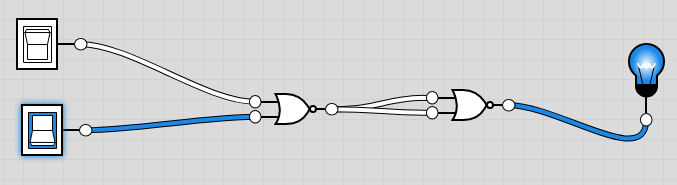
TASK-2

All logic gates using NOR gate

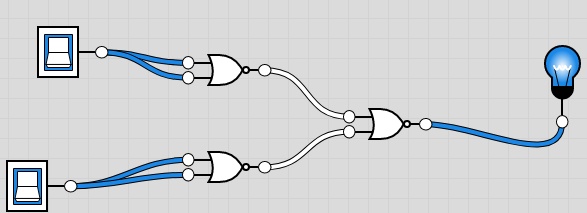
7\_not\_with\_nor\_



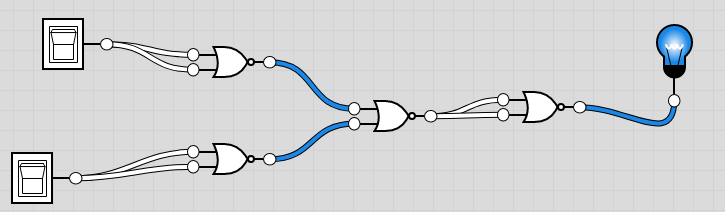
8\_or\_using\_nor



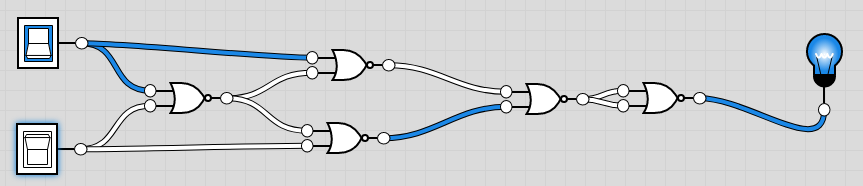
9\_and\_with\_nor



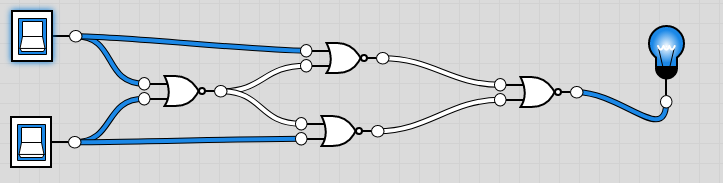
10\_nand\_with \_nor



11\_EX\_OR\_WITH\_NOR\_GATE

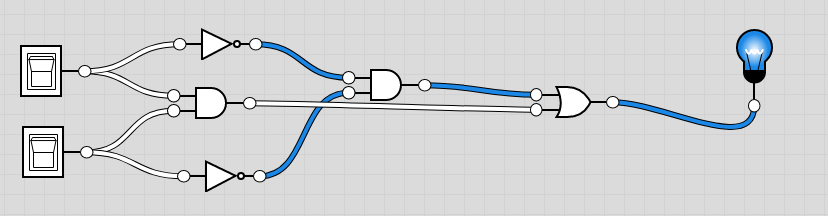


12\_X\_NOR\_with\_NOR\_GATE



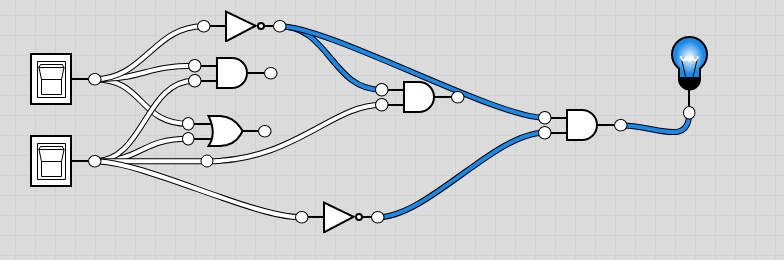
TASK-3





TASK-4





TASK-5

