המרכז
לחינוך סייבר
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Assembly for Reverse Engineering

Registers & Flags

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Registers

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- The CPU has some special hardware circuits
- Using them requires zero wait time
- Limited resource
- General purpose registers:
  - Used for all types of calculations
- Special purpose registers
  - IP
  - FLAGS

General Purpose Registers

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- 16 bit assembly:
  - AX – Accumulator register
  - BX – Base address register
  - CX – Count register
  - DX – Data register
  - SI – Source Index
  - DI – Destination Index
  - BP – Base Pointer
  - SP – Stack Pointer

8 Bit Registers

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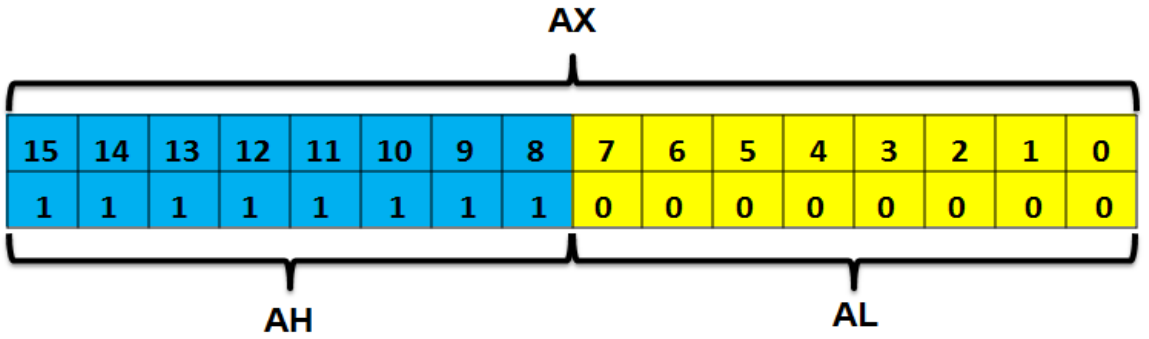
- For byte size operations, use byte registers:

16 bit	8 bit	8 bit
AX	AH	AL
BX	BH	BL
CX	CH	CL
DX	DH	DL

- mov ax, 0xFF00
  - mov ah, 0xFF
  - mov al, 0x00



32 Bit Registers

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- “E” stands for “Extended”

32 Bit	16 Bit	8 Bit
EAX	AX	AL / AH
EBX	BX	BL / BH
ECX	CX	CL / CH
EDX	DX	DL / DH
ESI	SI	
EDI	DI	
EBP	BP	
ESP	SP	

Other General Purpose Regs.

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- ESI / EDI – used for copying data from buffer to another
  - String operations
  - Will show up in some exercises
- EBP / ESP
  - Much of the course will be dedicated (stack, procedures)

EIP -Special Purpose Register

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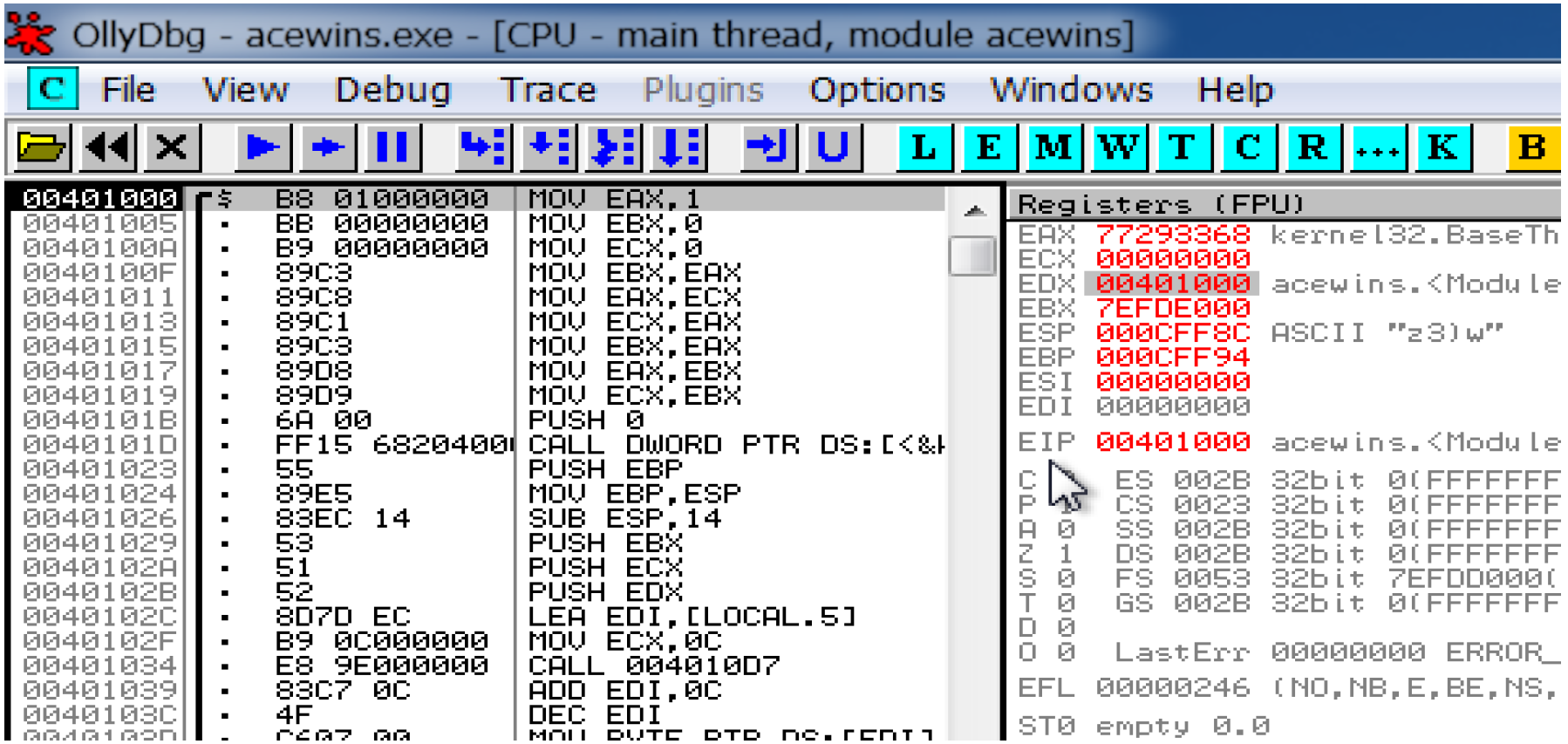
- Study hands on :-)



Summary

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- We have learned about:
  - CPU's general registers – important for reading code
  - EIP - important for tracing code
- By-products:
  - Practiced number representation
  - Practiced Ollydbg


QUESTIONS