Title

Analysis of RLC parallel corceit and verification of kCl in AC circuits.

Objectives

The objectives of this experiment are -

I) To determine phase relationship between I, and I'm a RLC Pavallel circuit.

2) Draw the complete vector diagram for a RLC parallel circuit.

3) Verification of KCL in AC circuits.

Theory

In ac circuits, admittance V is equal to 1/2. The unit of admittance in SI system is Siemens. Admittance is a measure of how well an ac circuit will allow current to flow in the circuit. The larger its value, the heavier the current flow for the same applied potential. The total admittance of a circuit can also be found by finding the sum of parallel admittances.



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The equation is like as follows $\frac{1}{1+1} = \frac{1}{2} + \frac{1}{2} + \frac{1}{2} + \frac{1}{2} + \dots + \frac{1}{2}$ Since $Y = \frac{1}{2}$ $\frac{1}{2} = \frac{1}{2} + \frac{1}{2} + \frac{1}{2} + \dots + \frac{1}{2} = \frac{1}{2}$

The reciprocal of reactionce (1/x) is called exceptance and is a measure of how susceptible an element is to the pursage of current though it.

For expacitor,

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AC = 100 s.m. AC = 1 s.s. R = 100 s.m. R = 100 s.m. $R_1 = 100 \text{ s.m.}$ $R_2 = 100 \text{ s.m.}$ $R_3 = 100 \text{ s.m.}$

Fig: RLC parallel circuit

In the circuit above the total Current I will divide into I_L and I_C in the parallel branches. Applying KCL $I = I_L + I_C$.

Apparatus

- 1. Oscilloscope
- 2. Function generator
- 3. Resistor: 100 56 (3)
- 4. Inductor: 6.3 mH
- 5. Capacitor : InF
- 6. Connecting wire
- 7. Bread board

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Results

f/km	E	IL	A.	To	Oc	IL+IL	T
1 1	10p-p	0.050	-28.8	6.91 x63	50.40	0.09891	
2	10p-p	0.020	-36°	0.020	36°	0.040	
4	10p-p	7,58×10-3	-28.8	0.05	28.8°	0.0579	

Calculations:

$$I_{i} = \frac{V_{i}}{X_{i}} = \frac{2}{39.58} - 4 - 28.8$$

$$T_c = \frac{V_c}{X_c} = \frac{1.1}{159.15}$$
 4 50.4 $X_c = \frac{1}{2nfc}$

For 2 kHz:

$$T_{L} = \frac{VL}{X_{LL}} = \frac{1.6 \ (3.36)}{2\pi f L} = \frac{1.6}{2\pi (1.03)(6.340^{-3})} \ 4^{-36}$$

Xu=2nfL

= 2n () no,

= 272 (1 MO2) (6.3 NO-3)

: 159.1500

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