

チップマウンタ搭載 画像処理アルゴリズムの検討

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1 問題設定

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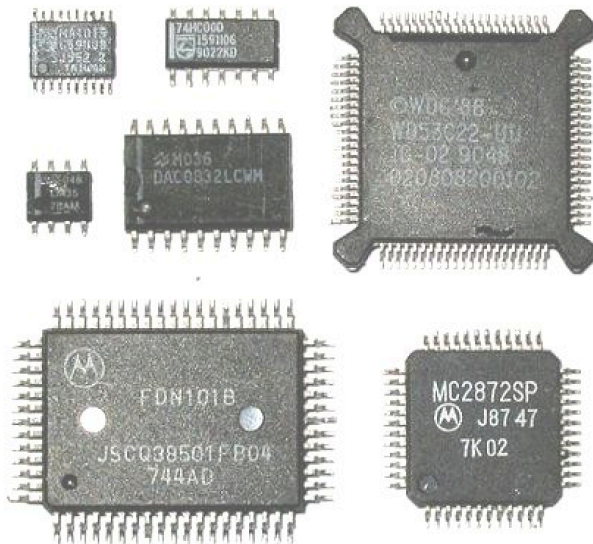


Figure 1: Sample photo of IC chips

References

- [1] 高井信勝, MATLAB 画像処理入門, 工学社 (2013)

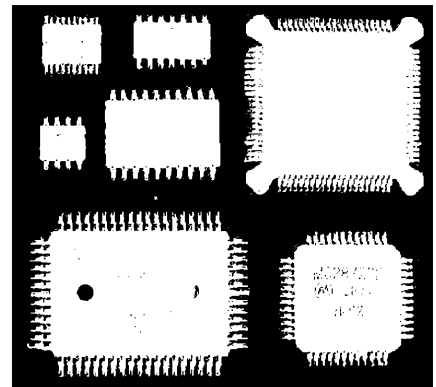


Figure 2: binarized(threshold 250)

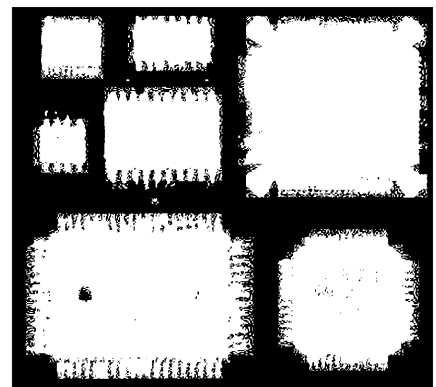


Figure 3: binarized 2(threshold 254)

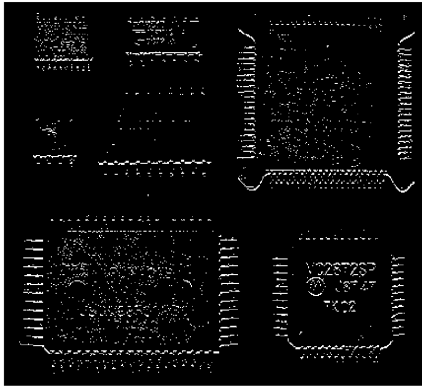


Figure 4: binarized laplacian

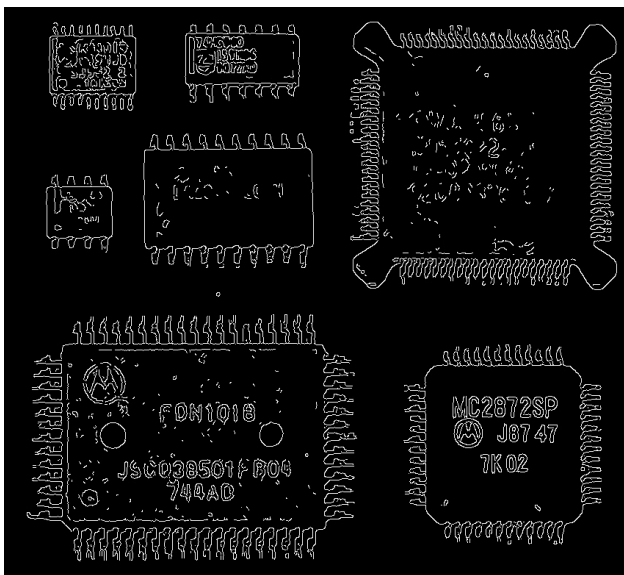


Figure 5: Canny edge

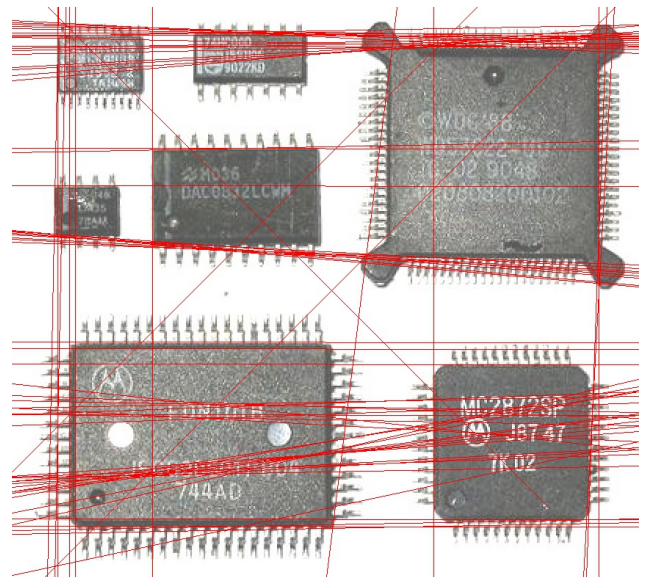


Figure 6: Canny edge and Hough lines