

1. Description

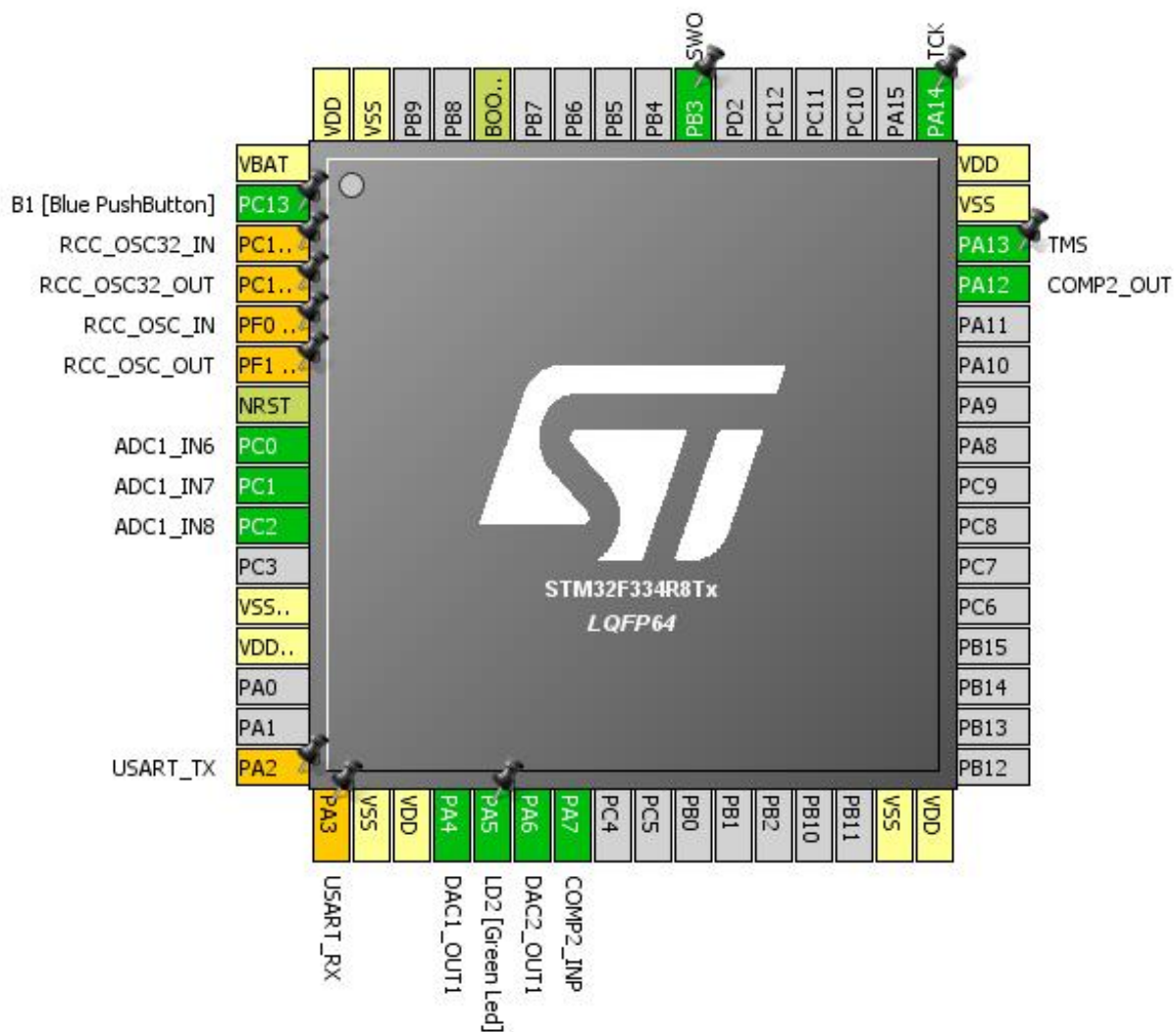
1.1. Project

Project Name	STM334_CompAdc
Board Name	NUCLEO-F334R8
Generated with:	STM32CubeMX 4.9.0
Date	01/15/2016

1.2. MCU

MCU Series	STM32F3
MCU Line	STM32F334
MCU name	STM32F334R8Tx
MCU Package	LQFP64
MCU Pin number	64

2. Pinout Configuration



3. Pins Configuration

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
2	PC13	I/O	GPIO_EXTI13	B1 [Blue PushButton]
3	PC14 / OSC32_IN *	I/O	RCC_OSC32_IN	
4	PC15 / OSC32_OUT *	I/O	RCC_OSC32_OUT	RCC_OSC32_OUT
5	PF0 / OSC_IN *	I/O	RCC_OSC_IN	
6	PF1 / OSC_OUT *	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	PC0	I/O	ADC1_IN6	
9	PC1	I/O	ADC1_IN7	
10	PC2	I/O	ADC1_IN8	
12	VSSA/VREF-	Power		
13	VDDA/VREF+	Power		
16	PA2 *	I/O	USART2_TX	USART_TX
17	PA3 *	I/O	USART2_RX	USART_RX
18	VSS	Power		
19	VDD	Power		
20	PA4	I/O	DAC1_OUT1	
21	PA5 **	I/O	GPIO_Output	LD2 [Green Led]
22	PA6	I/O	DAC2_OUT1	
23	PA7	I/O	COMP2_INP	
31	VSS	Power		
32	VDD	Power		
45	PA12	I/O	COMP2_OUT	
46	PA13	I/O	SYS_JTMS-SWDIO	TMS
47	VSS	Power		
48	VDD	Power		
49	PA14	I/O	SYS_JTCK-SWCLK	TCK
55	PB3	I/O	SYS_JTDO-TRACESWO	SWO
60	BOOT0	Boot		
63	VSS	Power		
64	VDD	Power		

** The pin is affected with an I/O function

* The pin is affected with a peripheral function but no peripheral mode is activated

4. IPs and Middleware Configuration

4.1. ADC1

IN6: IN6 Single-ended

IN7: IN7 Single-ended

IN8: IN8 Single-ended

ADCs_Common_Settings:

Mode Independent mode

ADC_Settings:

Clock Prescaler	ADC Asynchronous clock mode
Resolution	ADC 12-bit resolution
Data Alignment	Right alignment
Scan Conversion Mode	Disabled
Continuous Conversion Mode	Disabled
Discontinuous Conversion Mode	Disabled
DMA Continuous Requests	Disabled
End Of Conversion Selection	End of single conversion
Overrun behaviour	Overrun data overwritten
Low Power Auto Wait	Disabled

ADC_Regular_ConversionMode:

Enable Regular Conversions	Enable
Enable Regular Conversions	Enable
Rank	1
Channel	Channel 6 *
Sampling Time	1.5 Cycles
Offset Number	No offset
Offset	0

ADCgroup:

Number Of Conversion	1
External Trigger Conversion Edge	None
Enable Injected Conversions	Enable
Number Of Conversions	0
Number Of Conversion	1
External Trigger Conversion Edge	None

WatchDog1:

Enable Analog WatchDog1 Mode false

WatchDog2:

Enable Analog WatchDog2 Mode false

WatchDog3:

Enable Analog WatchDog3 Mode false

4.2. COMP2

mode: Input [+]

Input [-]: 1/2 Internal VRef

mode: External Output

Basic Parameters:

Interrupt Trigger Mode None

Blanking Source None

Output Parameters:

Output Polarity Not Inverted

Output Internal Selection None

4.3. DAC1

mode: OUT1 Configuration

DAC Out1 Settings:

Output Buffer Enable

Trigger None

4.4. DAC2

mode: OUT1 Configuration

DAC Out1 Settings:

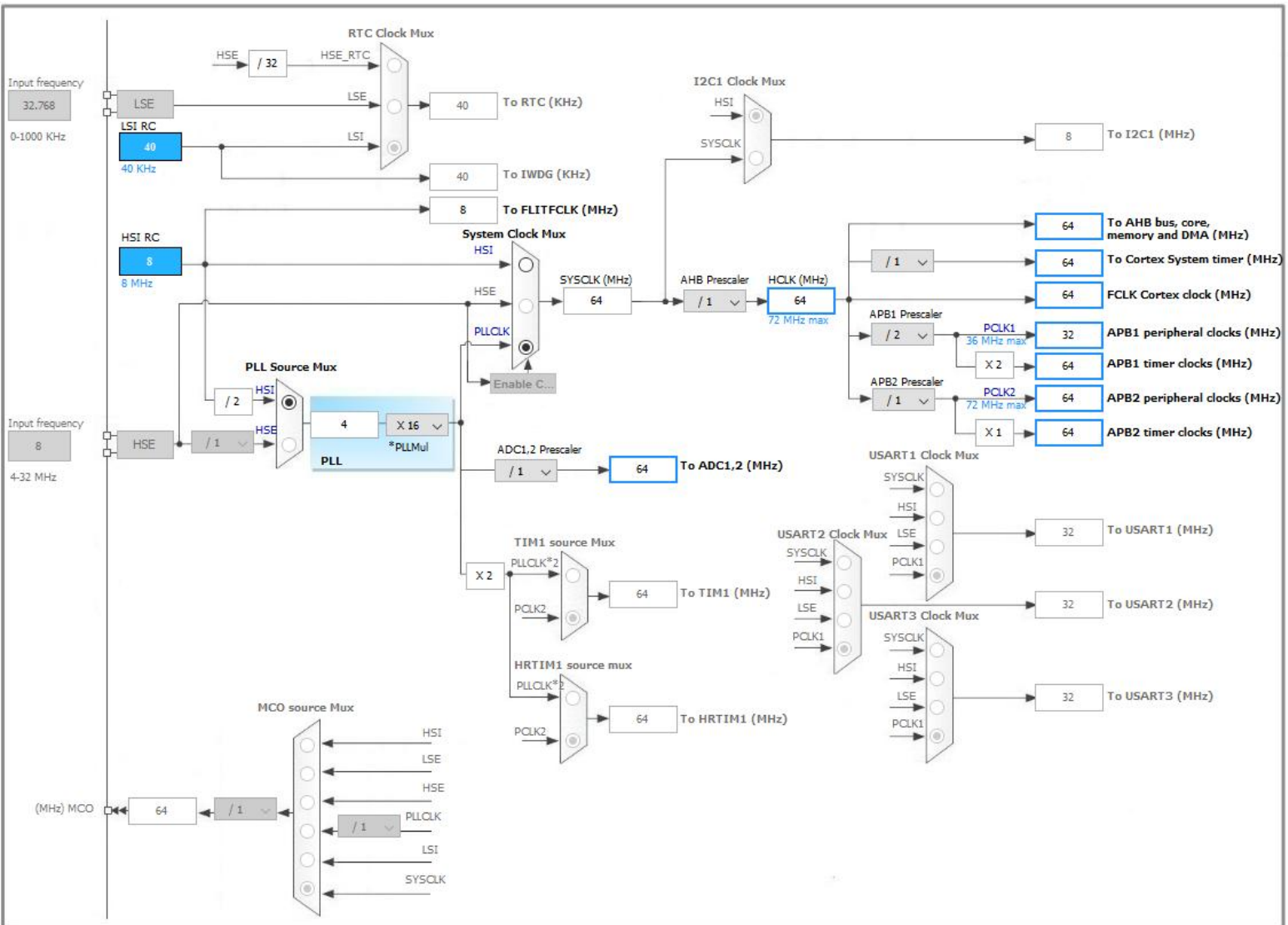
Trigger None

4.5. SYS

Debug: Trace Asynchronous Sw

*** User modified value**

2. Clock Tree Configuration



3. Power Plugin report

3.1. Microcontroller Selection

Series	STM32F3
Line	STM32F334
MCU	STM32F334R8Tx
Datasheet	025409_Rev3

3.2. Parameter Selection

Temperature	25
Vdd	3.6

4. Software Project

4.1. Project Settings

Name	Value
Project Name	STM334_CompAdc
Project Folder	D:\git\stackoverflow\STM32F332_CompAdc\STM334_CompAdc
Toolchain / IDE	SW4STM32
Firmware Package Name and Version	STM32Cube FW_F3 V1.2.0

4.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No

4.3. Toolchains Settings

Name	Value
Compiler Optimizations	Balanced Size/Speed