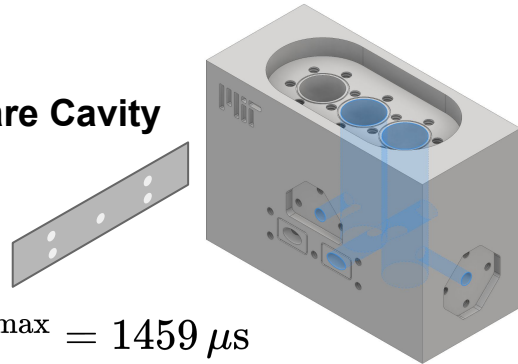


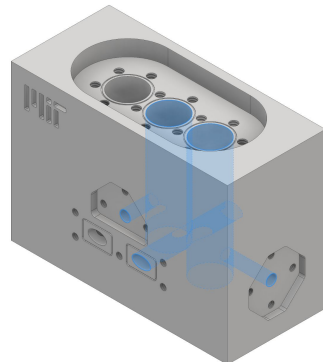
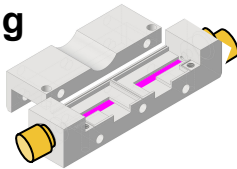
Bare Cavity

$$T_1^{\max} = 1459 \mu s$$



**Cavity + PCB
Housing**

$$T_1^{\max} = 1214 \mu s$$



**Cavity + Blank Si
Chips**

$$T_1^{\max} = 128 \mu s$$

