VLSI DESIGN TOOLS AND TECHNOLOGY (VDTT) PROGRAMME

May 17, 2017

Tentative List of Candidates Selected for Admission into
the VDTT M. Tech programme on a full time basis

The following applicants have been tentatively selected into the VDTT M. Tech programme on a full time basis. Admission offers will be issued after verification of details and approval from the Dean (Academics). The offer letters will contain details about other formalities to be completed, including payment of fees.

APPLICATION NO.	STUDENT NAME	SPONSORING ORGANIZATION
9244	SAURABH MATHUR	QUALCOMM
18988	SRISHTI GUPTA	QUALCOMM
7149	SHARIJ KUMAR	QUALCOMM
6733	KARAN GOYAL	QUALCOMM
6748	VAIBHAV GUPTA	QUALCOMM
6649	VIJAY SHARMA	TEXAS INSTRUMENTS
19960	VITTHAL SHARMA	TEXAS INSTRUMENTS
5490	TANYA GUPTA	PITNEY BOWES SOFTWARE INDIA
14900	ALOK GOPALRAO KESHATTIWAR	CONSTEMS AI-SYSTEMS PVT. LTD.
15280	MANISH KUMAR	CONSTEMS AI-SYSTEMS PVT. LTD.
6629	SHASHANK VARSHNEY	ULTRA-FAST THERMAL SIMULATION **(Research Project)**
6436	SURAJIT GHOSH	SUPPORT FOR SECURE EXECUTION IN DISTRIBUTED SYSTEMS **(Research Project)**

Waiting List

APPLICATION NO.	STUDENT NAME	SPONSORING ORGANIZATION
7087	SURAJ PANWAR	QUALCOMM
23236	AKASHDEEP SINGH	QUALCOMM

(Prof. Jayadeva)
Coordinator, VDTT Programme