VLSI DESIGN TOOLS AND TECHNOLOGY

INDIAN INSTITUTE OF TECHNOLOGY

Admission in M.Tech program for 1st Semester 2016-17

Interview dates: May 17th 2016: by Programme Executive Committee, May 18th 2016: by Sponsoring Industries

Short-listing Criteria in the M. Tech programme of VLSI Design Tools and Technology (JVL).

Call for interview (Full time with GATE):

GATE Discipline	Minimum GATE Score	Minimum degree performance
EC	800	
EE	800	Minimum degree performance is
CS	700	60% or 6.75 on
IN	800	10 Point Scale
IT	700	
Others	700	
IIT B.Tech.	-	CGPA>8

<u>Call for interview for Industrial Employees for</u> Full-Time/ Part-Time Program

- A.Candidates should be an employee of a sponsoring company
- B.He/She should have a minimum of 1 year experience
- C.He/she should satisfy the minimum criteria of the Institute.

Those who fulfil the criteria and didn't receive any call letter, please contact at vdttoffice@gmail.com

Accommodation Details

Category	Hostel
MEN applicants to EE Dept	Zanskar Hostel
	DD in favour of "Deans Discretionary Fund
	Zanskar Hostel" @Rs.230 per day
	Hostel Tel: 011-2659.7068
	Caretaker Tel No: 011-26597055 (off) /
	26596789 (res)
	Mess Supervisor: 011-26597010
WOMEN applicants (any Dept / Centre)	Kailash Hostel
	DD in favour of "Deans Discretionary Fund
	KAILASH Hostel" @Rs.230 per day
	Hostel Tel: 011-2659.6826 / 6976
	Caretaker Tel No: 011-26596839 (off) /
	26597061 (res)
	Mess Supervisor: 011-26597039

The candidates can stay on sharing basis in IIT Delhi Hostels by paying Rs.230/- for boarding (all meals) and lodging (stay). Bedding is not provided and candidates will have to make their own arrangements for bedding (typically, the Caretaker would be able to tell you where you may hire bedding; you are advised however to bring basic bedding with you).

For any queries, please feel free to contact:

Bharath Nandakumar Jahnavi Kulkarni

MTech, VDTT MTech, VDTT

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