VLSI DESIGN TOOLS AND TECHNOLOGY (VDTT) PROGRAMME

Dated: 17.5.16

The following candidates tentatively short-listed for interview with the sponsors on 18th May 2016. All the candidates are required to report at Room No. IV-202 at 8:30 AM tomorrow.

Application No.	Name
1375	JAI KUMAR SHARMA
3895	ISAAR AHMAD
3999	SACHINDERNATH SINGH
5204	AKHIL VALSARAJ
5260	VISESH G
5406	NAVKAMAL RAKRA
5462	JITIN JACOB
5655	SHARATH B K
6783	MANINDRA KUMAR
7264	AKASH RAVINDRA GOWDA
7560	NITI
9796	AKHILESH CHANDRA SATI
10547	SHREYA RAIZADA
10820	PRANAY SETHI
11077	VIVEK CHITTORA
12134	ANCHAL KHURANA
12139	SUNIL KUMAR
12504	SANU KUMAR
12779	SUHAS V DIXITH
13998	TANJYOT ARORA
14361	SUMIT SHALIGRAM DIWARE
15461	SHIKHA GOEL
15866	SAIKIRAN REDDY DWARAMPUDI
16016	VIKAS CHHILLAR
16196	GARIMA
16672	BHOJRAJ MAHULIYA
16722	KARTHIK M B
19894	DEEPESH KUMAR UPPAL
20436	YOJANA PRAKASH BIRJE
21772	PRAKHAR BAPAT

The list is arranged in increasing order of the application number of short listed candidates and does not reflect the merit list.

(Prof. Anshul Kumar)
Coordinator, VDTT Programme