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Ans to the question no-01

The LEA instruction is a Load Effective Address, which is an indirect instruction which means that TABLE-ADDRESS points to a memory location at which the address to load found

offset is an assembler directive in x86 assembly language. It actually means address.

Ans to the question - no. 02

Data segment is a portion of an object file on the corresponding virtual address space of a program that contains initialized static variables, that is global variables and static local variables.

- Data is a portion of an object file on the corresponding section of the program's virtual address space that contains executable instructions.

Ans to the question - 03

Assume DS: DATA CS: CODE is used for Different Registers present for different Purpose. So we have to assume DATA is the name given to Data segment register and CODE is the name given to Code segment register..

Ans to the question - 04

The registers that are used for memory accessing with is MBR.