Driver Architecture

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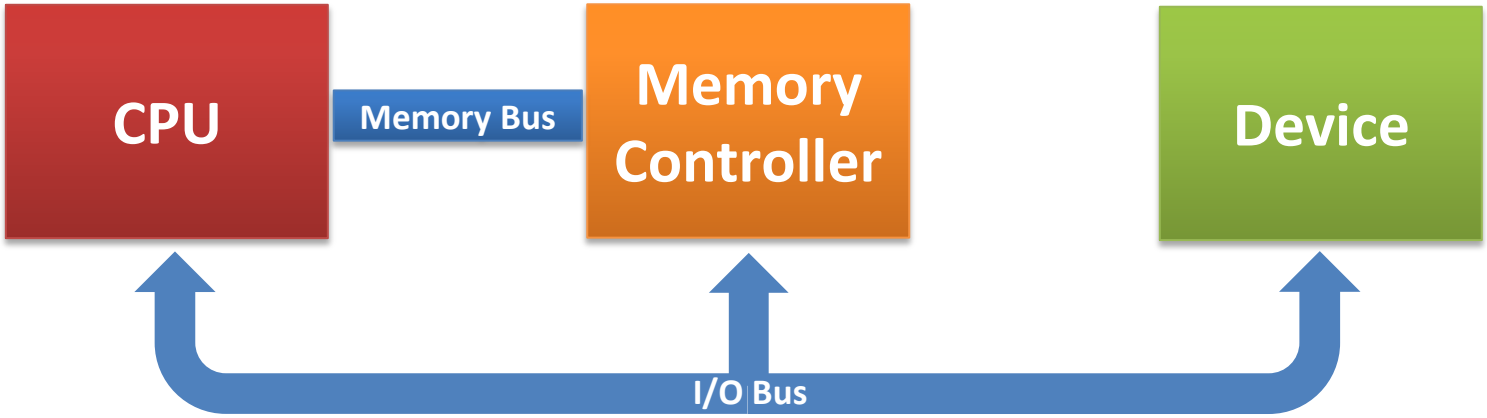
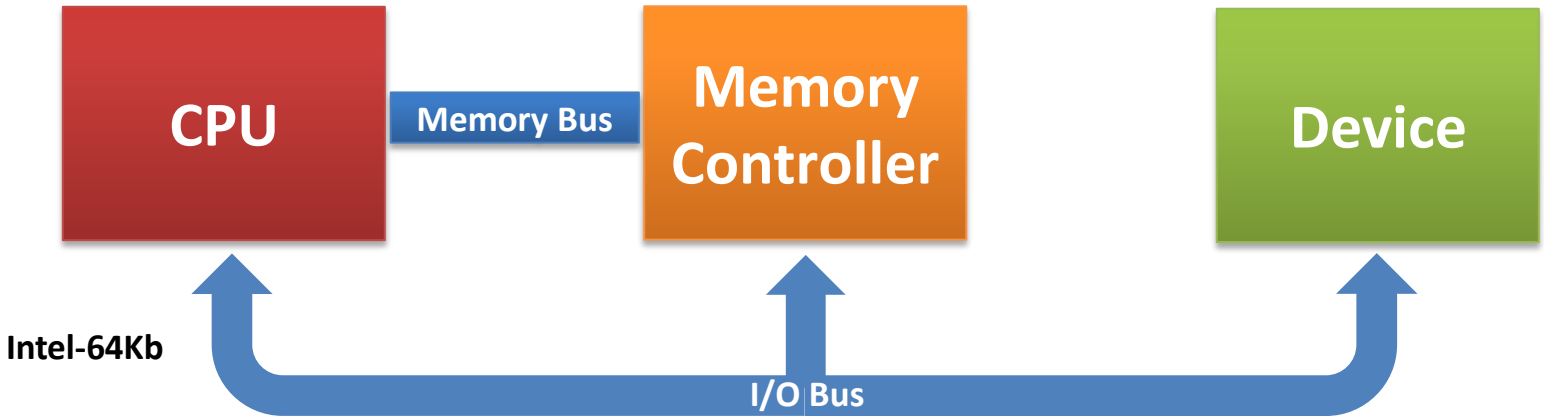
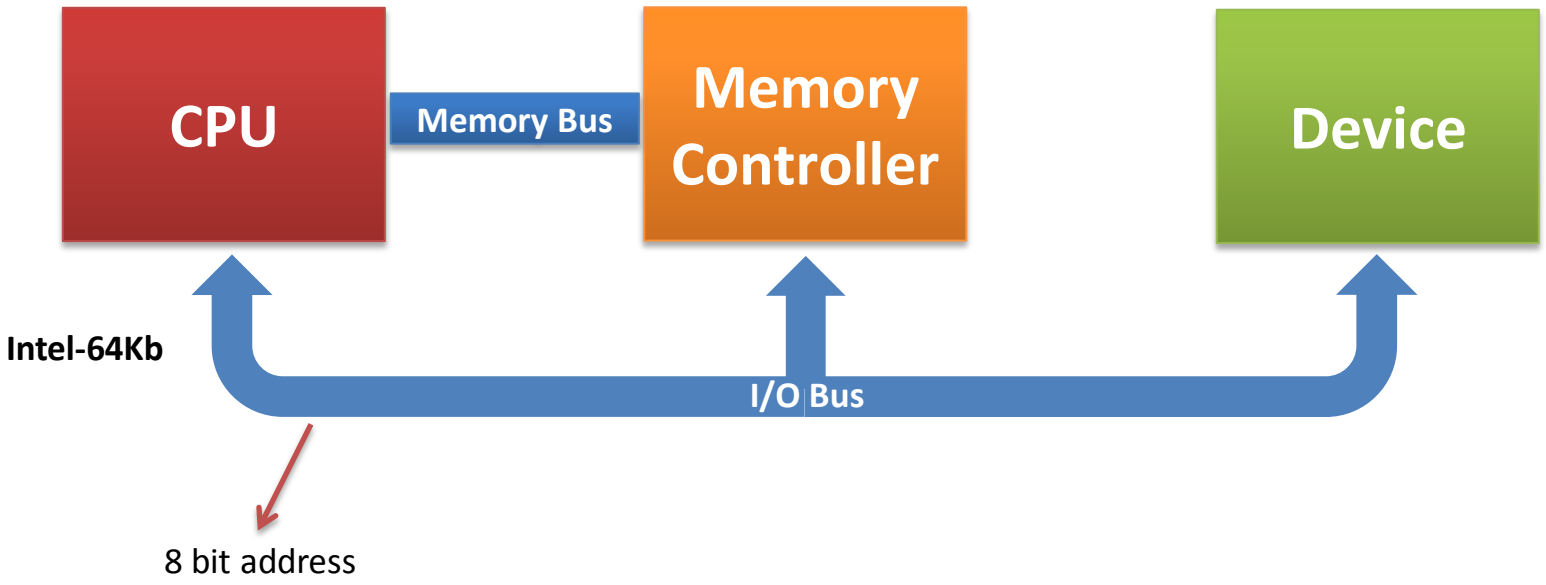
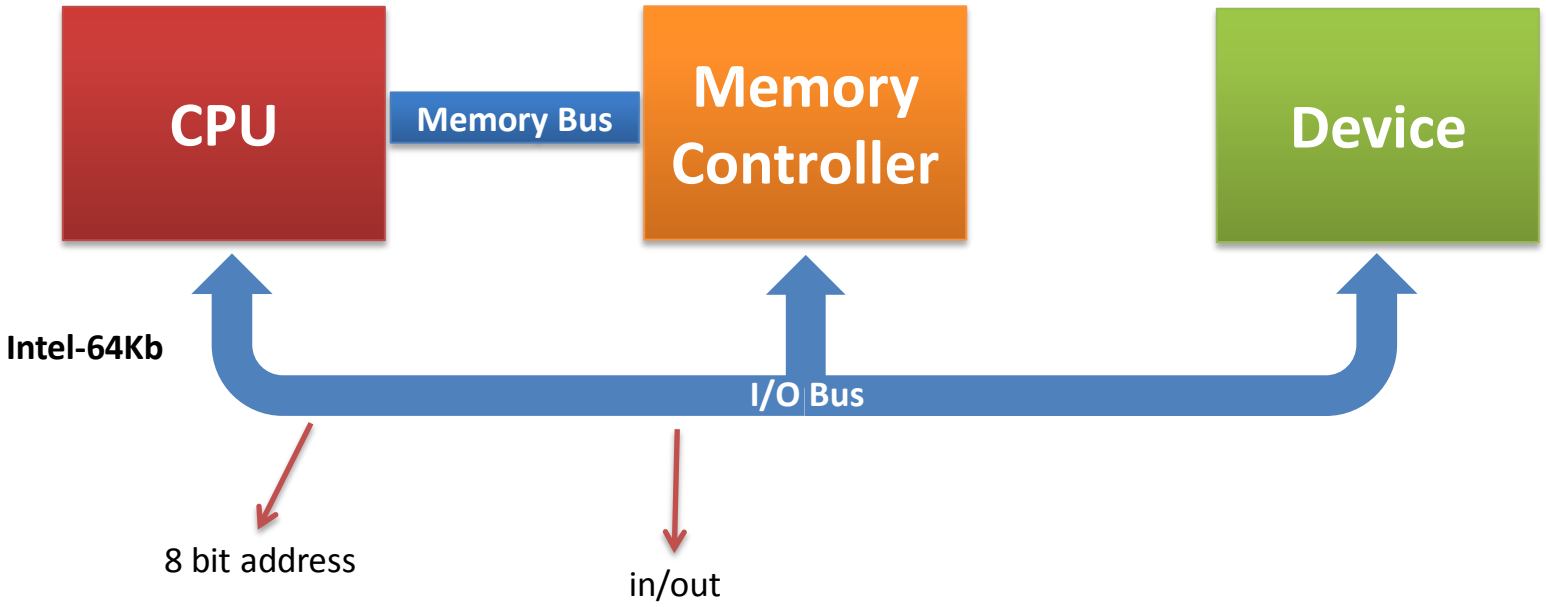
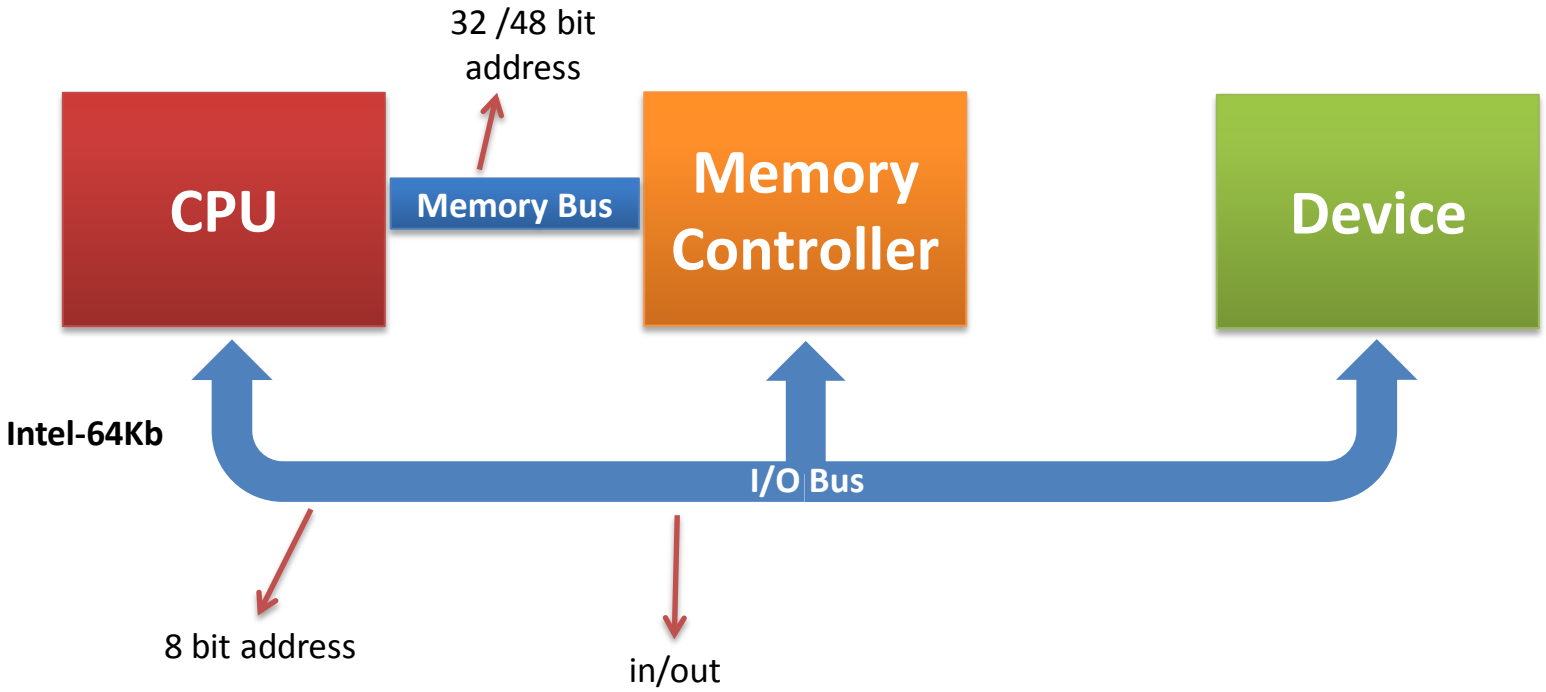
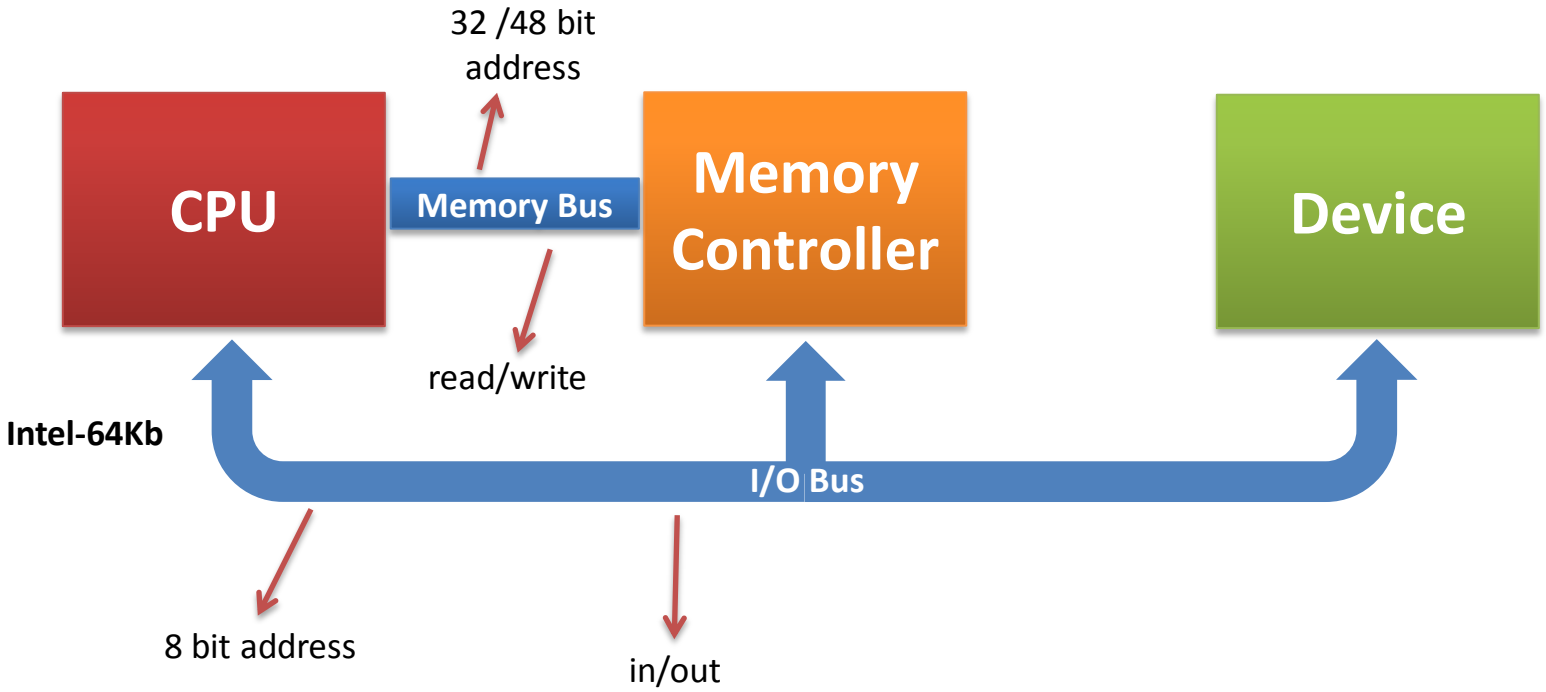

CPU
Memory Bus
Memory Controller
Device
I/O Bus

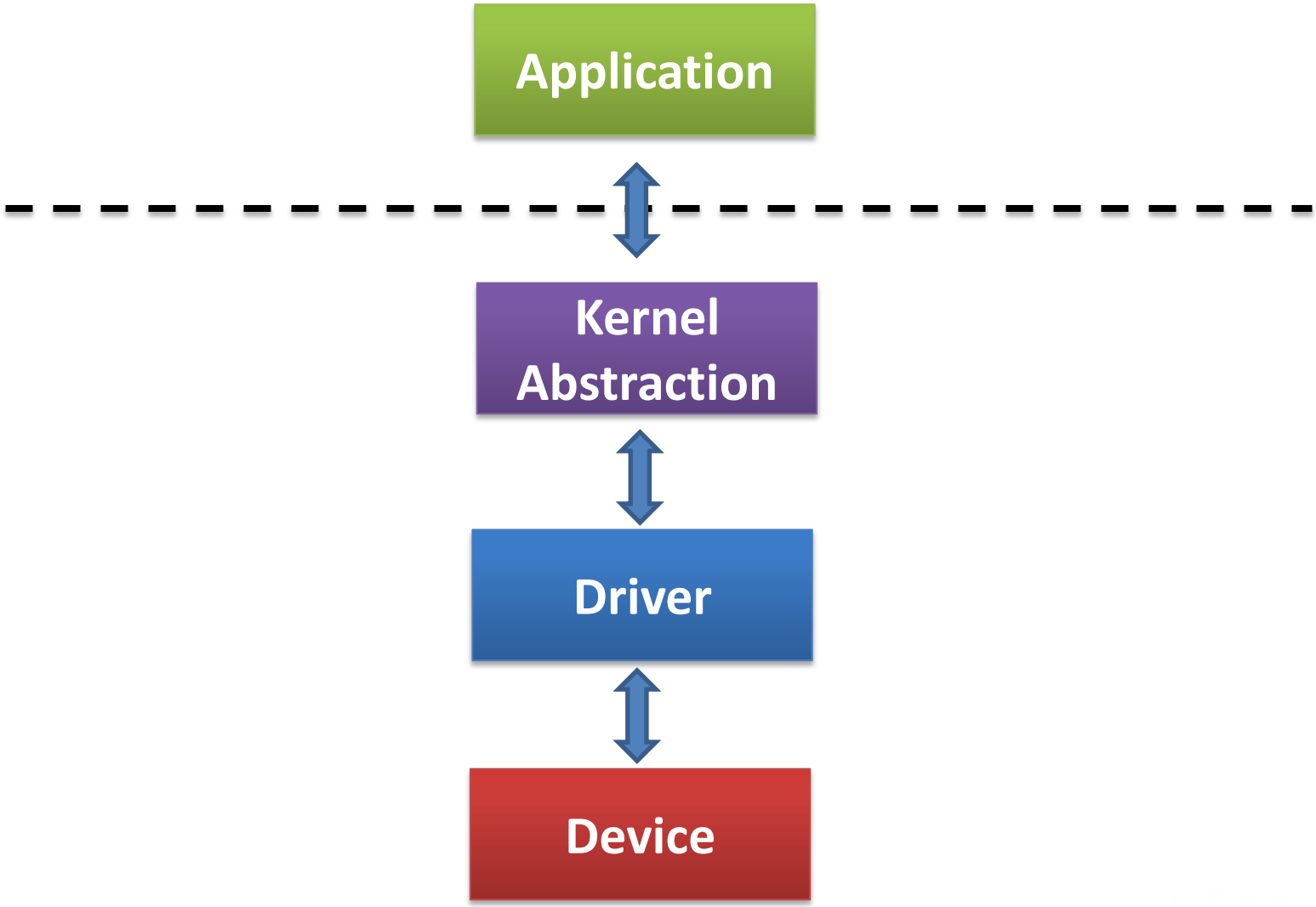

CPU
Memory Bus
Memory Controller
Device
Intel-64Kb
I/O Bus

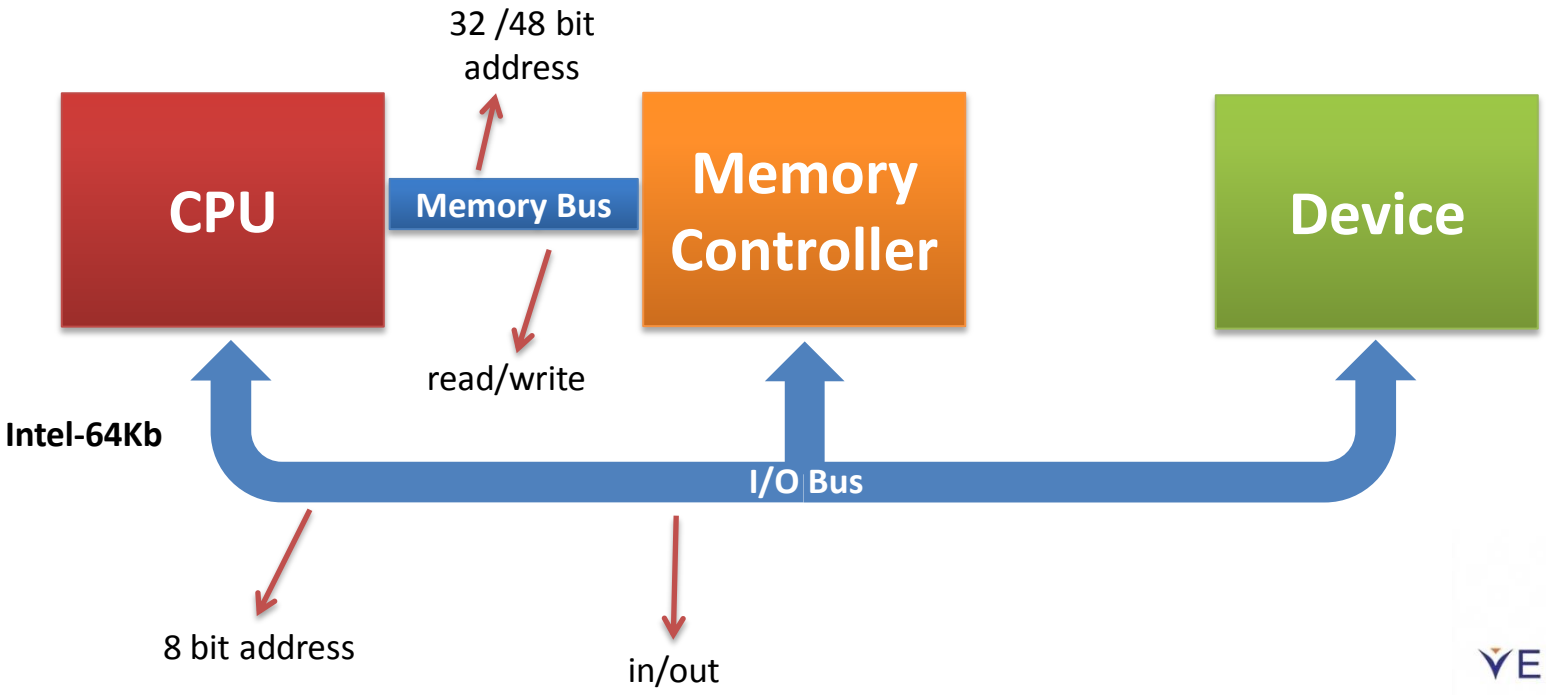

CPU
Memory Bus
Memory Controller
Device
Intel-64Kb
I/O Bus
8 bit address

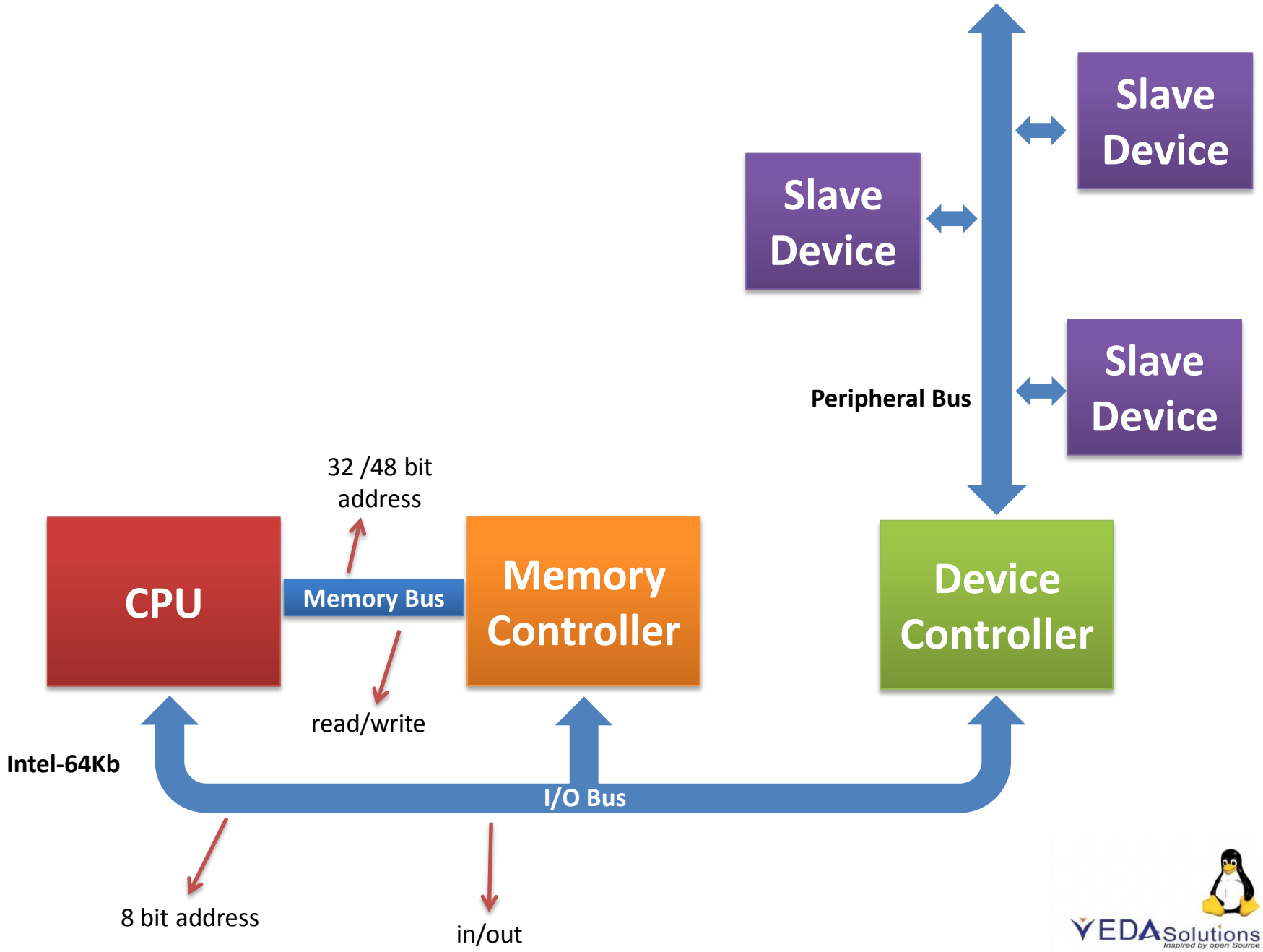

CPU
Memory Bus
Memory Controller
Device
Intel-64Kb
I/O Bus
8 bit address
in/out

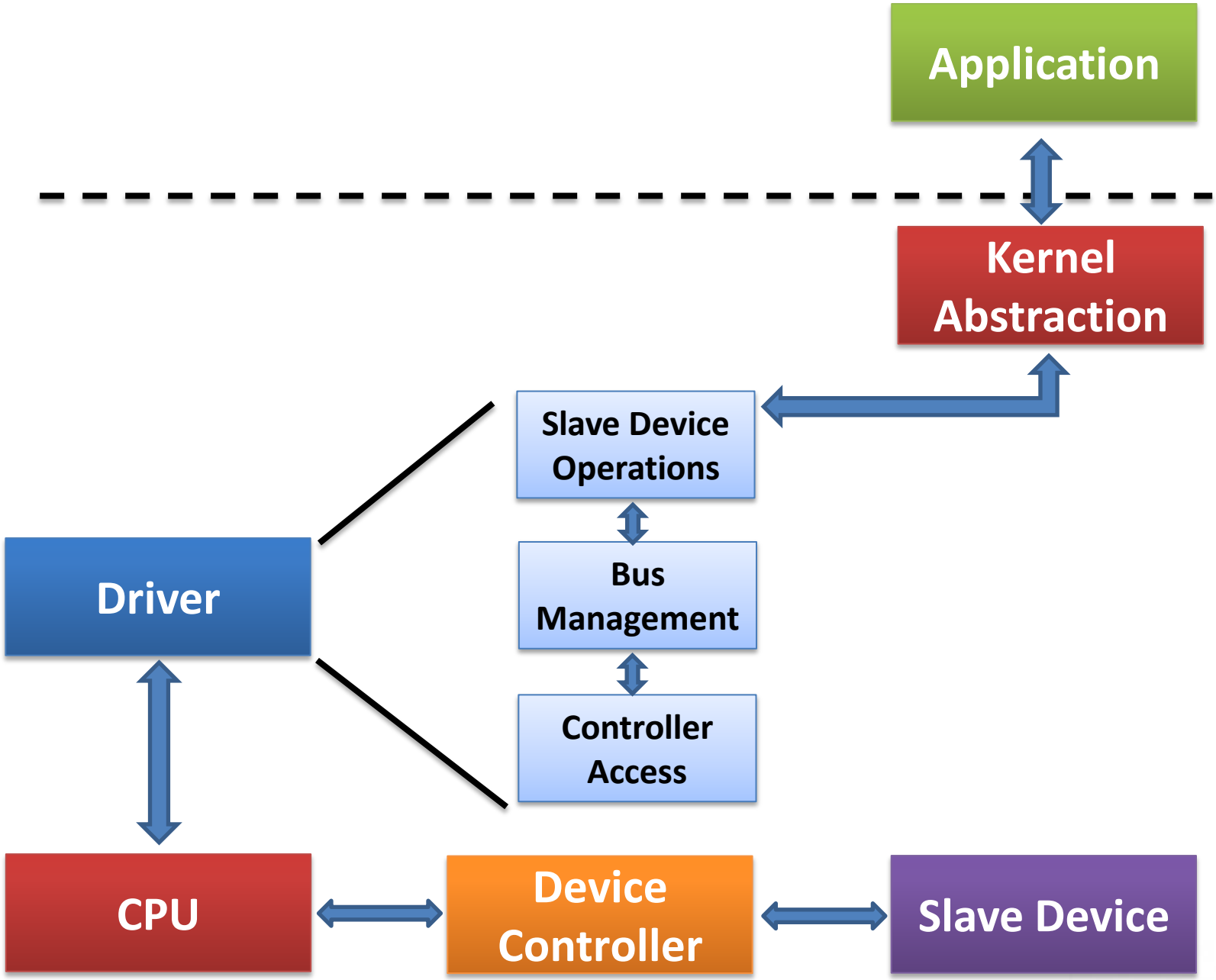

32 /48 bit
address
CPU
Memory Bus
Memory
Controller
Device
Intel-64Kb
I/O Bus
8 bit address
in/out

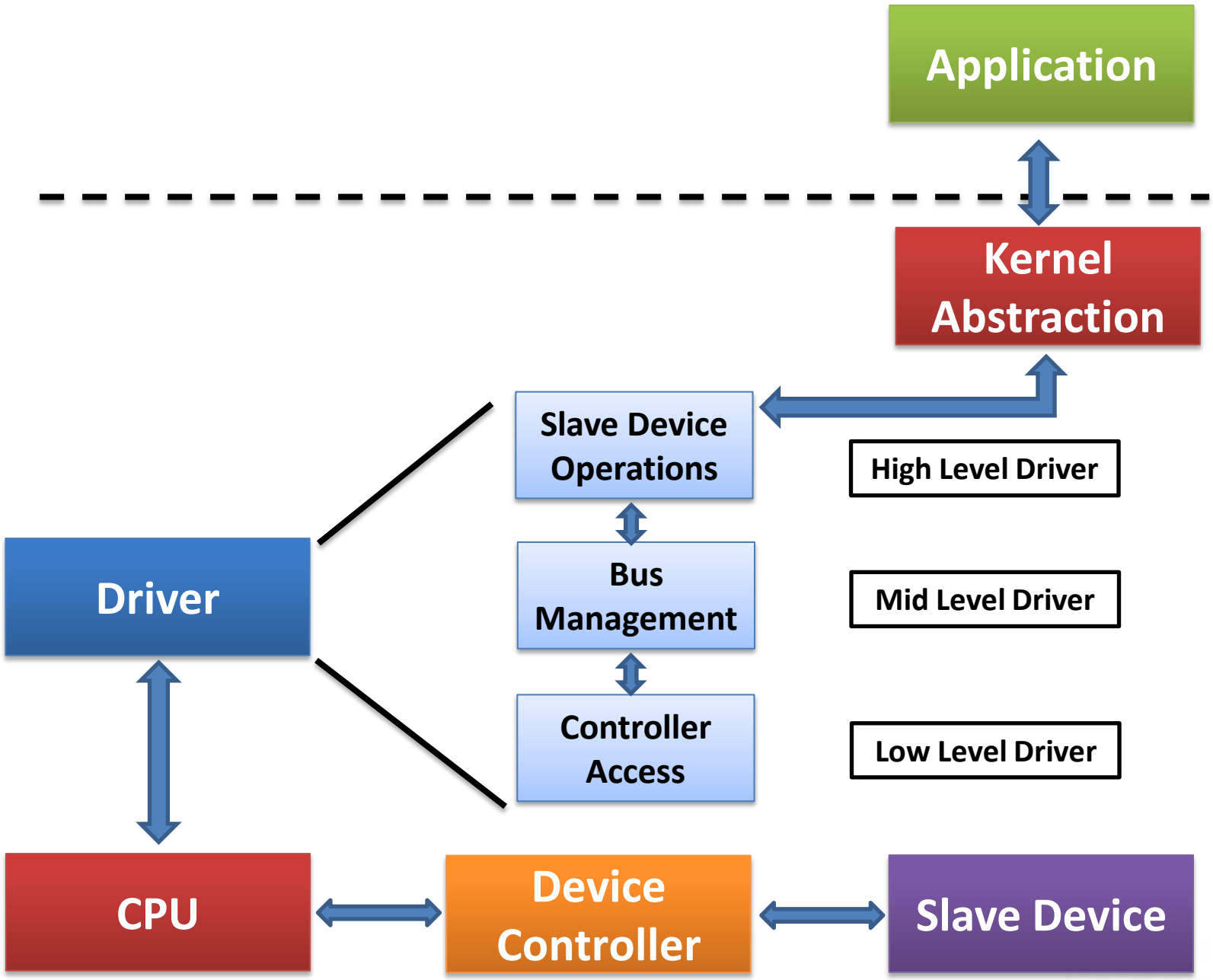

32 /48 bit
address
CPU
Memory Bus
Memory
Controller
Device
read/write
Intel-64Kb
I/O Bus
8 bit address
in/out


Application
Kernel Abstraction
Driver
Device

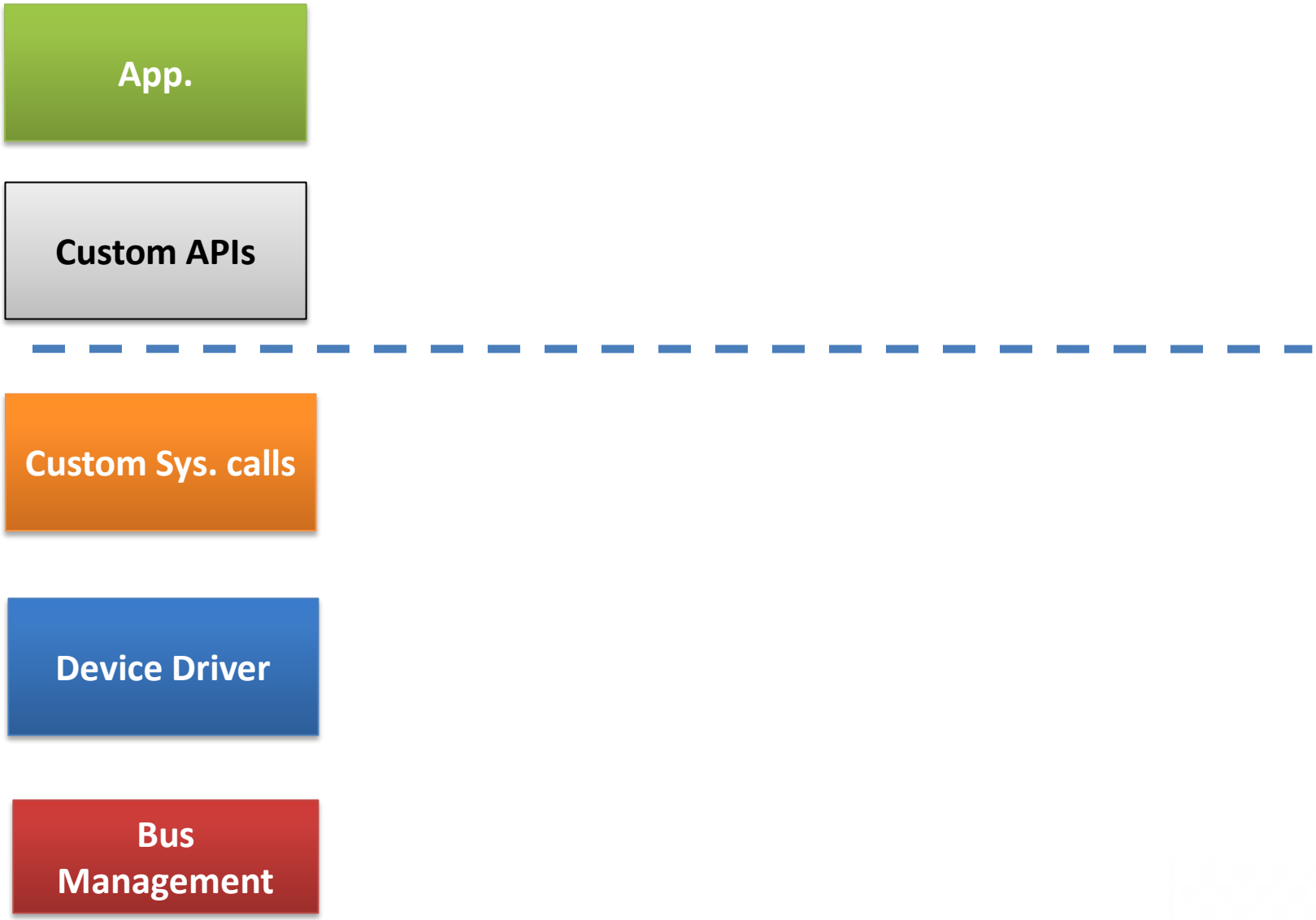

32 /48 bit
address
CPU
Memory Bus
Memory
Controller
Device
read/write
Intel-64Kb
I/O Bus
8 bit address
in/out


Slave Device
Slave Device
Slave Device
Peripheral Bus
32 /48 bit address
CPU
Memory Bus
Memory Controller
Device Controller
read/write
Intel-64Kb
I/O Bus
8 bit address
in/out


Application
Kernel Abstraction
Slave Device Operations
Bus Management
Controller Access
Driver
CPU
Device Controller
Slave Device


Application
Kernel Abstraction
Slave Device Operations
High Level Driver
Driver
Bus Management
Mid Level Driver
Controller Access
Low Level Driver
CPU
Device Controller
Slave Device

Kernel Abstraction



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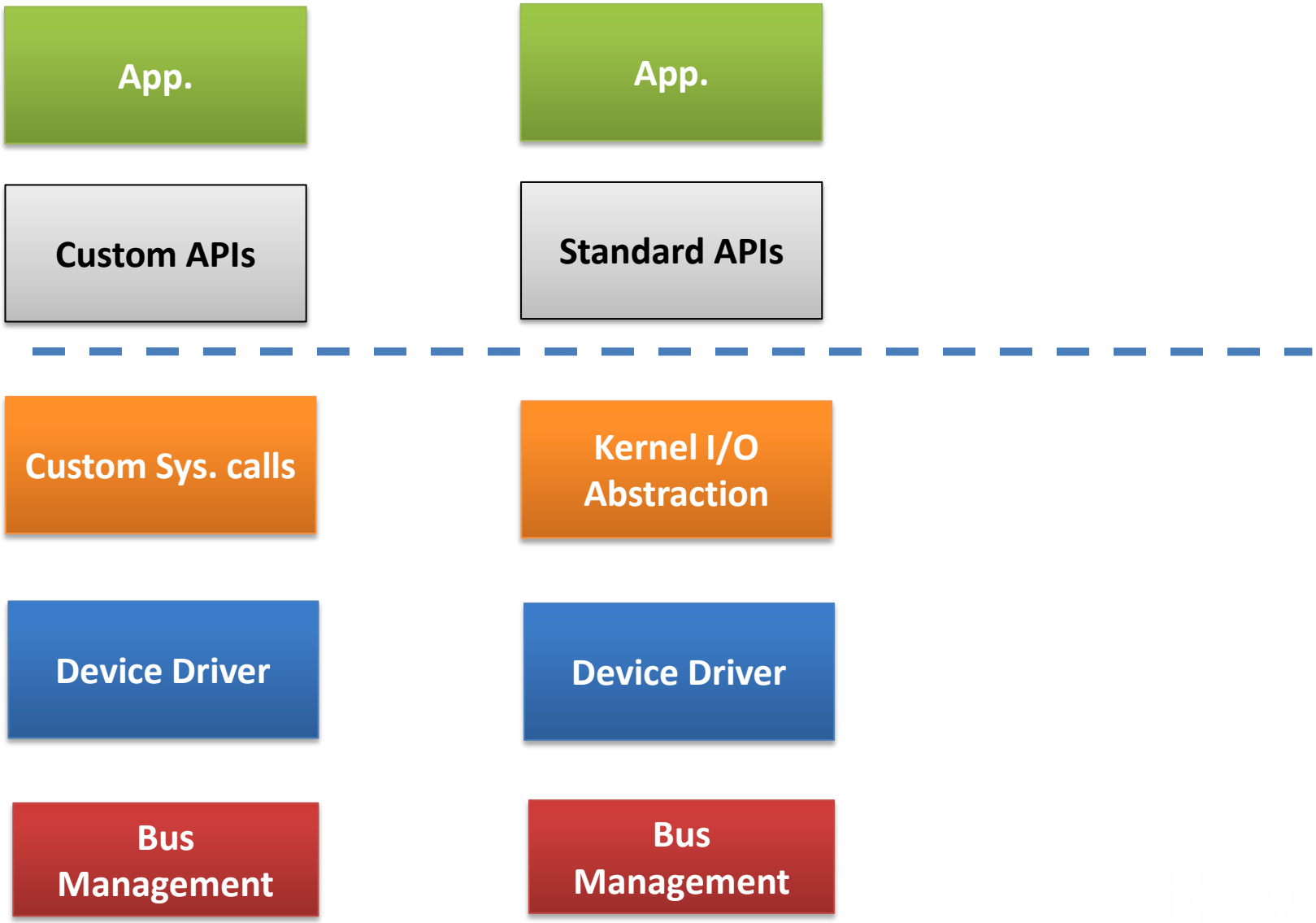
Kernel Abstraction



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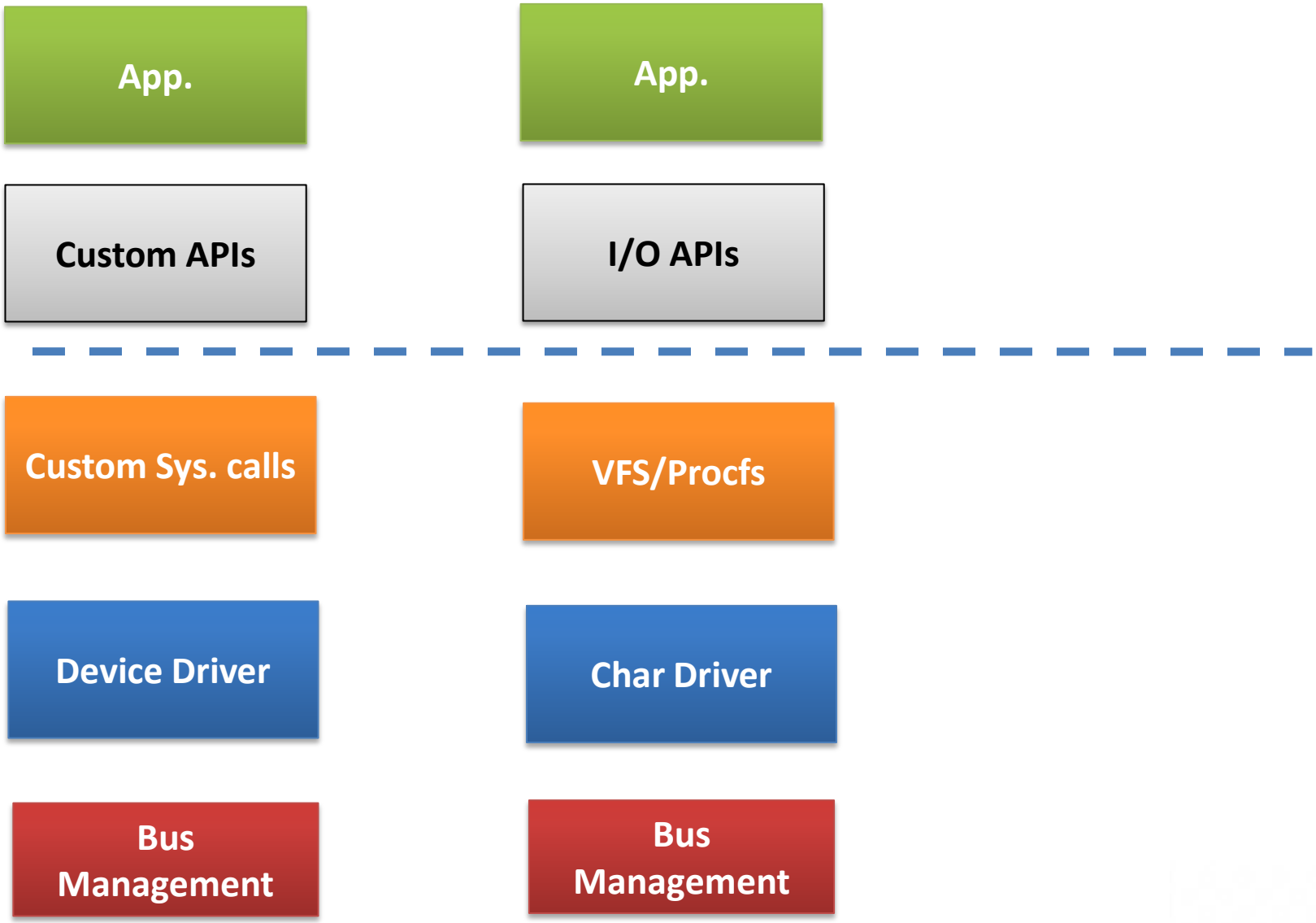
Kernel Abstraction



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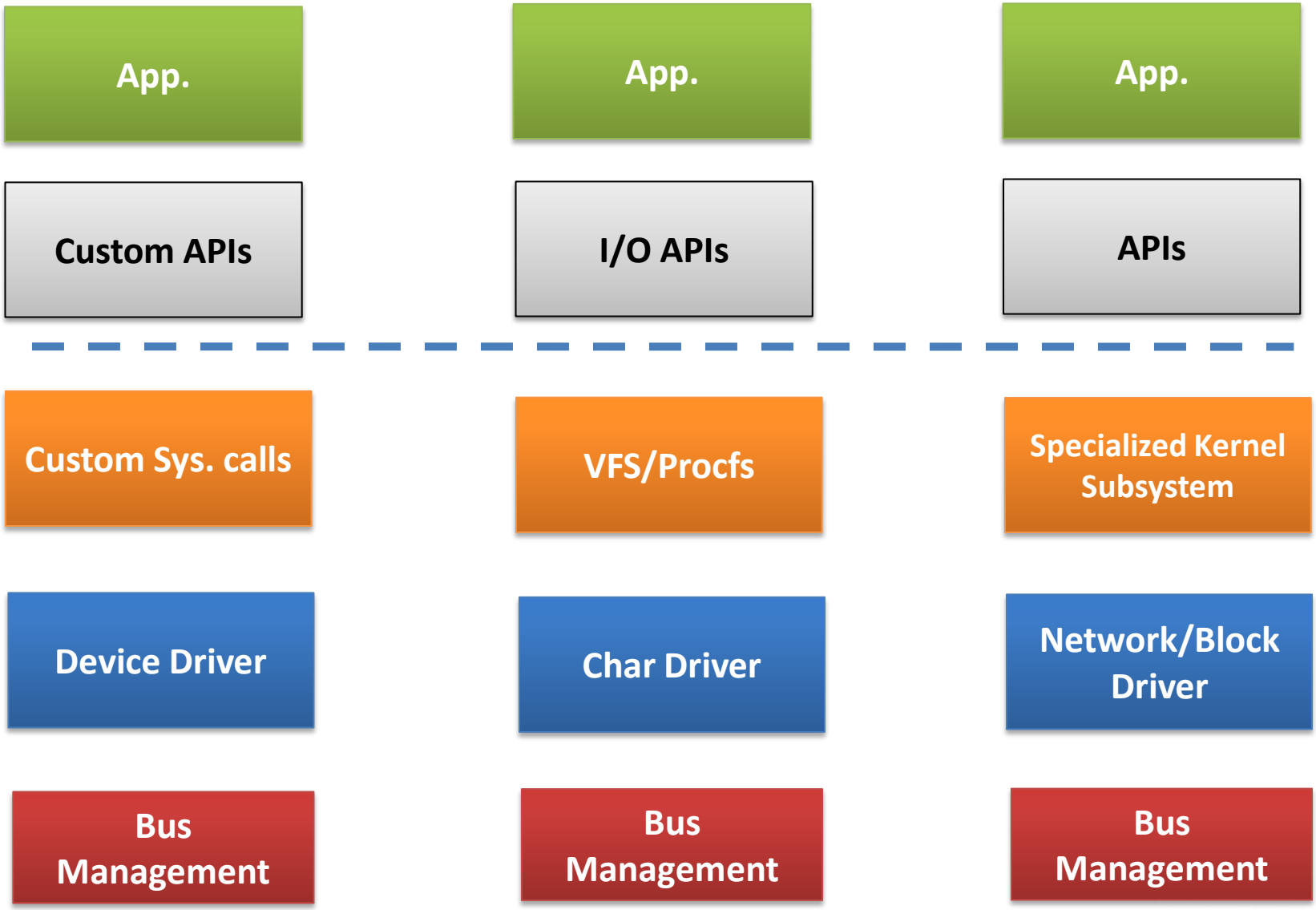
Kernel Abstraction



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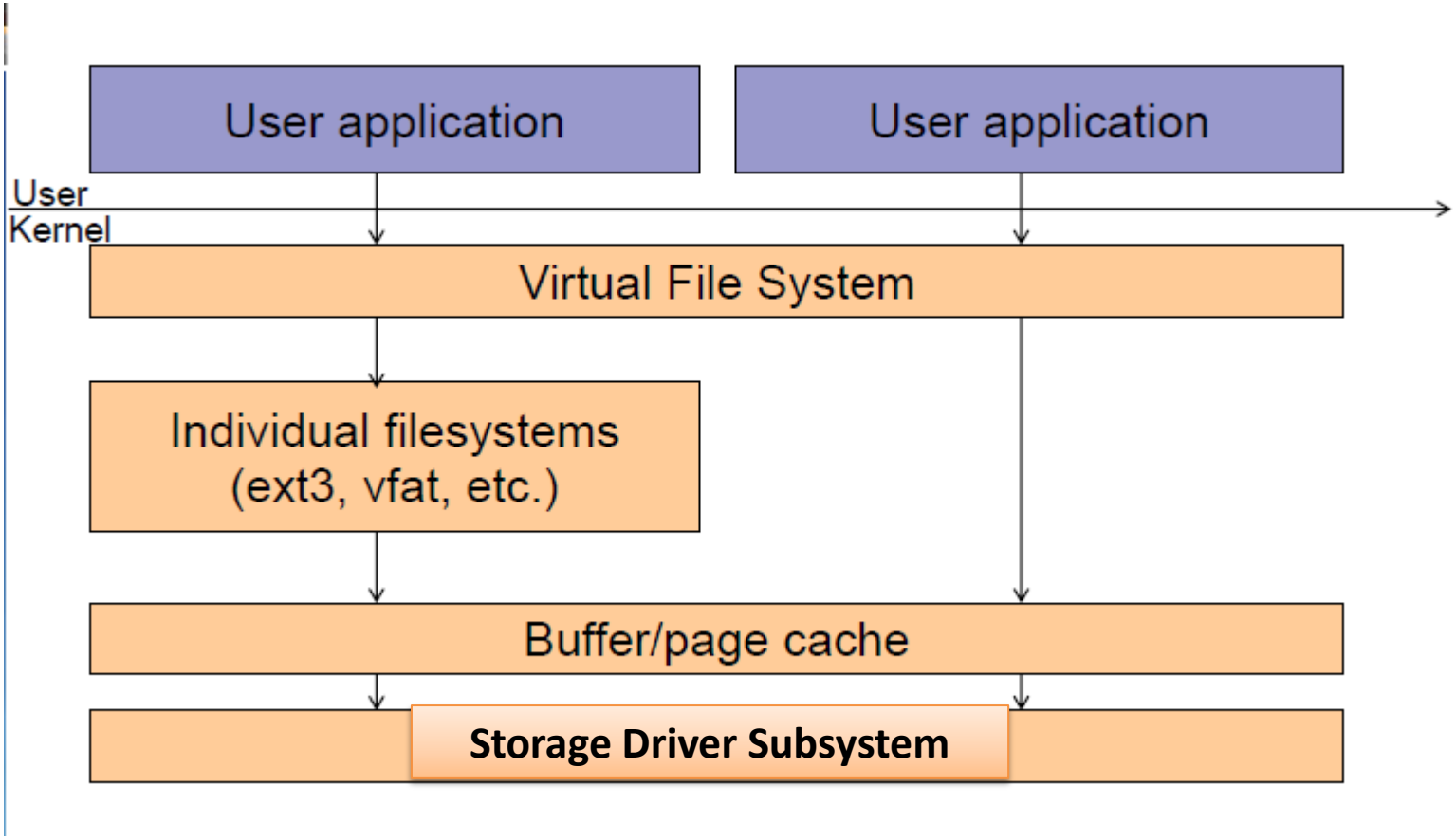
Storage Drivers



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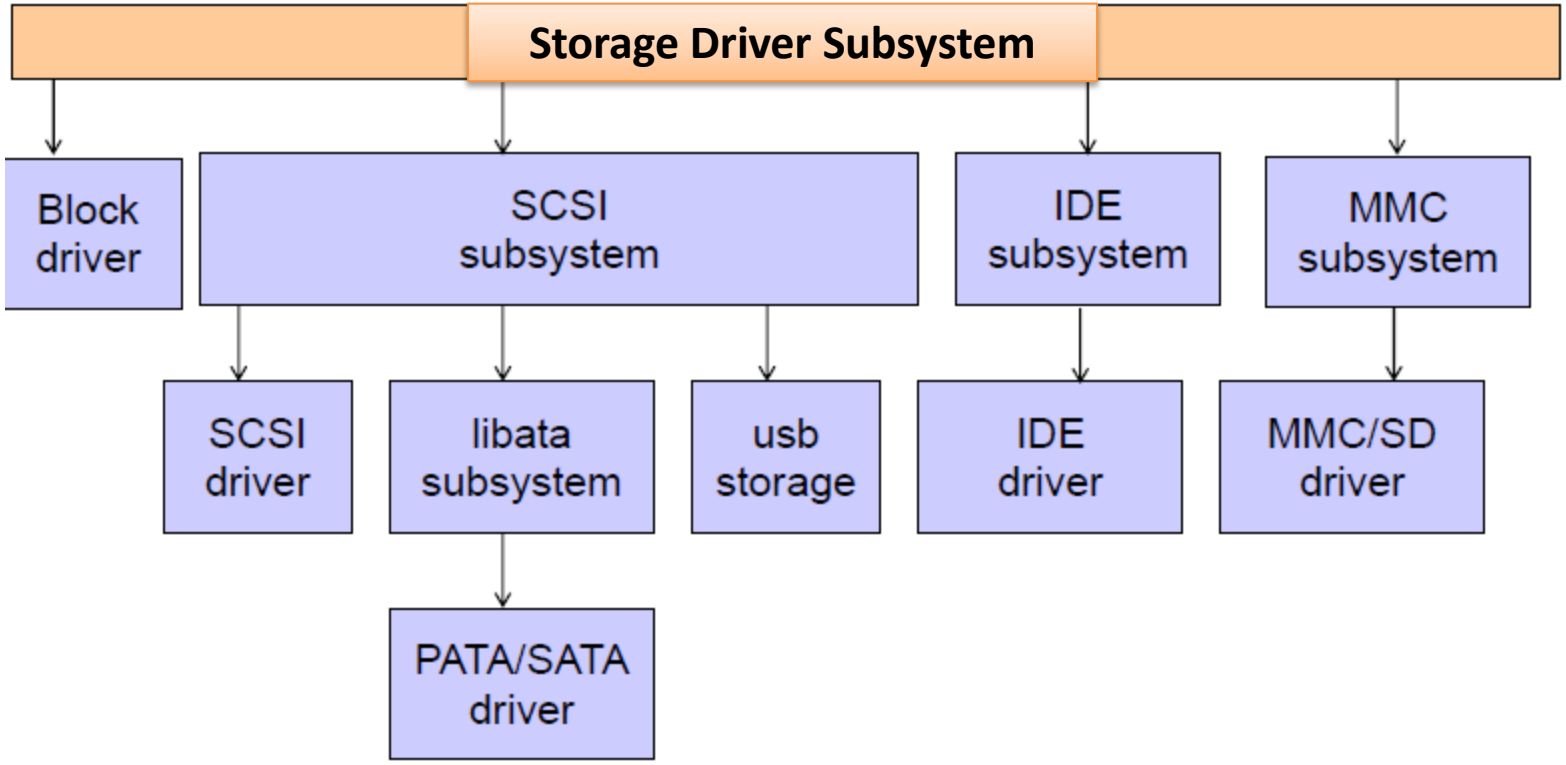
Storage Drivers



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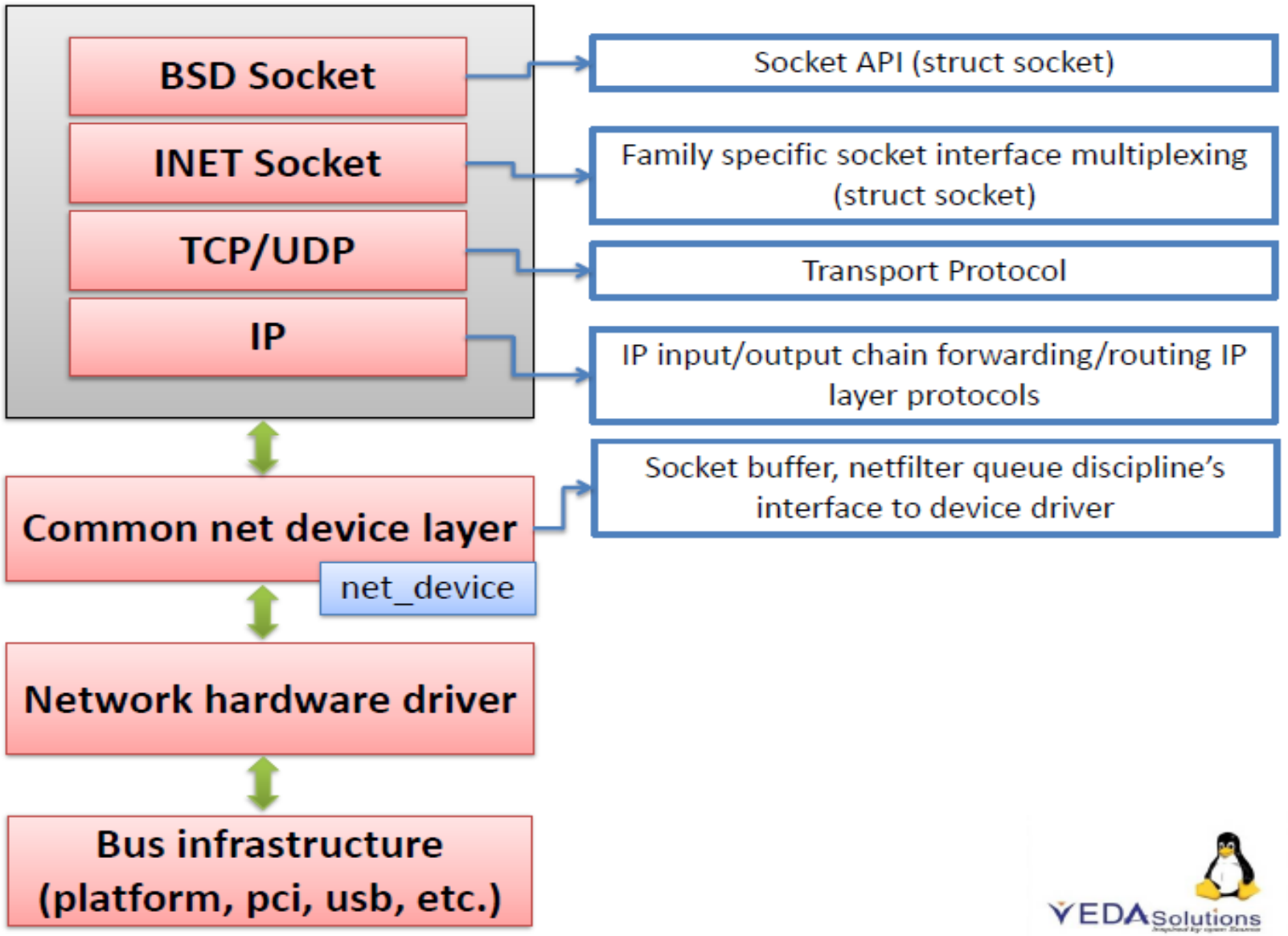
Network Communication Drivers

Architecture



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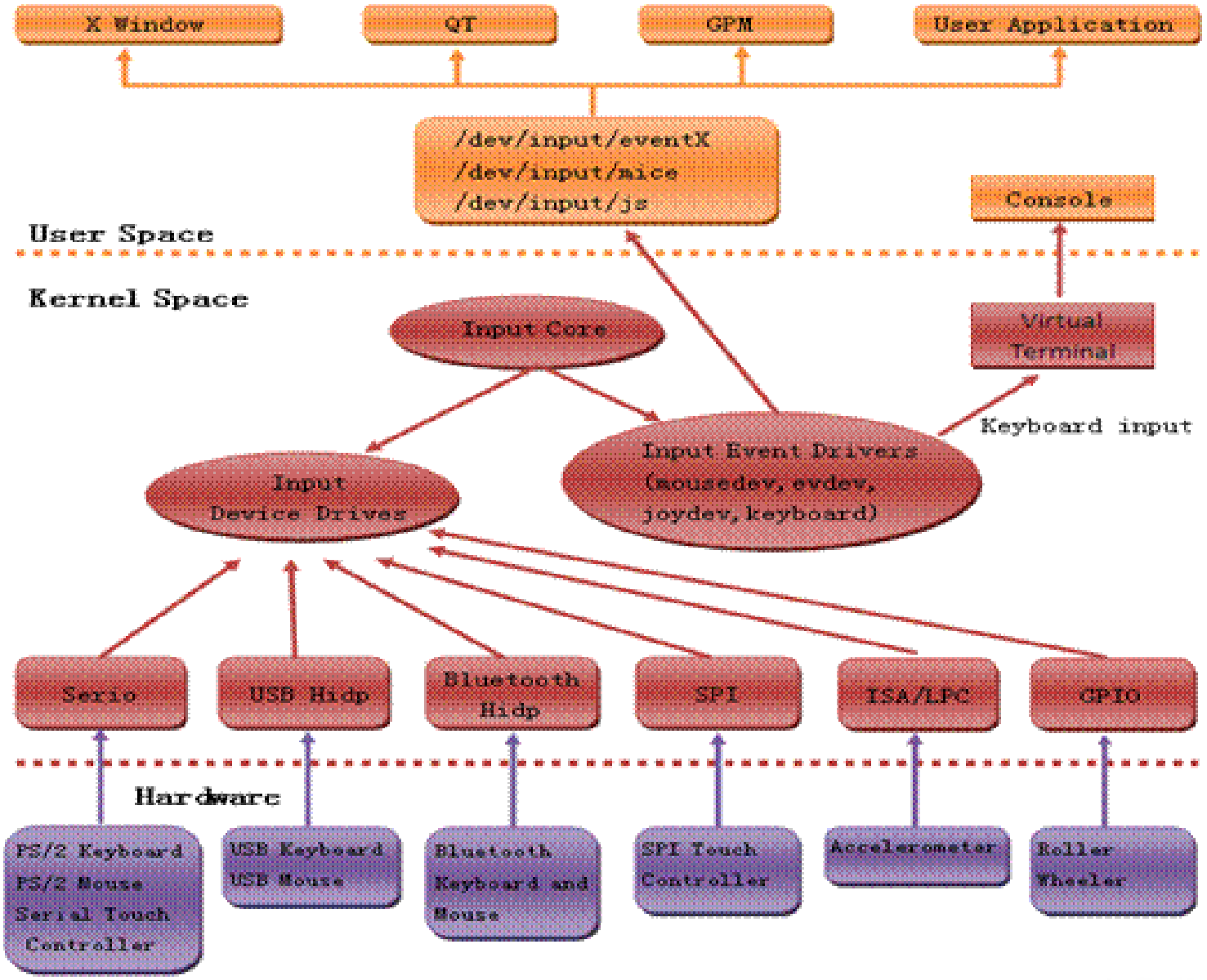
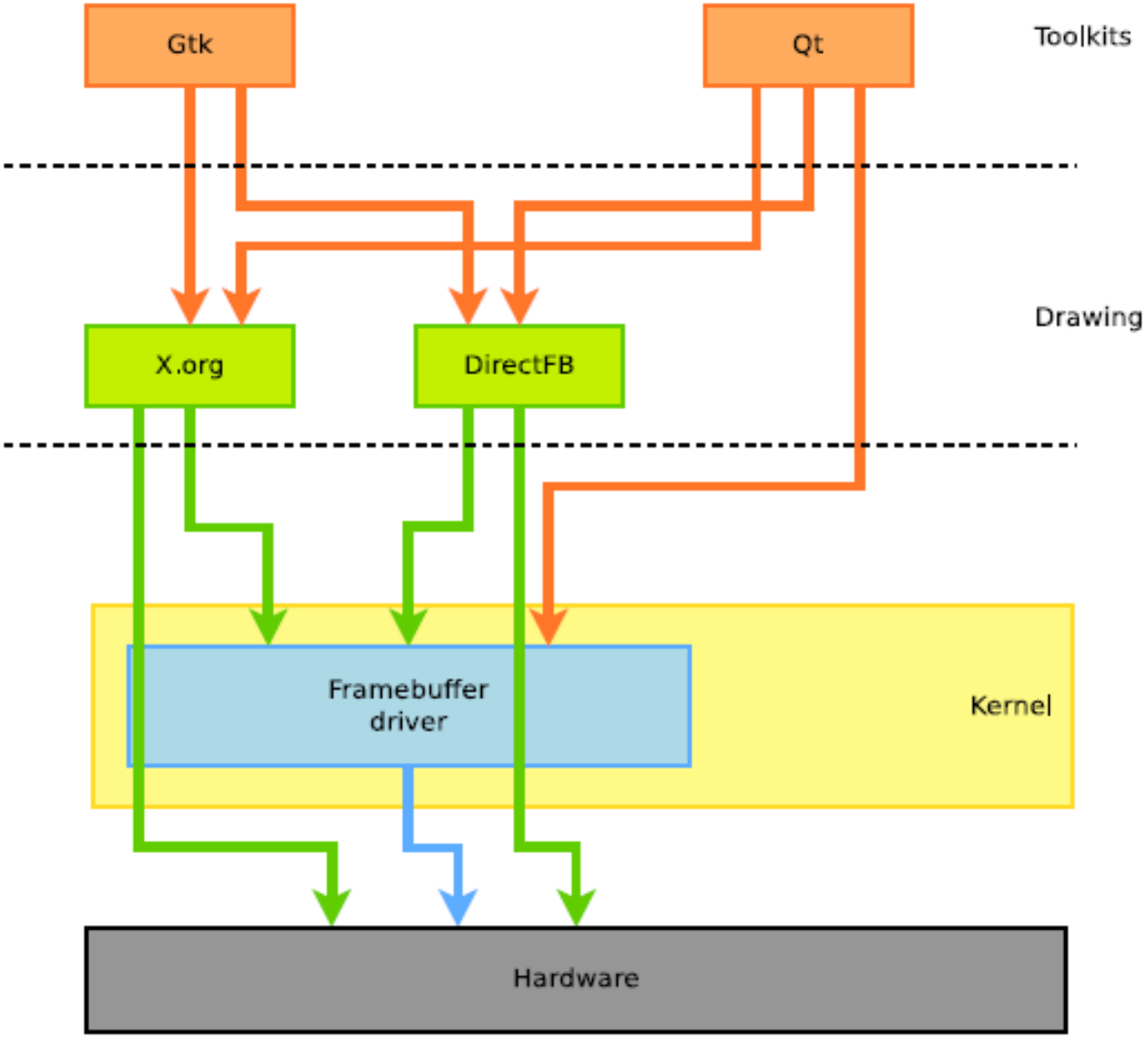
Linux Input Subsystem



Graphics Driver Subsystem



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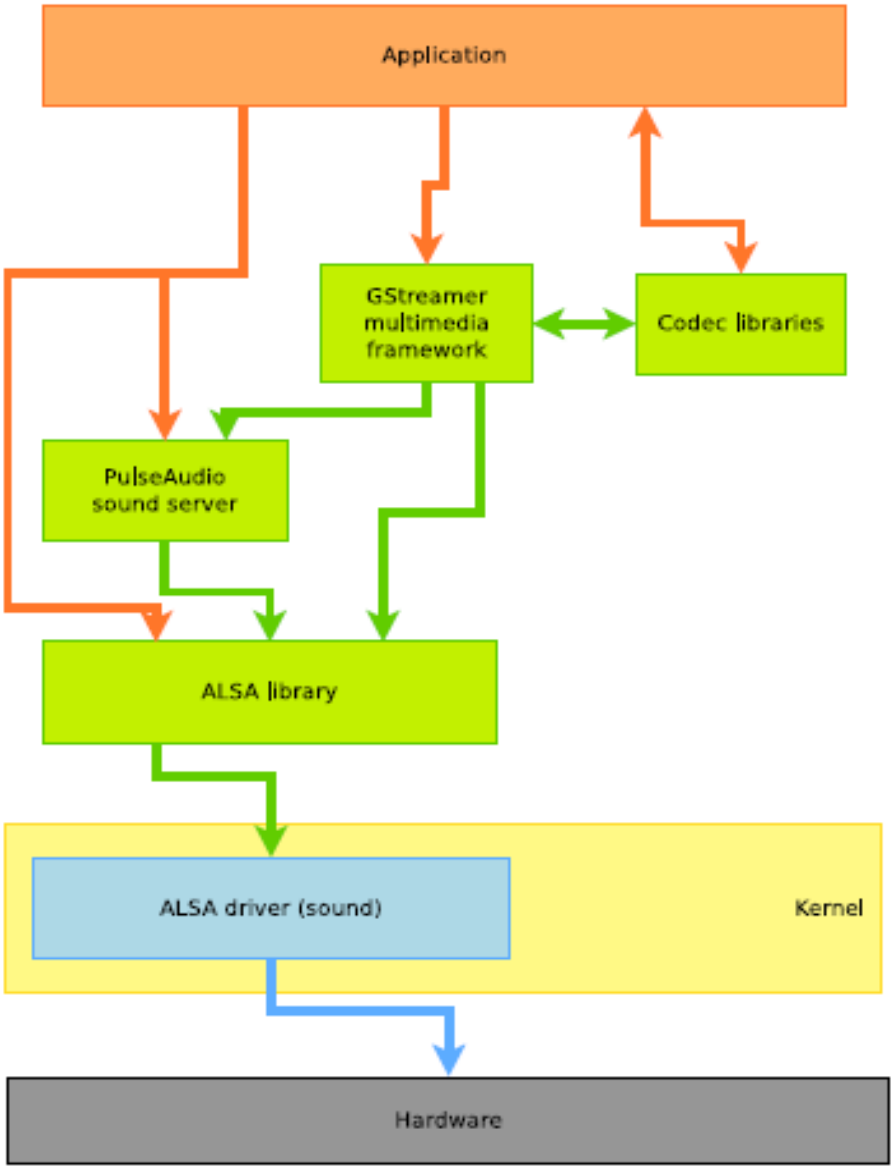
Sound Driver Subsystem



Thank You