$\S1$ SAT-TO-DIMACS INTRO 1

1. Intro. This is a filter that inputs the format used by SAT0, SAT1, etc., and outputs the well-known DIMACS format for satisfiability problems.

DIMACS format begins with zero or more optional comment lines, indicated by their first character 'c'. The next line should say 'p cnf n m', where n is the number of variables and m is the number of clauses. Then comes a string of m "clauses," which are sequences of nonzero integers of absolute value $\leq n$, followed by zero. A literal for the kth variable is represented by k; its complement is represented by -k.

SAT format is more flexible, more symbolic, and more complicated; it is explained in the programs cited above. I hacked this program from SAT3.

```
#include <stdio.h>
#include <stdlib.h>
#include <string.h>
#include "gb_flip.h"
#include <time.h>
  time_t myclock;
  typedef unsigned int uint;
                                           /* a convenient abbreviation */
  typedef unsigned long long ullng;
                                                     /* ditto */
  \langle \text{Type definitions 4} \rangle;
  \langle \text{Global variables 2} \rangle;
  main(int argc, char *argv[])
     register uint c, h, i, j, k, l, p, q, r, level, kk, pp, qq, ll;
     \langle \text{Process the command line } 3 \rangle;
     \langle \text{Initialize everything } 7 \rangle;
     \langle \text{Input the clauses 8} \rangle;
     if (verbose) (Report the successful completion of the input phase 20);
     myclock = time(0);
     printf("c_{\sqcup}file_{\sqcup}created_{\sqcup}by_{\sqcup}SAT-TO-DIMACS_{\sqcup}%s", ctime(\&myclock));
     \langle \text{ Output the clauses } 21 \rangle;
  }
2. \langle \text{Global variables 2} \rangle \equiv
  int random\_seed = 0; /* seed for the random words of gb\_rand */
  int verbose = 1;
                          /* level of verbosity */
  int hbits = 8;
                        /* logarithm of the number of the hash lists */
                               /* must exceed the length of the longest input line */
  int buf\_size = 1024;
See also section 6.
This code is used in section 1.
```

2 INTRO SAT-TO-DIMACS §3

3. On the command line one can say

```
'v⟨integer⟩' to enable various levels of verbose output on stderr;
'h⟨positive integer⟩' to adjust the hash table size;
'b⟨positive integer⟩' to adjust the size of the input buffer; and/or
's⟨integer⟩' to define the seed for any random numbers that are used.
⟨Process the command line 3⟩ ≡
for (j = argc - 1, k = 0; j; j - -)
switch (argv[j][0]) {
case 'v': k |= (sscanf (argv[j] + 1, "%d", &verbose) - 1); break;
case 'h': k |= (sscanf (argv[j] + 1, "%d", &buf_size) - 1); break;
case 'b': k |= (sscanf (argv[j] + 1, "%d", &buf_size) - 1); break;
case 's': k |= (sscanf (argv[j] + 1, "%d", &random_seed) - 1); break;
default: k = 1; /* unrecognized command-line option */
}
if (k ∨ hbits < 0 ∨ hbits > 30 ∨ buf_size ≤ 0) {
fprintf (stderr, "Usage: "%s | [v<n>] | [h<n>] | [b<n>] | [s<n] | (s<n) |
```

This code is used in section 1.

 $\S4$ SAT-TO-DIMACS THE I/O WRAPPER 3

4. The I/O wrapper. The following routines read the input and absorb it into temporary data areas from which all of the "real" data structures can readily be initialized. My intent is to incorporate these routines in all of the SAT-solvers in this series. Therefore I've tried to make the code short and simple, yet versatile enough so that almost no restrictions are placed on the sizes of problems that can be handled. These routines are supposed to work properly unless there are more than $2^{32} - 1 = 4,294,967,295$ occurrences of literals in clauses, or more than $2^{31} - 1 = 2,147,483,647$ variables or clauses.

In these temporary tables, each variable is represented by four things: its unique name; its serial number; the clause number (if any) in which it has most recently appeared; and a pointer to the previous variable (if any) with the same hash address. Several variables at a time are represented sequentially in small chunks of memory called "vchunks," which are allocated as needed (and freed later).

```
/* preferably (2^k - 1)/3 for some k */
#define vars_per_vchunk 341
\langle \text{Type definitions 4} \rangle \equiv
  typedef union {
    char ch8[8];
    uint u2[2];
    long long lng;
  } octa;
  typedef struct tmp_var_struct {
    octa name:
                     /* the name (one to eight ASCII characters) */
                     /* 0 for the first variable, 1 for the second, etc. */
    uint serial;
                    /* m if positively in clause m; -m if negatively there */
    int stamp;
                                         /* pointer for hash list */
    struct tmp_var_struct *next;
  } tmp_var;
  typedef struct vchunk_struct {
                                        /* previous chunk allocated (if any) */
    struct vchunk_struct *prev;
    tmp_var var[vars_per_vchunk];
  } vchunk;
See also section 5.
This code is used in section 1.
```

5. Each clause in the temporary tables is represented by a sequence of one or more pointers to the **tmp_var** nodes of the literals involved. A negated literal is indicated by adding 1 to such a pointer. The first literal of a clause is indicated by adding 2. Several of these pointers are represented sequentially in chunks of memory, which are allocated as needed and freed later.

```
#define cells\_per\_chunk 511 /* preferably 2^k - 1 for some k */ <br/>
{ Type definitions 4 } +\(\perp \) typedef struct chunk_struct {<br/>
    struct chunk_struct *prev; /* previous chunk allocated (if any) */<br/>
    tmp_var *cell[cells\_per\_chunk];<br/>
} chunk;
```

4 THE I/O WRAPPER SAT-TO-DIMACS §6

```
\langle \text{Global variables } 2 \rangle + \equiv
  \mathbf{char} * buf;
                   /* buffer for reading the lines (clauses) of stdin */
                          /* heads of the hash lists */
  tmp_var **hash;
  uint hash\_bits[93][8];
                              /* random bits for universal hash function */
                               /* the vchunk currently being filled */
  vchunk *cur\_vchunk;
  tmp\_var * cur\_tmp\_var;
                                 /* current place to create new tmp_var entries */
  tmp\_var *bad\_tmp\_var;
                                 /* the cur_tmp_var when we need a new vchunk */
  chunk *cur\_chunk;
                             /* the chunk currently being filled */
  tmp_var **cur_cell;
                              /* current place to create new elements of a clause */
  tmp_var **bad_cell;
                              /* the cur_cell when we need a new chunk */
                   /* how many distinct variables have we seen? */
  ullng vars;
  ullng clauses;
                      /* how many clauses have we seen? */
  ullng nullclauses; /* how many of them were null? */
  ullng cells;
                    /* how many occurrences of literals in clauses? */
7. \langle \text{Initialize everything 7} \rangle \equiv
  gb_init_rand(random_seed);
  buf = (\mathbf{char} *) \ malloc(buf\_size * \mathbf{sizeof}(\mathbf{char}));
  if (\neg buf) {
     fprintf(stderr, "Couldn't_allocate_the_input_buffer_(buf_size=%d)!\n", buf_size);
     exit(-2);
  hash = (\mathbf{tmp\_var} **) \ malloc(\mathbf{sizeof}(\mathbf{tmp\_var}) \ll hbits);
  if (\neg hash) {
    fprintf(stderr, "Couldn't_uallocate_u'd_uhash_ulist_uheads_u(hbits=%d)! \\ n", 1 \ll hbits, hbits);
     exit(-3);
  for (h = 0; h < 1 \ll hbits; h \leftrightarrow) hash[h] = \Lambda;
See also section 13.
This code is used in section 1.
```

§8 SAT-TO-DIMACS THE I/O WRAPPER

8. The hash address of each variable name has h bits, where h is the value of the adjustable parameter hbits. Thus the average number of variables per hash list is $n/2^h$ when there are n different variables. A warning is printed if this average number exceeds 10. (For example, if h has its default value, 8, the program will suggest that you might want to increase h if your input has 2560 different variables or more.)

All the hashing takes place at the very beginning, and the hash tables are actually recycled before any SAT-solving takes place; therefore the setting of this parameter is by no means crucial. But I didn't want to bother with fancy coding that would determine h automatically.

```
\langle \text{Input the clauses 8} \rangle \equiv
      while (1) {
             if (\neg fgets(buf, buf\_size, stdin)) break;
             clauses +\!\!+;
             if (buf[strlen(buf) - 1] \neq '\n') {
                   fprintf(stderr, "The \clause \cupon \clause 
                   fprintf(stderr, "umyubuf_size_uis_uonly_u%d!\n", buf_size);
                   fprintf(stderr, "Please \cup use \cup the \cup command-line \cup option \cup b < new size > . \n");
                   exit(-4);
             \langle \text{ Input the clause in } buf 9 \rangle;
      if ((vars \gg hbits) \ge 10) {
             fprintf(stderr, "There_lare_l\%ld_lvariables_lbut_lonly_l\%d_lhash_ltables; \n", vars, 1 \ll hbits);
             while ((vars \gg hbits) \ge 10) hbits +++;
            fprintf(stderr, "\_maybe\_you\_should\_use\_command-line\_option\_h%d?\n", hbits);
      clauses -= nullclauses;
      if (clauses \equiv 0) {
            fprintf(stderr, "No_{\square}clauses_{\square}were_{\square}input! \n");
             exit(-77);
      if (vars \ge *80000000) {
             fprintf(stderr, "Whoa, \_the\_input\_had\_%llu\_variables! \n", vars);
             exit(-664);
      if (clauses > #80000000) {
             fprintf(stderr, "Whoa, \_the\_input\_had\_\%llu\_clauses! \n", clauses);
             exit(-665);
      if (cells > #10000000) {
             fprintf(stderr, "Whoa, \_the \_input \_had \_\%llu \_occurrences \_of \_literals! \n", cells);
             exit(-666);
This code is used in section 1.
```

6 THE I/O WRAPPER SAT-TO-DIMACS §9

```
9. \langle Input the clause in buf \rangle \equiv
  for (j = k = 0; ; )  {
     while (buf[j] \equiv ' \cup ') j ++;
                                         /* scan to nonblank */
     if (buf[j] \equiv '\n') break;
     if (buf[j] < , , \lor buf[j] > , , ) {
       clauses);
        exit(-5);
     if (buf[j] \equiv , \, , \, ) \ i = 1, j ++;
     else i = 0;
     \langle Scan and record a variable; negate it if i \equiv 1 10\rangle;
  if (k \equiv 0) {
     fprintf(stderr, "(Empty line \%lld lis being ignored) \n", clauses);
                          /* strictly speaking it would be unsatisfiable */
  goto clause_done;
empty_clause: \langle Remove all variables of the current clause 17\rangle;
clause\_done: cells += k;
This code is used in section 8.
10. We need a hack to insert the bit codes 1 and/or 2 into a pointer value.
#define hack_in(q, t) (tmp_var *)(t | (ullng) q)
\langle Scan and record a variable; negate it if i \equiv 1 \mid 10 \rangle \equiv
  {
     register tmp_var *p;
     if (cur\_tmp\_var \equiv bad\_tmp\_var) (Install a new vchunk 11);
     \langle \text{Put the variable name beginning at } buf[j] \text{ in } cur\_tmp\_var \neg name \text{ and compute its hash code } h 14 \rangle;
     \langle \text{Find } cur\_tmp\_var \neg name \text{ in the hash table at } p \text{ 15} \rangle;
     if (p \rightarrow stamp \equiv clauses \lor p \rightarrow stamp \equiv -clauses) (Handle a duplicate literal 16)
     else {
       p \rightarrow stamp = (i ? -clauses : clauses);
       if (cur\_cell \equiv bad\_cell) (Install a new chunk 12);
       *cur\_cell = p;
       if (i \equiv 1) *cur\_cell = hack\_in(*cur\_cell, 1);
       if (k \equiv 0) *cur\_cell = hack\_in(*cur\_cell, 2);
       cur\_cell++, k++;
  }
This code is used in section 9.
```

 $\S11$ SAT-TO-DIMACS THE I/O WRAPPER 7

```
11.
       \langle \text{Install a new vchunk } 11 \rangle \equiv
  {
     register vchunk *new_vchunk;
     new\_vchunk = (\mathbf{vchunk} *) \ malloc(\mathbf{sizeof}(\mathbf{vchunk}));
     if (\neg new\_vchunk) {
        fprintf(stderr, "Can't_allocate_a_new_vchunk!\n");
        exit(-6);
     new\_vchunk \neg prev = cur\_vchunk, cur\_vchunk = new\_vchunk;
     cur\_tmp\_var = \&new\_vchunk \neg var[0];
     bad\_tmp\_var = \&new\_vchunk \neg var[vars\_per\_vchunk];
This code is used in section 10.
12. \langle \text{Install a new chunk } 12 \rangle \equiv
  {
     register chunk *new_chunk;
     new\_chunk = (\mathbf{chunk} *) \ malloc(\mathbf{sizeof}(\mathbf{chunk}));
     if (\neg new\_chunk) {
        fprintf(stderr, "Can't_{\square}allocate_{\square}a_{\square}new_{\square}chunk! \n");
        exit(-7);
     new\_chunk \neg prev = cur\_chunk, cur\_chunk = new\_chunk;
     cur\_cell = \&new\_chunk \neg cell[0];
     bad\_cell = \&new\_chunk \neg cell[cells\_per\_chunk];
This code is used in section 10.
      The hash code is computed via "universal hashing," using the following precomputed tables of random
13.
bits.
\langle \text{Initialize everything } 7 \rangle + \equiv
  for (j = 92; j; j--)
     for (k = 0; k < 8; k++) hash\_bits[j][k] = gb\_next\_rand();
14. \(\rightarrow\) Put the variable name beginning at buf[j] in cur\_tmp\_var\_name and compute its hash code h 14\(\rightarrow\)
  cur\_tmp\_var \rightarrow name.lng = 0;
  for (h = l = 0; buf[j + l] > ' " ' \land buf[j + l] \leq ' " '; l ++)  {
     if (l > 7) {
        fprintf(stderr, "Variable\_name\_%.9s...\_in\_the\_clause\_on\_line\_%lld\_is\_too\_long!\n",
             buf + j, clauses);
        exit(-8);
     h \oplus = hash\_bits[buf[j+l] - '!'][l];
     cur\_tmp\_var \rightarrow name.ch8[l] = buf[j+l];
  if (l \equiv 0) goto empty_clause; /* '~' by itself is like 'true' */
  i += l;
  h \&= (1 \ll hbits) - 1;
This code is used in section 10.
```

8 THE I/O WRAPPER SAT-TO-DIMACS §15

```
15. \langle \text{Find } cur\_tmp\_var \neg name \text{ in the hash table at } p \text{ 15} \rangle \equiv
  for (p = hash[h]; p; p = p \rightarrow next)
     if (p \rightarrow name.lng \equiv cur\_tmp\_var \rightarrow name.lng) break;
  if (\neg p) {
               /* new variable found */
     p = cur_tmp_var ++;
     p \rightarrow next = hash[h], hash[h] = p;
     p \rightarrow serial = vars ++;
     p \rightarrow stamp = 0;
This code is used in section 10.
16. The most interesting aspect of the input phase is probably the "unwinding" that we might need to do
when encountering a literal more than once in the same clause.
\langle Handle a duplicate literal \frac{16}{} \rangle \equiv
     if ((p \rightarrow stamp > 0) \equiv (i > 0)) goto empty\_clause;
This code is used in section 10.
17. An input line that begins with "" is silently treated as a comment. Otherwise redundant clauses are
logged, in case they were unintentional. (One can, however, intentionally use redundant clauses to force the
order of the variables.)
\langle Remove all variables of the current clause 17\rangle \equiv
  while (k) {
     \langle \text{Move } cur\_cell \text{ backward to the previous cell } 18 \rangle;
     k--;
  if ((buf[0] \neq ```) \lor (buf[1] \neq `` \cup `))
     fprintf(stderr, "(The_cause_on_line_%lld_is_always_satisfied)\n", clauses);
  else if (vars \equiv 0) printf("c_\%s", buf + 2);
                                                            /* retain opening comments */
  null clauses ++;
This code is used in section 9.
18. \langle \text{Move } cur\_cell \text{ backward to the previous cell } 18 \rangle \equiv
  if (cur\_cell > \& cur\_chunk \neg cell[0]) cur\_cell ---;
  else {
     register chunk *old\_chunk = cur\_chunk;
     cur\_chunk = old\_chunk \rightarrow prev; free(old\_chunk);
```

This code is used in sections 17 and 24.

 $cur_cell = bad_cell - 1;$

 $bad_cell = \&cur_chunk \neg cell[cells_per_chunk];$

 $\S19$ SAT-TO-DIMACS THE I/O WRAPPER \S

19. Here I must omit ' $free(old_vchunk)$ ' from the code that's usually in this section, because the variable data will be used later.

```
 \langle \text{Move } \textit{cur\_tmp\_var} \text{ backward to the previous temporary variable } 19 \rangle \equiv \\ \text{if } (\textit{cur\_tmp\_var} > \&\textit{cur\_vchunk} \neg \textit{var}[0]) \ \textit{cur\_tmp\_var} --; \\ \text{else } \{ \\ \text{register vchunk} *\textit{old\_vchunk} = \textit{cur\_vchunk}; \\ \textit{cur\_vchunk} = \textit{old\_vchunk} \neg \textit{prev}; \ /* \text{ and don't } \textit{free}(\textit{old\_vchunk}) */ \\ \textit{bad\_tmp\_var} = \&\textit{cur\_vchunk} \neg \textit{var}[\textit{vars\_per\_vchunk}]; \\ \textit{cur\_tmp\_var} = \textit{bad\_tmp\_var} - 1; \\ \} \\ \text{This code is used in section } 22.
```

20. \langle Report the successful completion of the input phase 20 \rangle \equiv $fprintf(stderr, "(%lld_variables, "%lld_clauses, "%llu_literals_successfully_read) \n", vars, clauses, cells);$

This code is used in section 1.

10 The output phase Sat-to-dimags $\S 21$

The output phase. I had to input everything first because DIMACS format specifies the number of variables and clauses right at the beginning. $\langle \text{ Output the clauses } 21 \rangle \equiv$ \langle Show the variable names as comments $22 \rangle$; printf("pucnfu%lldu%lld\n", vars, clauses); Translate all the temporary cells into the simple DIMACS form 23; $\langle \text{ Check consistency } 25 \rangle;$ This code is used in section 1. 22. This section is optional, but I'm including it today while I remember how to provide it. \langle Show the variable names as comments $22 \rangle \equiv$ **for** (c = vars; c; c --) { $\langle \text{Move } cur_tmp_var \text{ backward to the previous temporary variable } 19 \rangle;$ $printf("c_{\sqcup}\%.8s_{\sqcup}->_{\sqcup}\%d\n", cur_tmp_var\rightarrow name.ch8, c);$ This code is used in section 21. 23. (Translate all the temporary cells into the simple DIMACS form 23) \equiv for (c = clauses; c; c --) { \langle Translate the cells for the literals of clause c 24 \rangle ; $printf(" \cup 0 \setminus n");$ This code is used in section 21. **24.** #define $hack_out(q)$ (((ullng) q) & #3) #define $hack_clean(q)$ ((tmp_var *)((ullng) q & -4)) \langle Translate the cells for the literals of clause c 24 \rangle \equiv for (i = 0; i < 2; j ++) { $\langle \text{Move } cur_cell \text{ backward to the previous cell } 18 \rangle;$ $i = hack_out(*cur_cell);$ $p = hack_clean(*cur_cell) \neg serial;$ $printf("_\%s\%d", i \& 1 ? "-" : "", p + 1);$ This code is used in section 23. **25.** \langle Check consistency 25 $\rangle \equiv$ if $(cur_cell \neq \&cur_chunk \neg cell[0] \lor cur_chunk \neg prev \neq \Lambda \lor cur_tmp_var \neq$ & $cur_vchunk \rightarrow var[0] \lor cur_vchunk \rightarrow prev \neq \Lambda$) { fprintf(stderr, "This, can't, happen, (consistency, check, failure)!\n"); exit(-14);

This code is used in section 21.

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26. Index.

 $argc: \underline{1}, \underline{3}.$ argv: 1, 3. $bad_cell: \ \underline{6}, \ 10, \ 12, \ 18.$ $bad_{-}tmp_{-}var: \underline{6}, 10, 11, 19.$ $buf: \underline{6}, 7, 8, 9, 14, 17.$ $buf_size: 2, 3, 7, 8.$ c: <u>1</u>. cell: $\underline{5}$, 12, 18, 25. cells: $\underline{6}$, 8, 9, 20. $cells_per_chunk: \underline{5}, 12, 18.$ **chunk**: 5, 6, 12, 18. chunk_struct: $\underline{5}$. ch8: 4, 14, 22. $clause_done: \underline{9}.$ clauses: 6, 8, 9, 10, 14, 17, 20, 21, 23. ctime: 1.cur_cell: 6, 10, 12, 18, 24, 25. *cur_chunk*: <u>6</u>, 12, 18, 25. cur_tmp_var: 6, 10, 11, 14, 15, 19, 22, 25. $cur_vchunk: \underline{6}, 11, 19, 25.$ *empty_clause*: <u>9</u>, 14, 16. exit: 3, 7, 8, 9, 11, 12, 14, 25. fgets: 8.fprintf: 3, 7, 8, 9, 11, 12, 14, 17, 20, 25. free: 18, 19. gb_init_rand : 7. gb_next_rand : 13. gb_rand : 2. *h*: 1. $hack_clean: \underline{24}.$ $hack_in: \underline{10}.$ $hack_out$: $\underline{24}$. $hash: \underline{6}, 7, 15.$ $hash_bits: \underline{6}, 13, 14.$ hbits: 2, 3, 7, 8, 14. *i*: <u>1</u>. j: $\underline{1}$. k: <u>1</u>. $kk: \underline{1}.$ *l*: <u>1</u>. level: $\underline{1}$. $ll: \underline{1}.$ $lng: \underline{4}, 14, 15.$ $main: \underline{1}.$ malloc: 7, 11, 12. $myclock: \underline{1}.$ $name: \underline{4}, 14, 15, 22.$ $new_chunk: \underline{12}.$ $new_vchunk: \underline{11}.$ $next: \underline{4}, \underline{15}.$ $null clauses: \underline{6}, 8, 9, 17.$

octa: $\underline{4}$. $old_chunk: 18.$ $old_vchunk: \underline{19}.$ $p: \ \ \underline{1}, \ \underline{10}.$ $pp: \underline{1}.$ prev: $\underline{4}$, $\underline{5}$, 11, 12, 18, 19, 25. printf: 1, 17, 21, 22, 23, 24. q: $\underline{1}$. $qq: \underline{1}.$ r: 1. $random_seed: 2, 3, 7.$ serial: $\underline{4}$, $\underline{15}$, $\underline{24}$. sscanf: 3. $stamp: \underline{4}, 10, 15, 16.$ stderr: 3, 7, 8, 9, 11, 12, 14, 17, 20, 25. stdin: 6, 8. strlen: 8.time: 1. $tmp_var: \underline{4}, 5, 6, 7, 10, 24.$ $tmp_var_struct: \underline{4}.$ uint: 1, 4, 6. **ullng**: $\underline{1}$, 6, 10, 24. $u2: \underline{4}.$ $var: \underline{4}, 11, 19, 25.$ vars: 6, 8, 15, 17, 20, 21, 22. $vars_per_vchunk: \underline{4}, 11, 19.$ vchunk: 4, 6, 11, 19. vchunk_struct: 4. verbose: 1, 2, 3.

12 NAMES OF THE SECTIONS SAT-TO-DIMACS

```
\langle Check consistency 25\rangle Used in section 21.
\langle \text{Find } cur\_tmp\_var \neg name \text{ in the hash table at } p \mid 15 \rangle Used in section 10.
\langle \text{Global variables } 2, 6 \rangle Used in section 1.
\langle Handle a duplicate literal 16 \rangle Used in section 10.
\langle Initialize everything 7, 13\rangle Used in section 1.
\langle \text{ Input the clause in } buf 9 \rangle Used in section 8.
(Input the clauses 8) Used in section 1.
\langle \text{Install a new chunk 12} \rangle Used in section 10.
\langle \text{Install a new vchunk 11} \rangle Used in section 10.
\langle \text{Move } cur\_cell \text{ backward to the previous cell } 18 \rangle Used in sections 17 and 24.
(Move cur_tmp_var backward to the previous temporary variable 19) Used in section 22.
Output the clauses 21 Used in section 1.
\langle \text{Process the command line } 3 \rangle Used in section 1.
Put the variable name beginning at buf[j] in cur\_tmp\_var \neg name and compute its hash code h 14 \quad Used
(Remove all variables of the current clause 17) Used in section 9.
(Report the successful completion of the input phase 20) Used in section 1.
\langle Scan and record a variable; negate it if i \equiv 1 \mid 10 \rangle Used in section 9.
\langle Show the variable names as comments 22\rangle Used in section 21.
 Translate all the temporary cells into the simple DIMACS form 23
                                                                                         Used in section 21.
\langle Translate the cells for the literals of clause c 24\rangle Used in section 23.
\langle \text{ Type definitions } 4, 5 \rangle Used in section 1.
```

SAT-TO-DIMACS

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