Thapar Institute of Engineering and Technology, Patiala

Computer Science & Engineering Department

BE-Computer Engineering (VII semester), MST 28 September 2024 Time: 02 Hours, MM: 30			UCS704: Embedded System Design Name of Faculty: Dr. Anju, Dr. Deepshikha, Dr. Rohit, Dr. Manisha			
	Attemp	ata.	question must be answered at one p		Assum	ie
					CO	BL
Q.1 (a)	Explain the working of the following pins of 8051 microcontrollers:			(4)	CO3	L1
	i.	i. \overline{PSEN}				
	ii.	\overline{EA}				
	iii.	RXD				
	iv.	RST				
(b)	i.	Convert the binary number: $(11011101100.0101)_2 = (?)_{BCD}$			CO1	L2
	ii.	Perform BCD subtraction using 1 (38.26) ₁₀	0's compliment method : $(26.25)_{10}$ -			
Q.2 (a)	Draw	Draw the circuit diagram and explain the working of Power-on Reset and			CO3	L1
	Brow	Brown-out Reset used in embedded systems.				
(b)	A 32 - bit wide main memory unit with a capacity of 1 GB is built using				CO3	L2
	$256M\ X\ 4\text{-bit}\ DRAM\ chips.$ The number of rows of memory cells in the					
	DRAM chip is 2^{14} . The time taken to perform one refresh operation is 50					
	nanoseconds. The refresh period is 2 milliseconds. Find the percentage					
	(rounded to the closest integer) of the time available for performing the					
	mem	ory read/write operations in the ma	ain memory unit.			
Q.3 (a)	Give one primary advantage of invasive signal reading technique over Non-			(2)	CO1	L4
	invasive technique in Brain Machine Interface (BMI). Is it possible to use					

human organ as end device in BMI? Justify your answer with help of an example.

LED is used as display in many embedded systems. Sixteen segments LED is (6) CO1 L3 an alphanumeric popular display which uses 16 LEDs to represent various alphabet and numbers as shown in fig.1. Assume "a1" as the LSB bit, "m" as MSB bit and refer remaining bits in order.

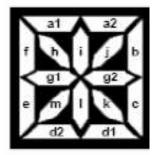


Fig.1: 16-Segment LED display

Display the following characters on the 16-segment LED display and also provide the sixteen bit binary code used to display the characters using common anode connection:

- i. V (capital)
- ii. f (small)
- iii. 4
- Q.4 Describe Light Detecting Register (LDR) sensor. Using LDR, draw a circuit (3) CO3 L1 for light activated switch and explain its working.

Explain following with diagram:

(4) CO₃ L₂

- (b) i. H-Bridge
 - ii. High Impedance state or Hi-Z state

