More Reductions

Abhiram Ranade

March 29, 2016

Today we prove:

$$\mathit{IS} \leq_{\mathit{K}} \mathit{ILP} \leq_{\mathit{K}} \mathit{CSAT} \leq_{\mathit{K}} \mathit{CNFSAT} \leq_{\mathit{K}} \mathit{IS}$$

Decision versions

Today we prove:

 $IS \leq_{\mathcal{K}} ILP \leq_{\mathcal{K}} CSAT \leq_{\mathcal{K}} CNFSAT \leq_{\mathcal{K}} IS$

Decision versions

CSAT: "Circuit satisfiability"

Today we prove:

 $IS <_K ILP <_K CSAT <_K CNFSAT <_K IS$

Decision versions

CSAT: "Circuit satisfiability"

CNFSAT: "Conjunctive normal form satisfiability"

Today we prove:

 $IS \leq_{\mathcal{K}} ILP \leq_{\mathcal{K}} CSAT \leq_{\mathcal{K}} CNFSAT \leq_{\mathcal{K}} IS$ Decis

Decision versions

CSAT: "Circuit satisfiability"

CNFSAT: "Conjunctive normal form satisfiability"

Thus all above problems are equivalent for the purposes of designing polynomial time algorithms.

Original Input: $m \times n$ Matrix A, m vector b, n vector c.

Original Input: $m \times n$ Matrix A, m vector b, n vector c. 0-1 Optimization problem: $\max c^T x$ s.t. $x \in \{0,1\}^n$, $Ax \le b$

Original Input: $m \times n$ Matrix A, m vector b, n vector c. 0-1 Optimization problem: $\max c^T x$ s.t. $x \in \{0,1\}^n$, $Ax \le b$

Natural existence version: Does there exist an $x \in \{0,1\}^n$ s.t. $Ax \le b$ and $c^Tx \ge t$, where t is an additional input.

Original Input: $m \times n$ Matrix A, m vector b, n vector c. 0-1 Optimization problem: $\max c^T x$ s.t. $x \in \{0,1\}^n$, $Ax \le b$

Natural existence version: Does there exist an $x \in \{0,1\}^n$ s.t. $Ax \le b$ and $c^Tx \ge t$, where t is an additional input.

But the condition $c^T x \ge t$ can be included into $Ax \le b$

Original Input: $m \times n$ Matrix A, m vector b, n vector c. 0-1 Optimization problem: $\max c^T x$ s.t. $x \in \{0,1\}^n$, $Ax \le b$

Natural existence version: Does there exist an $x \in \{0,1\}^n$ s.t. $Ax \le b$ and $c^Tx \ge t$, where t is an additional input.

But the condition $c^T x \ge t$ can be included into $Ax \le b$ Add row $-c^T$ to A, and append -t to b.

Original Input: $m \times n$ Matrix A, m vector b, n vector c. 0-1 Optimization problem: $\max c^T x$ s.t. $x \in \{0,1\}^n$, $Ax \le b$

Natural existence version: Does there exist an $x \in \{0,1\}^n$ s.t. $Ax \le b$ and $c^Tx \ge t$, where t is an additional input.

But the condition $c^T x \ge t$ can be included into $Ax \le b$ Add row $-c^T$ to A, and append -t to b.

(0-1) ILP Existence version:

Original Input: $m \times n$ Matrix A, m vector b, n vector c. 0-1 Optimization problem: $\max c^T x$ s.t. $x \in \{0,1\}^n$, $Ax \le b$

Natural existence version: Does there exist an $x \in \{0,1\}^n$ s.t. $Ax \le b$ and $c^Tx \ge t$, where t is an additional input.

But the condition $c^T x \ge t$ can be included into $Ax \le b$ Add row $-c^T$ to A, and append -t to b.

(0-1) ILP Existence version: Input: Matrix *A*, vector *b*.

Original Input: $m \times n$ Matrix A, m vector b, n vector c. 0-1 Optimization problem: $\max c^T x$ s.t. $x \in \{0,1\}^n$, $Ax \le b$

Natural existence version: Does there exist an $x \in \{0,1\}^n$ s.t. $Ax \le b$ and $c^Tx \ge t$, where t is an additional input.

But the condition $c^T x \ge t$ can be included into $Ax \le b$ Add row $-c^T$ to A, and append -t to b.

(0-1) ILP Existence version:

Input: Matrix A, vector b.

Output: Does there exist $x \in \{0,1\}^n$ s.t. $Ax \le b$?

Original Input: $m \times n$ Matrix A, m vector b, n vector c. 0-1 Optimization problem: $\max c^T x$ s.t. $x \in \{0,1\}^n$, $Ax \le b$

Natural existence version: Does there exist an $x \in \{0,1\}^n$ s.t. $Ax \le b$ and $c^Tx \ge t$, where t is an additional input.

But the condition $c^T x \ge t$ can be included into $Ax \le b$ Add row $-c^T$ to A, and append -t to b.

(0-1) ILP Existence version:

Input: Matrix A, vector b.

Output: Does there exist $x \in \{0,1\}^n$ s.t. $Ax \le b$?

Note: (0-1) ILP-Existence problem is a decision problem.

IS(G,k): Does a graph G have an independent set of size k? ILP(A,b): Does there exist a 0-1 vector x s.t. $Ax \le b$?

IS(G,k): Does a graph G have an independent set of size k? ILP(A,b): Does there exist a 0-1 vector x s.t. $Ax \le b$?

Instance map:

IS(G,k): Does a graph G have an independent set of size k? ILP(A,b): Does there exist a 0-1 vector x s.t. $Ax \le b$?

Instance map:

IS(G,k): Does a graph G have an independent set of size k? ILP(A,b): Does there exist a 0-1 vector x s.t. $Ax \le b$?

Instance map:

$$\forall (u,v) \in E(G): x_u + x_v \leq 1$$

IS(G,k): Does a graph G have an independent set of size k? ILP(A,b): Does there exist a 0-1 vector x s.t. $Ax \le b$?

Instance map:

$$\forall (u,v) \in E(G): x_u + x_v \leq 1$$

$$\sum_{u} x_{u} = k$$

IS(G,k): Does a graph G have an independent set of size k? ILP(A,b): Does there exist a 0-1 vector x s.t. $Ax \le b$?

Instance map:

$$\forall (u, v) \in E(G) : x_u + x_v \leq 1$$

$$\sum_{u} x_{u} = k$$
 Equivalent: $\sum_{u} x_{u} \le k$, $-\sum_{u} x_{u} \le -k$

IS(G,k): Does a graph G have an independent set of size k? ILP(A,b): Does there exist a 0-1 vector x s.t. $Ax \le b$?

Instance map:

 x_u : 0-1 variable for each vertex u.

$$\forall (u, v) \in E(G) : x_u + x_v \leq 1$$

$$\sum_{u} x_{u} = k$$
 Equivalent:
$$\sum_{u} x_{u} \le k, \quad -\sum_{u} x_{u} \le -k$$

Form A, b from the above inequalities.

$IS <_{\kappa} ILP$

IS(G,k): Does a graph G have an independent set of size k? ILP(A,b): Does there exist a 0-1 vector x s.t. Ax < b?

Instance map:

 x_{μ} : 0-1 variable for each vertex u.

$$\forall (u, v) \in E(G) : x_u + x_v \leq 1$$

$$\sum_{u} x_{u} = k$$
 Equivalent:
$$\sum_{u} x_{u} \le k, \quad -\sum_{u} x_{u} \le -k$$

Form A, b from the above inequalities.

Instance map function runs in time polynomial in size of G.

$IS <_{\kappa} ILP$

IS(G,k): Does a graph G have an independent set of size k? ILP(A,b): Does there exist a 0-1 vector x s.t. $Ax \le b$?

Instance map:

 x_{μ} : 0-1 variable for each vertex u.

$$\forall (u, v) \in E(G) : x_u + x_v \leq 1$$

$$\sum_{u} x_{u} = k$$
 Equivalent:
$$\sum_{u} x_{u} \le k, \quad -\sum_{u} x_{u} \le -k$$
 Form A b from the above inequalities

Form A, b from the above inequalities.

Instance map function runs in time polynomial in size of G.

$$IS(G,k) = true \Rightarrow ILP(A,b) = true.$$

$IS <_{\kappa} ILP$

IS(G,k): Does a graph G have an independent set of size k? ILP(A,b): Does there exist a 0-1 vector x s.t. $Ax \le b$?

Instance map:

 x_{μ} : 0-1 variable for each vertex u.

$$\forall (u, v) \in E(G) : x_u + x_v \leq 1$$

$$\sum_{u} x_{u} = k$$
 Equivalent:
$$\sum_{u} x_{u} \le k, \quad -\sum_{u} x_{u} \le -k$$
 Form A b from the above inequalities

Form A, b from the above inequalities.

Instance map function runs in time polynomial in size of G.

$$IS(G,k) = true \Rightarrow ILP(A,b) = true.$$

$$ILP(A,b) = true \Rightarrow IS(G,k) = true.$$

Input: C = a combinational circuit with inputs 1, ..., n and a single output.

Input: C = a combinational circuit with inputs 1, ..., n and a single output.

C is specified by giving its DAG, and vertex labels: AND, OR, NOT

Input: C = a combinational circuit with inputs 1, ..., n and a single output.

C is specified by giving its DAG, and vertex labels: AND, OR, NOT

Output: Does there exist $z = (z_1, \ldots, z_n) \in \{0, 1\}^n$ s.t. if for all i input i is set to value z_i the output will take the value true.

0 means false/NO, 1 means true/YES.

Input: C = a combinational circuit with inputs 1, ..., n and a single output.

C is specified by giving its DAG, and vertex labels: AND, OR, NOT

Output: Does there exist $z=(z_1,\ldots,z_n)\in\{0,1\}^n$ s.t. if for all i input i is set to value z_i the output will take the value true.

0 means false/NO, 1 means true/YES.

If such a z exists, then C is said to be satisfiable.

Input: C = a combinational circuit with inputs 1, ..., n and a single output.

C is specified by giving its DAG, and vertex labels: AND, OR, NOT

Output: Does there exist $z=(z_1,\ldots,z_n)\in\{0,1\}^n$ s.t. if for all i input i is set to value z_i the output will take the value true.

0 means false/NO, 1 means true/YES.

If such a z exists, then C is said to be satisfiable. If no such z exists, then C is unsatisfiable.

$ILP \leq_K CSAT$

$ILP \leq_{\kappa} CSAT$

ILP(A,b) : Does there exist 0-1 vector x s.t. $Ax \le b$.

$ILP <_{\kappa} CSAT$

ILP(A,b) : Does there exist 0-1 vector x s.t. $Ax \leq b$. CSAT(C) : Does there exist 0-1 vector z which if given as input to C produces output 1?

$ILP \leq_{\kappa} CSAT$

ILP(A,b): Does there exist 0-1 vector x s.t. $Ax \le b$. CSAT(C): Does there exist 0-1 vector z which if given as input to C produces output 1?

Reduction idea: (1) $z \equiv x$.

ILP(A,b): Does there exist 0-1 vector x s.t. $Ax \le b$. CSAT(C): Does there exist 0-1 vector z which if given as input to C produces output 1?

Reduction idea: (1) $z \equiv x$. (2) Make the circuit check $Az \leq b$.

ILP(A,b): Does there exist 0-1 vector x s.t. $Ax \le b$.

 $\mathsf{CSAT}(\mathsf{C})$: Does there exist 0-1 vector z which if given as input to C produces output 1?

Reduction idea: (1) $z \equiv x$. (2) Make the circuit check $Az \leq b$.

Instance map:

ILP(A,b): Does there exist 0-1 vector x s.t. $Ax \le b$.

CSAT(C): Does there exist 0-1 vector z which if given as input to C produces output 1?

Reduction idea: (1) $z \equiv x$. (2) Make the circuit check $Az \leq b$.

Instance map: IM(A,b){

ILP(A,b): Does there exist 0-1 vector x s.t. $Ax \le b$.

 $\mathsf{CSAT}(\mathsf{C})$: Does there exist 0-1 vector z which if given as input to C produces output 1?

Reduction idea: (1) $z \equiv x$. (2) Make the circuit check $Az \leq b$.

Instance map: IM(A,b){

Construct a circuit with *n* inputs. $z_1, \ldots, z_n =$ values on inputs.

ILP(A,b): Does there exist 0-1 vector x s.t. $Ax \le b$. CSAT(C): Does there exist 0-1 vector z which if given as input to

C produces output 1?

Reduction idea: (1) $z \equiv x$. (2) Make the circuit check $Az \leq b$.

Instance map: IM(A,b){

Construct a circuit with *n* inputs. $z_1, \ldots, z_n = \text{values on inputs.}$

Subcircuit C_1 for checking condition $a_{11}z_1 + a_{12}z_2 + \ldots + a_{1n} \leq b_1$

$ILP \leq_{\kappa} CSAT$

ILP(A,b): Does there exist 0-1 vector x s.t. $Ax \le b$. CSAT(C): Does there exist 0-1 vector z which if given as input to C produces output 1?

Reduction idea: (1) $z \equiv x$. (2) Make the circuit check $Az \leq b$.

Instance map: IM(A,b){

Construct a circuit with n inputs. $z_1,\ldots,z_n=$ values on inputs. Subcircuit C_1 for checking condition $a_{11}z_1+a_{12}z_2+\ldots+a_{1n}\leq b_1$ "AND bits of a_{1j} with z_j , add the results and feed to comparator having other input b_1 "

$ILP \leq_K CSAT$

ILP(A,b): Does there exist 0-1 vector x s.t. $Ax \le b$. CSAT(C): Does there exist 0-1 vector z which if given as input to C produces output 1?

Reduction idea: (1) $z \equiv x$. (2) Make the circuit check $Az \leq b$.

Instance map: IM(A,b){

Construct a circuit with n inputs. $z_1,\ldots,z_n=$ values on inputs. Subcircuit C_1 for checking condition $a_{11}z_1+a_{12}z_2+\ldots+a_{1n}\leq b_1$ "AND bits of a_{1j} with z_j , add the results and feed to comparator having other input b_1 "

$ILP \leq_K CSAT$

ILP(A,b): Does there exist 0-1 vector x s.t. $Ax \le b$. CSAT(C): Does there exist 0-1 vector z which if given as input to C produces output 1?

Reduction idea: (1) $z \equiv x$. (2) Make the circuit check $Az \leq b$.

Instance map: IM(A,b){

Construct a circuit with n inputs. $z_1,\ldots,z_n=$ values on inputs. Subcircuit C_1 for checking condition $a_{11}z_1+a_{12}z_2+\ldots+a_{1n}\leq b_1$ "AND bits of a_{1j} with z_j , add the results and feed to comparator having other input b_1 "

Overall circuit : AND together outputs of all C_i .

► Construction happens in polytime in *n*

ILP(A,b): Does there exist 0-1 vector x s.t. $Ax \le b$. CSAT(C): Does there exist 0-1 vector z which if given as input to C produces output 1?

Reduction idea: (1) $z \equiv x$. (2) Make the circuit check $Az \leq b$.

Instance map: IM(A,b){

Construct a circuit with n inputs. $z_1,\ldots,z_n=$ values on inputs. Subcircuit C_1 for checking condition $a_{11}z_1+a_{12}z_2+\ldots+a_{1n}\leq b_1$ "AND bits of a_{1j} with z_j , add the results and feed to comparator having other input b_1 "

- Construction happens in polytime in n
- ▶ ILP(A,b) has a solution x

$ILP \leq_{\kappa} CSAT$

ILP(A,b): Does there exist 0-1 vector x s.t. $Ax \le b$. CSAT(C): Does there exist 0-1 vector z which if given as input to C produces output 1?

Reduction idea: (1) $z \equiv x$. (2) Make the circuit check $Az \leq b$.

Instance map: IM(A,b){

Construct a circuit with n inputs. $z_1, \ldots, z_n = \text{values on inputs}$. Subcircuit C_1 for checking condition $a_{11}z_1 + a_{12}z_2 + \ldots + a_{1n} \leq b_1$ "AND bits of a_{1j} with z_j , add the results and feed to comparator having other input b_1 "

- Construction happens in polytime in n
- ► ILP(A,b) has a solution x
 ⇒ if x = circuit input, then output = 1.

$ILP \leq_{\kappa} CSAT$

ILP(A,b): Does there exist 0-1 vector x s.t. $Ax \le b$. CSAT(C): Does there exist 0-1 vector z which if given as input to C produces output 1?

Reduction idea: (1) $z \equiv x$. (2) Make the circuit check $Az \leq b$.

Instance map: IM(A,b){

Construct a circuit with n inputs. $z_1,\ldots,z_n=$ values on inputs. Subcircuit C_1 for checking condition $a_{11}z_1+a_{12}z_2+\ldots+a_{1n}\leq b_1$ "AND bits of a_{1j} with z_j , add the results and feed to comparator having other input b_1 "

- Construction happens in polytime in n
- ► ILP(A,b) has a solution x⇒ if x = circuit input, then output = 1. ⇒ C is satisfiable

$ILP \leq_K CSAT$

ILP(A,b): Does there exist 0-1 vector x s.t. $Ax \le b$. CSAT(C): Does there exist 0-1 vector z which if given as input to C produces output 1?

Reduction idea: (1) $z \equiv x$. (2) Make the circuit check $Az \leq b$.

Instance map: IM(A,b){

Construct a circuit with n inputs. $z_1,\ldots,z_n=$ values on inputs. Subcircuit C_1 for checking condition $a_{11}z_1+a_{12}z_2+\ldots+a_{1n}\leq b_1$ "AND bits of a_{1j} with z_j , add the results and feed to comparator having other input b_1 "

- Construction happens in polytime in n
- ▶ ILP(A,b) has a solution x⇒ if x = circuit input, then output = 1. ⇒ C is satisfiable
- C is satisfiable for inputs z

$ILP \leq_K CSAT$

ILP(A,b): Does there exist 0-1 vector x s.t. $Ax \le b$. CSAT(C): Does there exist 0-1 vector z which if given as input to C produces output 1?

Reduction idea: (1) $z \equiv x$. (2) Make the circuit check $Az \leq b$.

Instance map: IM(A,b){

Construct a circuit with n inputs. $z_1, \ldots, z_n = \text{values on inputs}$. Subcircuit C_1 for checking condition $a_{11}z_1 + a_{12}z_2 + \ldots + a_{1n} \leq b_1$ "AND bits of a_{1j} with z_j , add the results and feed to comparator having other input b_1 "

- Construction happens in polytime in n
- ▶ ILP(A,b) has a solution x⇒ if x = circuit input, then output = 1. ⇒ C is satisfiable
- C is satisfiable for inputs z

$$\Rightarrow Az \leq b$$



ILP(A,b): Does there exist 0-1 vector x s.t. $Ax \le b$. CSAT(C): Does there exist 0-1 vector z which if given as input to C produces output 1?

Reduction idea: (1) $z \equiv x$. (2) Make the circuit check $Az \leq b$.

Instance map: IM(A,b){

Construct a circuit with *n* inputs. $z_1, \ldots, z_n = values$ on inputs. Subcircuit C_1 for checking condition $a_{11}z_1 + a_{12}z_2 + \ldots + a_{1n} \leq b_1$ "AND bits of a_{1i} with z_i , add the results and feed to comparator having other input b_1 "

- Construction happens in polytime in n
- ▶ ILP(A,b) has a solution x \Rightarrow if x = circuit input, then output $= 1. \Rightarrow C$ is satisfiable
- C is satisfiable for inputs z
 - \Rightarrow $Az \leq b \Rightarrow$ ILP instance has a solution.

Input: Circuit in conjunctive normal form:

 \triangleright z_1, \ldots, z_n : Circuit inputs.

- \triangleright z_1, \ldots, z_n : Circuit inputs.
- ▶ There are *m* OR gates, each with *k* inputs. Circuit inputs or their complements are connected to inputs of OR gates.

- \triangleright z_1, \ldots, z_n : Circuit inputs.
- ▶ There are *m* OR gates, each with *k* inputs. Circuit inputs or their complements are connected to inputs of OR gates.
- ▶ Outputs of OR gates are connected to an AND gate.

- $ightharpoonup z_1, \ldots, z_n$: Circuit inputs.
- ► There are m OR gates, each with k inputs. Circuit inputs or their complements are connected to inputs of OR gates.
- ▶ Outputs of OR gates are connected to an AND gate.
- ▶ z : Output of AND gate, also the output of overall circuit.

Input: Circuit in conjunctive normal form:

- \triangleright z_1, \ldots, z_n : Circuit inputs.
- ► There are m OR gates, each with k inputs. Circuit inputs or their complements are connected to inputs of OR gates.
- Outputs of OR gates are connected to an AND gate.
- ► z : Output of AND gate, also the output of overall circuit.

Output: Is it possible to assign values to inputs s.t. circuit output is 1?

Input: Circuit in conjunctive normal form:

- \triangleright z_1, \ldots, z_n : Circuit inputs.
- ▶ There are *m* OR gates, each with *k* inputs. Circuit inputs or their complements are connected to inputs of OR gates.
- ▶ Outputs of OR gates are connected to an AND gate.
- ▶ z : Output of AND gate, also the output of overall circuit.

Output: Is it possible to assign values to inputs s.t. circuit output is 1?

Obvious result: CNFSAT \leq_K CSAT

Input: Circuit in conjunctive normal form:

- \triangleright z_1, \ldots, z_n : Circuit inputs.
- ▶ There are *m* OR gates, each with *k* inputs. Circuit inputs or their complements are connected to inputs of OR gates.
- ▶ Outputs of OR gates are connected to an AND gate.
- z : Output of AND gate, also the output of overall circuit.

Output: Is it possible to assign values to inputs s.t. circuit output is 1?

Obvious result: CNFSAT \leq_K CSAT Instance map: Identity.

Input: Circuit in conjunctive normal form:

- \triangleright z_1, \ldots, z_n : Circuit inputs.
- ▶ There are *m* OR gates, each with *k* inputs. Circuit inputs or their complements are connected to inputs of OR gates.
- Outputs of OR gates are connected to an AND gate.
- z : Output of AND gate, also the output of overall circuit.

Output: Is it possible to assign values to inputs s.t. circuit output is 1?

Obvious result: CNFSAT \leq_K CSAT Instance map: Identity.

If R is a special case of Q, then $R \leq_K Q$ with instance map = identity.

Input: Circuit in conjunctive normal form:

- \triangleright z_1, \ldots, z_n : Circuit inputs.
- ▶ There are *m* OR gates, each with *k* inputs. Circuit inputs or their complements are connected to inputs of OR gates.
- Outputs of OR gates are connected to an AND gate.
- z : Output of AND gate, also the output of overall circuit.

Output: Is it possible to assign values to inputs s.t. circuit output is 1?

Obvious result: CNFSAT $\leq_{\mathcal{K}}$ CSAT Instance map: Identity.

If R is a special case of Q, then $R \leq_K Q$ with instance map = identity.

Note: CNFSAT input can also be given as a formula in propositional logic: conjunction of "clauses", where clause = disjunctions of variables or their negations.

$CSAT \leq_K CNFSAT$

$CSAT \leq_{\kappa} CNFSAT$

Theorem: Every combinational circuit can be expressed as a CNF circuit.

Theorem: Every combinational circuit can be expressed as a CNF circuit.

False start for reduction:

IM(C){ Return CNF circuit C' equivalent to C.}

C' can be exponentially large! Instance map may not run in polytime.

Theorem: Every combinational circuit can be expressed as a CNF circuit.

False start for reduction:

IM(C){ Return CNF circuit C' equivalent to C.}

C' can be exponentially large! Instance map may not run in polytime.

Observation: C is satisfiable if there exist values for the inputs z_1, \ldots, z_n , internal wires y_1, \ldots, y_m , and output z of C s.t.

Theorem: Every combinational circuit can be expressed as a CNF circuit.

False start for reduction:

IM(C){ Return CNF circuit C' equivalent to C.}

C' can be exponentially large! Instance map may not run in polytime.

Observation: C is satisfiable if there exist values for the inputs z_1, \ldots, z_n , internal wires y_1, \ldots, y_m , and output z of C s.t.

1. Output takes value 1.

Theorem: Every combinational circuit can be expressed as a CNF circuit.

False start for reduction:

IM(C){ Return CNF circuit C' equivalent to C.}

C' can be exponentially large! Instance map may not run in polytime.

Observation: C is satisfiable if there exist values for the inputs z_1, \ldots, z_n , internal wires y_1, \ldots, y_m , and output z of C s.t.

- 1. Output takes value 1.
- 2. The values at the inputs and output of each gate in *C* are consistent with the function of the gate.

Theorem: Every combinational circuit can be expressed as a CNF circuit.

False start for reduction:

IM(C){ Return CNF circuit C' equivalent to C.}

C' can be exponentially large! Instance map may not run in polytime.

Observation: C is satisfiable if there exist values for the inputs z_1, \ldots, z_n , internal wires y_1, \ldots, y_m , and output z of C s.t.

- 1. Output takes value 1.
- 2. The values at the inputs and output of each gate in *C* are consistent with the function of the gate.

Reduction idea: C' will take as input the values of $z_1, \ldots, z_n, y_1, \ldots, y_m, z$ and *check* if these are consistent with 1-2.

Theorem: Every combinational circuit can be expressed as a CNF circuit.

False start for reduction:

IM(C){ Return CNF circuit C' equivalent to C.}

C' can be exponentially large! Instance map may not run in polytime.

Observation: C is satisfiable if there exist values for the inputs z_1, \ldots, z_n , internal wires y_1, \ldots, y_m , and output z of C s.t.

- 1. Output takes value 1.
- 2. The values at the inputs and output of each gate in *C* are consistent with the function of the gate.

Reduction idea: C' will take as input the values of $z_1, \ldots, z_n, y_1, \ldots, y_m, z$ and *check* if these are consistent with 1-2.

C is satisfiable

Theorem: Every combinational circuit can be expressed as a CNF circuit.

False start for reduction:

IM(C){ Return CNF circuit C' equivalent to C.}

C' can be exponentially large! Instance map may not run in polytime.

Observation: C is satisfiable if there exist values for the inputs z_1, \ldots, z_n , internal wires y_1, \ldots, y_m , and output z of C s.t.

- 1. Output takes value 1.
- 2. The values at the inputs and output of each gate in *C* are consistent with the function of the gate.

Reduction idea: C' will take as input the values of $z_1, \ldots, z_n, y_1, \ldots, y_m, z$ and *check* if these are consistent with 1-2.

C is satisfiable

 \Leftrightarrow Values exist for $z_1, \ldots, z_n, y_1, \ldots, y_m, z$ satisfying 1-2.

Theorem: Every combinational circuit can be expressed as a CNF circuit.

False start for reduction:

IM(C){ Return CNF circuit C' equivalent to C.}

C' can be exponentially large! Instance map may not run in polytime.

Observation: C is satisfiable if there exist values for the inputs z_1, \ldots, z_n , internal wires y_1, \ldots, y_m , and output z of C s.t.

- 1. Output takes value 1.
- 2. The values at the inputs and output of each gate in *C* are consistent with the function of the gate.

Reduction idea: C' will take as input the values of $z_1, \ldots, z_n, y_1, \ldots, y_m, z$ and *check* if these are consistent with 1-2.

C is satisfiable

- \Leftrightarrow Values exist for $z_1, \ldots, z_n, y_1, \ldots, y_m, z$ satisfying 1-2.
- $\Leftrightarrow C'$ is satisfiable



The reduction

 $IM(General\ combinational\ circuit\ C)\{$

IM(General combinational circuit C){ $C^* = Circuit equivalent to C$, having only 2 input gates.

```
\begin{split} & \text{IM}(\text{General combinational circuit C}) \{ \\ & C^* = \text{Circuit equivalent to } C \text{, having only 2 input gates.} \\ & \qquad \qquad \text{Can be generated in polytime.} \end{split}
```

 $IM(General\ combinational\ circuit\ C)\{$ $C^* = Circuit\ equivalent\ to\ C,\ having\ only\ 2\ input\ gates.$ Can be generated in polytime. Inputs to C': Inputs, wires and output of C^* .

```
IM(General combinational circuit C){ C^* = \text{Circuit equivalent to } C\text{, having only 2 input gates.} Can be generated in polytime. Inputs to C': Inputs, wires and output of C^*. Clauses in C': \{z\} \cup L_1 \cup L_2 \cup \ldots \cup L_k \text{ where } z = \text{output of } C^*
```

 L_i asserts that Gate i in C^* "works correctly" $\cup \{z\}$

```
IM(General combinational circuit C){
C^* = \text{Circuit equivalent to } C, having only 2 input gates.
                                        Can be generated in polytime.
Inputs to C': Inputs, wires and output of C^*.
Clauses in C': \{z\} \cup L_1 \cup L_2 \cup ... \cup L_k where
z = output of C^*
L_i asserts that Gate i in C^* "works correctly" \cup \{z\}
L_i for AND gate with inputs u, v output w:
w = uv
```

```
IM(General combinational circuit C){
C^* = \text{Circuit equivalent to } C, having only 2 input gates.
                                          Can be generated in polytime.
Inputs to C': Inputs, wires and output of C^*.
Clauses in C': \{z\} \cup L_1 \cup L_2 \cup ... \cup L_k where
z = output of C^*
L_i asserts that Gate i in C^* "works correctly" \cup \{z\}
L_i for AND gate with inputs u, v output w:
w = uv \equiv (w \rightarrow uv)(uv \rightarrow w)
```

```
IM(General combinational circuit C){
C^* = \text{Circuit equivalent to } C, having only 2 input gates.
                                          Can be generated in polytime.
Inputs to C': Inputs, wires and output of C^*.
Clauses in C': \{z\} \cup L_1 \cup L_2 \cup ... \cup L_k where
z = output of C^*
L_i asserts that Gate i in C^* "works correctly" \cup \{z\}
L_i for AND gate with inputs u, v output w:
w = uv \equiv (w \rightarrow uv)(uv \rightarrow w) \equiv (uv + w')((uv)' + w)
```

```
IM(General combinational circuit C){
C^* = \text{Circuit equivalent to } C, having only 2 input gates.
                                          Can be generated in polytime.
Inputs to C': Inputs, wires and output of C^*.
Clauses in C': \{z\} \cup L_1 \cup L_2 \cup ... \cup L_k where
z = \text{output of } C^*
L_i asserts that Gate i in C^* "works correctly" \cup \{z\}
L_i for AND gate with inputs u, v output w:
w = uv \equiv (w \rightarrow uv)(uv \rightarrow w) \equiv (uv + w')((uv)' + w)
\equiv (u + w')(v + w')(u' + v' + w)
                                                              Clause set L:
```

```
IM(General combinational circuit C){
C^* = \text{Circuit equivalent to } C, having only 2 input gates.
                                          Can be generated in polytime.
Inputs to C': Inputs, wires and output of C^*.
Clauses in C': \{z\} \cup L_1 \cup L_2 \cup ... \cup L_k where
z = \text{output of } C^*
L_i asserts that Gate i in C^* "works correctly" \cup \{z\}
L_i for AND gate with inputs u, v output w:
w = uv \equiv (w \rightarrow uv)(uv \rightarrow w) \equiv (uv + w')((uv)' + w)
\equiv (u + w')(v + w')(u' + v' + w)
                                                              Clause set L:
L_i for OR, NOT gates: similar.
```

▶ *IM* runs in time polynomial in size of *C*.

- ▶ *IM* runs in time polynomial in size of *C*.
- C is satisfiable:
 - $\Rightarrow \exists$ input values z_1, \ldots, z_n for C that make its output z = 1.

- ▶ *IM* runs in time polynomial in size of *C*.
- C is satisfiable:
 - $\Rightarrow \exists$ input values z_1, \dots, z_n for C that make its output z = 1. $y_1, \dots, y_k =$ values in internal wires of C^* for input z_1, \dots, z_n

- ▶ *IM* runs in time polynomial in size of *C*.
- C is satisfiable:
 - $\Rightarrow \exists$ input values z_1, \ldots, z_n for C that make its output z=1. $y_1, \ldots, y_k=$ values in internal wires of C^* for input z_1, \ldots, z_n C' will produce 1, when fed $z_1, \ldots, z_n, y_1, \ldots, y_k, z$.

- ▶ *IM* runs in time polynomial in size of *C*.
- C is satisfiable:
 - $\Rightarrow \exists$ input values z_1, \ldots, z_n for C that make its output z = 1. $y_1, \ldots, y_k =$ values in internal wires of C^* for input z_1, \ldots, z_n C' will produce 1, when fed $z_1, \ldots, z_n, y_1, \ldots, y_k, z$.
 - \Rightarrow C' is satisfiable.

- ▶ *IM* runs in time polynomial in size of *C*.
- C is satisfiable:
 - $\Rightarrow \exists$ input values z_1, \ldots, z_n for C that make its output z=1. $y_1, \ldots, y_k=$ values in internal wires of C^* for input z_1, \ldots, z_n C' will produce 1, when fed $z_1, \ldots, z_n, y_1, \ldots, y_k, z$.
 - \Rightarrow C' is satisfiable.
- ► C' is satisfiable:
 - $\Rightarrow \exists z_1, \dots, z_n, y_1, \dots, y_k, z$ input values that produce 1 at output.

- ▶ *IM* runs in time polynomial in size of *C*.
- C is satisfiable:
 - $\Rightarrow \exists$ input values z_1, \ldots, z_n for C that make its output z=1. $y_1, \ldots, y_k=$ values in internal wires of C^* for input z_1, \ldots, z_n C' will produce 1, when fed $z_1, \ldots, z_n, y_1, \ldots, y_k, z$.
 - \Rightarrow C' is satisfiable.
- ► C' is satisfiable:
 - $\Rightarrow \exists z_1, \dots, z_n, y_1, \dots, y_k, z$ input values that produce 1 at output.

Feeding z_1, \ldots, z_n to C^* will produce 1. Also to C.

- ▶ *IM* runs in time polynomial in size of *C*.
- C is satisfiable:
 - $\Rightarrow \exists$ input values z_1, \ldots, z_n for C that make its output z = 1. $y_1, \ldots, y_k =$ values in internal wires of C^* for input z_1, \ldots, z_n C' will produce 1, when fed $z_1, \ldots, z_n, y_1, \ldots, y_k, z$.
 - \Rightarrow C' is satisfiable.
- ▶ C' is satisfiable:
 - $\Rightarrow \exists z_1, \dots, z_n, y_1, \dots, y_k, z$ input values that produce 1 at output.
 - Feeding z_1, \ldots, z_n to C^* will produce 1. Also to C.
 - \Rightarrow C is satisfiable.

- ▶ *IM* runs in time polynomial in size of *C*.
- C is satisfiable:
 - $\Rightarrow \exists$ input values z_1, \ldots, z_n for C that make its output z = 1. $y_1, \ldots, y_k =$ values in internal wires of C^* for input z_1, \ldots, z_n C' will produce 1, when fed $z_1, \ldots, z_n, y_1, \ldots, y_k, z$.
 - \Rightarrow C' is satisfiable.
- C' is satisfiable:
 - $\Rightarrow \exists z_1, \dots, z_n, y_1, \dots, y_k, z$ input values that produce 1 at output.

Feeding z_1, \ldots, z_n to C^* will produce 1. Also to C.

 \Rightarrow C is satisfiable.

Remark: Each clause in C' in the above reduction has at most 3 variables/negations. Can get exactly 3 with slight modification.

- ▶ *IM* runs in time polynomial in size of *C*.
- C is satisfiable:
 - $\Rightarrow \exists$ input values z_1,\ldots,z_n for C that make its output z=1. $y_1,\ldots,y_k=$ values in internal wires of C^* for input z_1,\ldots,z_n C' will produce 1, when fed $z_1,\ldots,z_n,y_1,\ldots,y_k,z$.
 - \Rightarrow C' is satisfiable.
- C' is satisfiable:
 - $\Rightarrow \exists z_1, \dots, z_n, y_1, \dots, y_k, z$ input values that produce 1 at output.

Feeding z_1, \ldots, z_n to C^* will produce 1. Also to C.

 \Rightarrow C is satisfiable.

Remark: Each clause in C' in the above reduction has at most 3 variables/negations. Can get exactly 3 with slight modification.

3CNFSAT or 3SAT: Satisfiablilty when each clause has exactly 3 variables or their negations.



- ▶ *IM* runs in time polynomial in size of *C*.
- C is satisfiable:
 - $\Rightarrow \exists$ input values z_1, \ldots, z_n for C that make its output z = 1. $y_1, \ldots, y_k =$ values in internal wires of C^* for input z_1, \ldots, z_n C' will produce 1, when fed $z_1, \ldots, z_n, y_1, \ldots, y_k, z$.
 - \Rightarrow C' is satisfiable.
- C' is satisfiable:
 - $\Rightarrow \exists z_1, \dots, z_n, y_1, \dots, y_k, z$ input values that produce 1 at output.
 - Feeding z_1, \ldots, z_n to C^* will produce 1. Also to C.
 - \Rightarrow C is satisfiable.

Remark: Each clause in C' in the above reduction has at most 3 variables/negations. Can get exactly 3 with slight modification.

3CNFSAT or 3SAT: Satisfiablilty when each clause has exactly 3 variables or their negations.

Observation: Let z be a single literal that appears in a clause in some formula. Let α be a variable that does not appear in the formula. Then z can be replaced by $(z+\alpha)(z+\alpha')$ in the formula without changing its value.

Observation: Let z be a single literal that appears in a clause in some formula. Let α be a variable that does not appear in the formula. Then z can be replaced by $(z + \alpha)(z + \alpha')$ in the formula without changing its value.

Proof:
$$(z + \alpha)(z + \alpha')$$

Observation: Let z be a single literal that appears in a clause in some formula. Let α be a variable that does not appear in the formula. Then z can be replaced by $(z+\alpha)(z+\alpha')$ in the formula without changing its value.

Proof:
$$(z + \alpha)(z + \alpha') = z + z(\alpha + \alpha') + \alpha\alpha'$$

Observation: Let z be a single literal that appears in a clause in some formula. Let α be a variable that does not appear in the formula. Then z can be replaced by $(z+\alpha)(z+\alpha')$ in the formula without changing its value.

Proof:
$$(z + \alpha)(z + \alpha') = z + z(\alpha + \alpha') + \alpha\alpha' = z$$

Observation: Let z be a single literal that appears in a clause in some formula. Let α be a variable that does not appear in the formula. Then z can be replaced by $(z+\alpha)(z+\alpha')$ in the formula without changing its value.

Proof:
$$(z + \alpha)(z + \alpha') = z + z(\alpha + \alpha') + \alpha\alpha' = z$$

Thus we can convert clauses with fewer than 2 terms to clauses with 3 terms by adding a few new variables which do not affect the value of the CNF formula.

1. Let C denote a circuit consisting of just a single 4 input AND gate. Derive the circuite C' as obtained in the reduction. Add dummy variables as needed to make this a 3CNF formula.

- Let C denote a circuit consisting of just a single 4 input AND gate. Derive the circuite C' as obtained in the reduction. Add dummy variables as needed to make this a 3CNF formula.
- 2. Derive a clause set that asserts that the output of a 2 input OR gate is indeed the OR of the inputs.

- Let C denote a circuit consisting of just a single 4 input AND gate. Derive the circuite C' as obtained in the reduction. Add dummy variables as needed to make this a 3CNF formula.
- 2. Derive a clause set that asserts that the output of a 2 input OR gate is indeed the OR of the inputs.
- 3. Show that $3SAT \leq_K CNFSAT$.

- Let C denote a circuit consisting of just a single 4 input AND gate. Derive the circuite C' as obtained in the reduction. Add dummy variables as needed to make this a 3CNF formula.
- 2. Derive a clause set that asserts that the output of a 2 input OR gate is indeed the OR of the inputs.
- 3. Show that $3SAT \leq_K CNFSAT$.

Remark: 3CNFSAT or 3SAT is a special case of CNFSAT or 3CNFSAT.

- Let C denote a circuit consisting of just a single 4 input AND gate. Derive the circuite C' as obtained in the reduction. Add dummy variables as needed to make this a 3CNF formula.
- 2. Derive a clause set that asserts that the output of a 2 input OR gate is indeed the OR of the inputs.
- 3. Show that $3SAT \leq_K CNFSAT$.

Remark: 3CNFSAT or 3SAT is a special case of CNFSAT or 3CNFSAT.

So reducing to 3SAT is harder than reducing to SAT.

- 1. Let C denote a circuit consisting of just a single 4 input AND gate. Derive the circuite C' as obtained in the reduction. Add dummy variables as needed to make this a 3CNF formula.
- 2. Derive a clause set that asserts that the output of a 2 input OR gate is indeed the OR of the inputs.
- 3. Show that $3SAT \leq_K CNFSAT$.

Remark: 3CNFSAT or 3SAT is a special case of CNFSAT or 3CNFSAT.

So reducing to 3SAT is harder than reducing to SAT. But reducing from 3SAT is easier than reducing from SAT.

- 1. Let C denote a circuit consisting of just a single 4 input AND gate. Derive the circuite C' as obtained in the reduction. Add dummy variables as needed to make this a 3CNF formula.
- 2. Derive a clause set that asserts that the output of a 2 input OR gate is indeed the OR of the inputs.
- 3. Show that $3SAT \leq_K CNFSAT$.

Remark: 3CNFSAT or 3SAT is a special case of CNFSAT or 3CNFSAT.

So reducing to 3SAT is harder than reducing to SAT. But reducing from 3SAT is easier than reducing from SAT. Hence we wanted to prove $CSAT \leq_K 3SAT$ rather than $CSAT <_K SAT$.

$3SAT \leq_K IS$

3SAT(3CNF formula C) : Is C satisfiable

C = conjunction of clauses

IS(G,k): Does G have an IS of size k?

3SAT(3CNF formula C) : Is C satisfiable

C = conjunction of clauses

IS(G,k): Does G have an IS of size k?

The questions in the two problems:

3SAT(3CNF formula C) : Is C satisfiable

C = conjunction of clauses

IS(G,k): Does G have an IS of size k?

The questions in the two problems:

3SAT: Should a literal be set to true?

3SAT(3CNF formula C) : Is C satisfiable

C = conjunction of clauses

IS(G,k): Does G have an IS of size k?

The questions in the two problems:

3SAT: Should a literal be set to true? IS: Should a vertex be put in the IS?

3SAT(3CNF formula C) : Is C satisfiable

C = conjunction of clauses

IS(G,k): Does G have an IS of size k?

The questions in the two problems:

3SAT: Should a literal be set to true? IS: Should a vertex be put in the IS?

Constraints:

3SAT(3CNF formula C) : Is C satisfiable

C = conjunction of clauses

IS(G,k): Does G have an IS of size k?

The questions in the two problems:

3SAT: Should a literal be set to true? IS: Should a vertex be put in the IS?

Constraints:

3SAT: A literal and its complement should both not be true.

3SAT(3CNF formula C) : Is C satisfiable

C = conjunction of clauses

IS(G,k): Does G have an IS of size k?

The questions in the two problems:

3SAT: Should a literal be set to true? IS: Should a vertex be put in the IS?

Constraints:

3SAT: A literal and its complement should both not be true.

3SAT: At least one literal in a clause should be set true.

3SAT(3CNF formula C) : Is C satisfiable

C = conjunction of clauses

IS(G,k): Does G have an IS of size k?

The questions in the two problems:

3SAT: Should a literal be set to true? IS: Should a vertex be put in the IS?

Constraints:

3SAT: A literal and its complement should both not be true.

3SAT: At least one literal in a clause should be set true.

IS: Vertices connected by an edge should both not be selected.

3SAT(3CNF formula C) : Is C satisfiable

C = conjunction of clauses

IS(G,k): Does G have an IS of size k?

The questions in the two problems:

3SAT: Should a literal be set to true? IS: Should a vertex be put in the IS?

Constraints:

3SAT: A literal and its complement should both not be true.

3SAT: At least one literal in a clause should be set true.

IS: Vertices connected by an edge should both not be selected.

At least *k* vertices must be selected.

3SAT(3CNF formula C) : Is C satisfiable

C = conjunction of clauses

IS(G,k): Does G have an IS of size k?

The questions in the two problems:

3SAT: Should a literal be set to true? IS: Should a vertex be put in the IS?

Constraints:

3SAT: A literal and its complement should both not be true.

3SAT: At least one literal in a clause should be set true.

IS: Vertices connected by an edge should both not be selected.

At least *k* vertices must be selected.

Ideas for reduction:

3SAT(3CNF formula C) : Is C satisfiable

C = conjunction of clauses

IS(G,k): Does G have an IS of size k?

The questions in the two problems:

3SAT: Should a literal be set to true? IS: Should a vertex be put in the IS?

Constraints:

3SAT: A literal and its complement should both not be true.

3SAT: At least one literal in a clause should be set true.

IS: Vertices connected by an edge should both not be selected.

At least *k* vertices must be selected.

Ideas for reduction:

Each literal should be represented by a vertex?



3SAT(3CNF formula C) : Is C satisfiable

C = conjunction of clauses

IS(G,k): Does G have an IS of size k?

The questions in the two problems:

3SAT: Should a literal be set to true? IS: Should a vertex be put in the IS?

Constraints:

3SAT: A literal and its complement should both not be true.

3SAT: At least one literal in a clause should be set true.

IS: Vertices connected by an edge should both not be selected.

At least *k* vertices must be selected.

Ideas for reduction:

Each literal should be represented by a vertex?

The vertices corresponding to a literal and its complement should be connected by an edge?

4□ > 4個 > 4 量 > 4 量 > 1 量 9 Q (P)

IM(CNF formula C){

IM(CNF formula C){ Let L_i be the clauses.

IM(CNF formula C){ Let L_i be the clauses. Create vertex for each literal in the formula.

IM(CNF formula C){ Let L_i be the clauses.

Create vertex for each literal in the formula.

Connect all vertices corresponding to a clause by edges.

IM(CNF formula C){ Let L_i be the clauses. Create vertex for each literal in the formula. Connect all vertices corresponding to a clause by edges.

If u appears in one clause, and u' in another, connect corresponding vertices by a edge.

```
IM(CNF formula C){
Let L_i be the clauses.
Create vertex for each literal in the formula.
Connect all vertices corresponding to a clause by edges.
```

If u appears in one clause, and u' in another, connect corresponding vertices by a edge.

```
k = \text{number of clauses.}
Return Graph constructed, k \}
```

```
IM(CNF formula C){
Let L_i be the clauses.
Create vertex for each literal in the formula.
Connect all vertices corresponding to a clause by edges.
```

If u appears in one clause, and u' in another, connect corresponding vertices by a edge.

```
k = number of clauses.
Return Graph constructed, k
```

▶ *IM* runs in time polynomial in formula size.

IM(CNF formula C){ Let L_i be the clauses. Create vertex for each literal in the formula. Connect all vertices corresponding to a clause by edges.

If u appears in one clause, and u' in another, connect corresponding vertices by a edge.

k = number of clauses. Return Graph constructed, k

- ▶ *IM* runs in time polynomial in formula size.
- lacktriangle Formula C is satisfiable \Rightarrow every clause contains a true literal

IM(CNF formula C){ Let L_i be the clauses. Create vertex for each literal in the formula. Connect all vertices corresponding to a clause by edges.

If u appears in one clause, and u' in another, connect corresponding vertices by a edge.

k = number of clauses. Return Graph constructed, k

- ▶ *IM* runs in time polynomial in formula size.
- lackbox Formula C is satisfiable \Rightarrow every clause contains a true literal
 - \Rightarrow Corresponding vertices form an IS of size k

IM(CNF formula C){ Let L_i be the clauses.

Create vertex for each literal in the formula.

Connect all vertices corresponding to a clause by edges.

If u appears in one clause, and u' in another, connect corresponding vertices by a edge.

k = number of clauses.

- ▶ *IM* runs in time polynomial in formula size.
- Formula C is satisfiable ⇒ every clause contains a true literal
 ⇒ Corresponding vertices form an IS of size k
- ▶ IS(G,k)=true \Rightarrow IS of k vertices present.

IM(CNF formula C){ Let L_i be the clauses.

Create vertex for each literal in the formula.

Connect all vertices corresponding to a clause by edges.

If u appears in one clause, and u' in another, connect corresponding vertices by a edge.

k = number of clauses.

- ▶ *IM* runs in time polynomial in formula size.
- Formula C is satisfiable ⇒ every clause contains a true literal
 ⇒ Corresponding vertices form an IS of size k
- ► IS(G,k)=true ⇒ IS of k vertices present.
 Each vertex must come from different clause.

IM(CNF formula C){ Let L_i be the clauses.

Create vertex for each literal in the formula.

Connect all vertices corresponding to a clause by edges.

If u appears in one clause, and u' in another, connect corresponding vertices by a edge.

k = number of clauses.

- ▶ *IM* runs in time polynomial in formula size.
- Formula C is satisfiable ⇒ every clause contains a true literal
 ⇒ Corresponding vertices form an IS of size k
- ▶ IS(G,k)=true \Rightarrow IS of k vertices present. Each vertex must come from different clause. Vertex for $u \in IS \Rightarrow$ Vertex for $u' \notin IS$

IM(CNF formula C){ Let L_i be the clauses.

Create vertex for each literal in the formula.

Connect all vertices corresponding to a clause by edges.

If u appears in one clause, and u' in another, connect corresponding vertices by a edge.

k = number of clauses.

- ▶ *IM* runs in time polynomial in formula size.
- Formula C is satisfiable ⇒ every clause contains a true literal
 ⇒ Corresponding vertices form an IS of size k
- ▶ IS(G,k)=true \Rightarrow IS of k vertices present. Each vertex must come from different clause. Vertex for $u \in IS \Rightarrow$ Vertex for $u' \notin IS$ Set all literals associated with IS vertices true.

IM(CNF formula C){ Let L_i be the clauses.

Create vertex for each literal in the formula.

Connect all vertices corresponding to a clause by edges.

If u appears in one clause, and u' in another, connect corresponding vertices by a edge.

k = number of clauses.

Return Graph constructed, k

- ► *IM* runs in time polynomial in formula size.
- Formula C is satisfiable ⇒ every clause contains a true literal
 ⇒ Corresponding vertices form an IS of size k
- ▶ IS(G,k)=true \Rightarrow IS of k vertices present. Each vertex must come from different clause. Vertex for $u \in IS \Rightarrow$ Vertex for $u' \notin IS$ Set all literals associated with IS vertices true.

If this does not set truth of both u, u' for some u, set u=true.



IM(CNF formula C){ Let L_i be the clauses.

Create vertex for each literal in the formula.

Connect all vertices corresponding to a clause by edges.

If u appears in one clause, and u' in another, connect corresponding vertices by a edge.

k = number of clauses.

Return Graph constructed, k

- ▶ *IM* runs in time polynomial in formula size.
- Formula C is satisfiable ⇒ every clause contains a true literal
 ⇒ Corresponding vertices form an IS of size k
- ► IS(G,k)=true \Rightarrow IS of k vertices present. Each vertex must come from different clause. Vertex for $u \in IS \Rightarrow$ Vertex for $u' \notin IS$ Set all literals associated with IS vertices true. If this does not set truth of both u, u' for some

If this does not set truth of both u, u' for some u, set u=true.



