Shreyansh Shah

[CodeChef](#) / [Github](#)

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EDUCATION

- **School of Engineering and Applied Science, Ahmedabad University**   July 2018 – Present
  Bachelor of Technology in Information and Communication Technology   *CGPA: 2.83/4*

- **Gujarat Secondary and Higher Secondary Education Board**
  Higher Secondary Certificate Examination; 93.05 PR   *March 2018*
  Secondary Certificate Examination; 99.80 PR   *March 2016*

WORK EXPERIENCE

- **Undergraduate Research Programme, Ahmedabad University -** *Researcher*   *February 2020 – Present*
  Selected and currently working on 'Mathematical modeling for 5G-NR and Wi-Fi 6' under Prof Dhaval Patel, funded by DST-UKIERI and University of Washington - Seattle - USA. Reproduced several papers from top-quality journals and conferences like IEEE Transactions on communications, IEEE ICC, IEEE VTC.

- **SAFARI INFOSOFT -** *Web Developer Intern*   *May 2020 – August 2020*
  Worked as a front end developer intern and designed several web pages using JavaScript and React. Created modular, responsive templates using CSS techniques and JavaScript.

KEY PROJECTS

- **Speech Enhancement** - [Github](#)   *Team of 4, Monsoon 2019*
  Removes noise from Recorded audio file and improves quality of audio. Implemented in matlab using wiener filter algorithm.

- **Implementation of Various Shortest Path Algorithm** - [Github](#)   *Team of 3, Monsoon 2019*
  Project Will find Shortest Path based on User's Given Starting Point And Ending Point And will also display route on map with different landmarks coming in between starting point and ending point

- **Online Shopping System - Database design and Implementation** -   *Team of 3, Winter 2020*
  Back-end and gui implemented in JAVA with Oracle database. Key functionalities: Different Access for admin/seller and customers, Invoice generator, Details of past orders etc.

- **MIPS 16 bit Micro Processor with booth multiplication implemented** -   *Team of 4, Monsoon 2019*
  RISC-based 28 instructions MIPS processor with hazard handling. Dataflow modelled verilog code was dumped on FPGA.

PROGRAMMING SKILLS

- **Languages**: HTML, CSS, JavaScript, Python, Java, C/C++, Verilog, MATLAB, MySQL, Oracle
- **Technologies/Frameworks**: Django, NumPy, SciPy, Pandas, Matplotlib, Bootstrap, Git, LaTeX

ACHIEVEMENTS

- **Scholarships**: Tuition fee waiver at Ahmedabad University, MYSY scholarship for 80+ PR in GSHSEB.
- **JEE-Advanced (formerly IIT-JEE) 2018**: Declared qualified.

EXTRA CURRICULAR

- **Position of Responsibility**: Core Committee Member of Environment Club-Ahmedabad University, IEEE Women In Engineering(WIE) Vice Chair
- **Competitive Programming**: Solved over 100 problems across platforms on trees, graphs, recursion, bit manipulation etc.