

| | Keil software. |
|---|--|
| | |
| | Project -> uvision |
| | |
| | select device - 35T89 x 516 RD2 |
| | b |
| | new file -> save leell.c |
| | |
| | source urop (right dick) |
| | 1 |
| | add existing file. (c wase select |
| | (c wale select |
| | * |
| | target 1 (xight click) |
| | target 1 (xight click) d options |
| | output |
| | create HEX file. |
| | Assessing the second se |
| | Total T |
| | Tanget -> built target. |
| | (right) |
| | Flash |
| | |
| | options (SST) |
| | 01100 |
| | J 9600 |
| | Device manger com3) Jene Ports (com3) Jame Relet |
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| 1 | , Juin |
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| # | |
| # | pownlod vser Codl |

... -> Hax file select

| 3) Pract. DAC waveform gen. |
|---------------------------------------|
| Debug - start/slop debug 8t |
| fralution ok. |
| logic analysis (Red symbol) |
| 5. 1189 avoi getup - new line |
| (A) (A) (A) Kill'all > 100 A Kill all |
| new line (PI) |
| close. |
| Debug → Rune |

Savitrinai Divida Bussell

| -Open |
|---|
| |
| 1) Open MPLAB IDE |
| 2) Open file > New Project |
| > Microchip embedded > Stand alone |
| Project \ |
| 3> Next |
| 4) Advance 8 bit MCU'S (Families) |
| Device - PIC - 18F450 |
| 5> Simulator - X(8 (V1.30) |
| Next . |
| 6) Live Project name |
| 1) Click Set as main Project |
| 8) Finish |
| 9) Right click on source fire |
| 10) New |
| 11) C source file |
| R) Crive Fire name (Same) |
| 13) lalvite code |
| 14) Right (lick on Project tile 15) (no to the Properties |
| 15) (no to the Properties |
| 16) XC8 linker |
| 17) Option -> Additional Option |
| 18) (ode Offset - 800 |
| 9) Apply > OK |
| 19) Right click on Project title |
| 9) Apply > OK 19) Right (lick on Project title 20) (lean & build 21) Minimize Screen |
| 21) Minimize Screen |

Pic loadet - AN1310 (open app Cao to Program Setting Select comport Click Flosh Program memory I red Reset button, break 11 Stop red Open > hex file > dist > default > Production Write device (on Software