**Modifications -**

* Added **Ifetch** with Load requests for all the states because L1 cache is used both for instruction fetch and load requests.
* Added **Data\_from\_Dir\_Ack\_Cnt\_Last** request to **IM\_AD** and **SM\_AD**. For this request, I write data to cache, update acknowledgement count, deallocate the entry from the TBE, pop the response from local core.