| | TopicDate |
|-------|---|
| | Name: Shreyes Bhat Kera |
| | 30: 2018A7P311198 |
| | |
| Q1. | Samaphoras. |
| | empte fill motes bit- Ailed |
| | (n) |
| | Luitin : empty = 10 halif-filled = 0 antex=1 |
| | Juitin!: empty=10 halif-filled= 0 and tex=1 full=0 2 3 pointers for adding 8 remain |
| 50 | P. do ? |
| () | create half filled them in next P |
| 333 | wait (empty) |
| 33.34 | wait (motex) |
| | Add half finished iten to Suffer |
| | Cismal Cunter) |
| | signal (calffilled) } = 4/2 (1) |
| | D (1) S/HCC |
| | P2 do 3 |
| | wait (half filled) |
| | wait (mutex) |
| | frist half finised item; Soller |
| | signal (unster) |
| | 3 Star Gent School (1) |
| | Ctuat (full) |
| | Is wait (motes) |
| | · versue item from duffer |
| | sosral (motey) |
| | (Unal Compty) & while CD |
| | |

| II To | picDate |
|-------|---|
| | TopicDate |
| | Name: Shreyas Bhat Kera Io: 2018A7951119 |
| 22. | P= 2, Q= 0, R=0, P, =2 |
| | Process P (Process R) do 2 do 2 do 3 |
| 9 | wait (P) wait (Q) wait (R) wait (R) wait (R) |
| | wait (P,) wait (P,) section 7 wait (P,) vigral (P) |
| | Section 7 Section 3 Section 3 article(1) |
| | Signal (P) |
| | signal (P1) signal (P) signal (P1) Swhite (1) |
| | |
| | |
| | |
| | 100000000000000000000000000000000000000 |
| | |