Computer Architecture

Instructions for Lab Test

Lab Test for CA course will be held as per the following details:

Date: 8/11/20

Time: 3.00 PM to 4.15 PM

Syllabus: Complete portion covered in lab sessions. (OPEN BOOK)

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The test will be conducted through google forms. On the day of the test, the google form link will be shared with you through the comment box in google meet. Please login into the google meet on the day of the test at 3.00 PM to receive the link. All students should use the URL of Prof. S. Mohan's class to join google meet and receive the test link (https://meet.google.com/mtx-qjyu-zzw).

The test will consist of a problem which you have to code and test in verilog. It is recommended that you use the first 15 mins to read and understand the problem and next 1 hour to solve the problem. Keep the following important points in mind:

1. During the course of the exam, you can use any no. of files as per your choice.
2. Finally, **only one .v file (no .txt files please!)** whose name is your id no. should be submitted**. Ex. 2016A3PS0275P.v**. In this file the first two lines should have **your name and id no.** Please **comment** them.
3. **Strictly follow the naming convention** as suggested, for the modules and signals.

The single .v file that you have made should be uploaded through google forms. Do not forget to press the submit button. You will be allowed to do submission only once. No submissions would be accepted through email etc.

Important: It is student's responsibility to make the submission well within time. Submission received after this time will be marked late and will be penalized. We recommend that you should stop working on the test by 4.05 PM and use the rest of time only for submission.