Lab 1

ECE 3300 LAB

SECTION 02

Instructor: Mohamed Aly

September 11, 2021
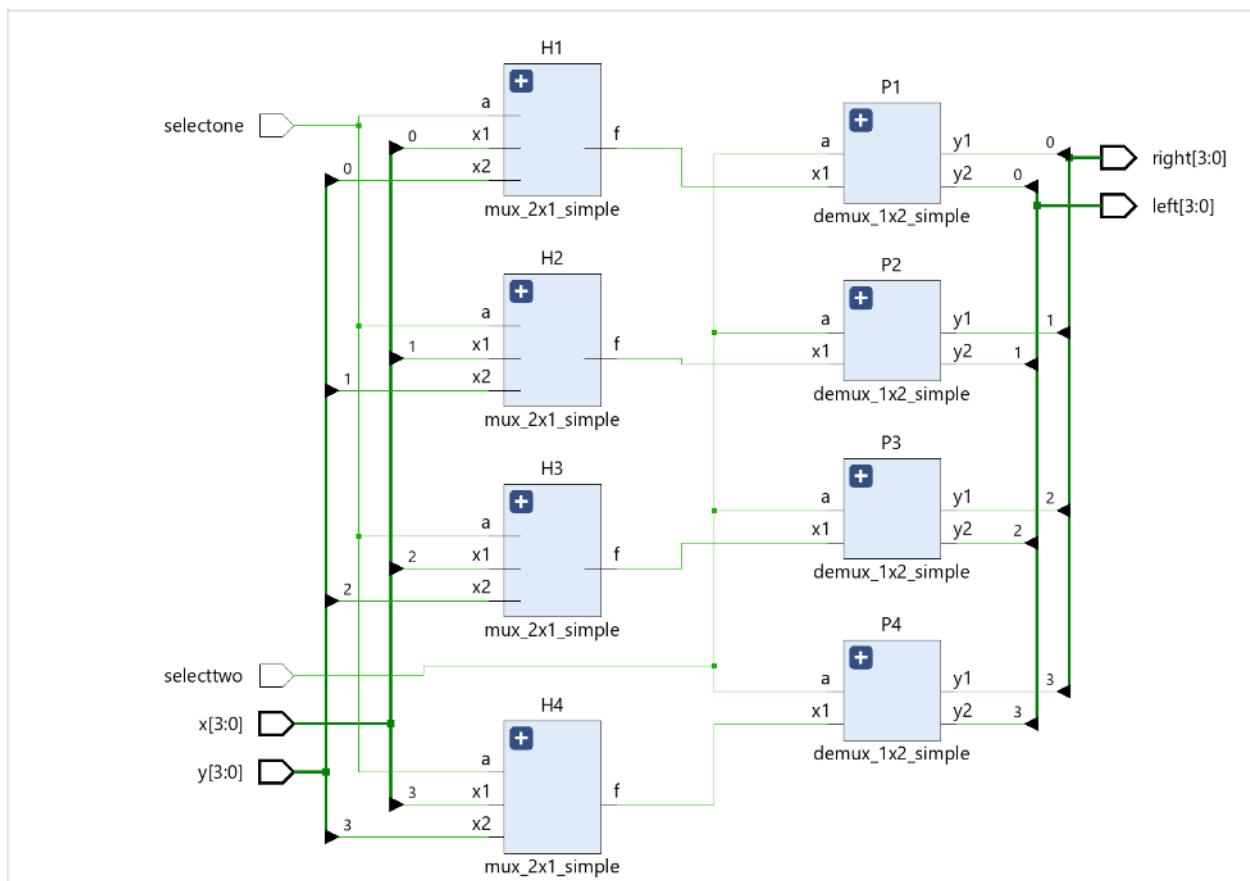
Group I

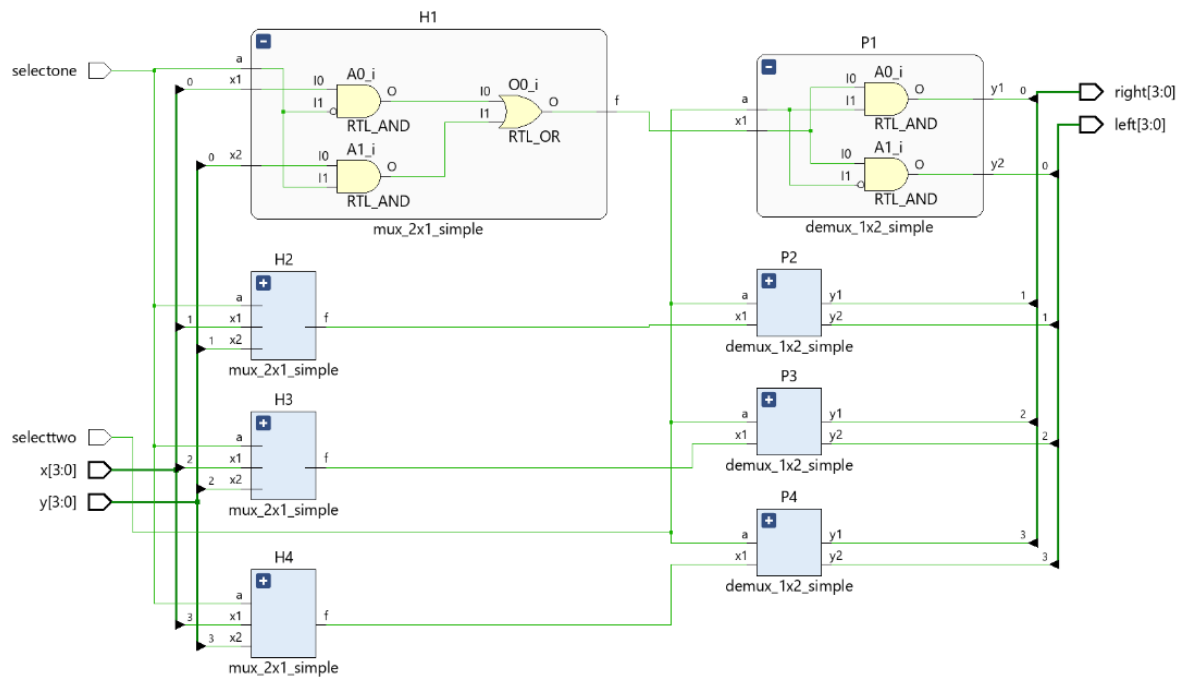| Name	Bronco ID	Email
Jose Soto-Covarrubias	013778700	jls1@cpp.edu
Shreyas Surana	013790140	ssurana@cpp.edu

Abstract

Purpose of the experiment was to introduce the group to FPGA boards by using the programmer, Vivado. Task was to create two 4-bit inputs with two switches. One switch would be called "selectone", which was to choose between input x or input y. The second switch, called "selecttwo" was to flip the LEDS to the right or to the left. Mux and demux was programmed using structural modeling.
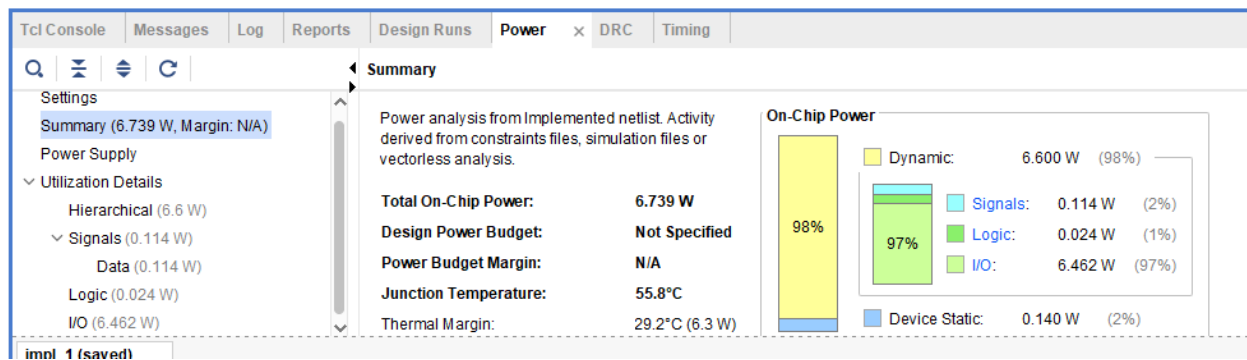
Area/Resources Information

Elaborated Design

Power Usage

Post-Implementation Resource Utilization

Resource	Utilization	Available	Utilization %
LUT	4	63400	0.01
IO	18	210	8.57