Lab 6

ECE 3300 LAB

SECTION 02

Instructor: Mohamed Aly

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Group I

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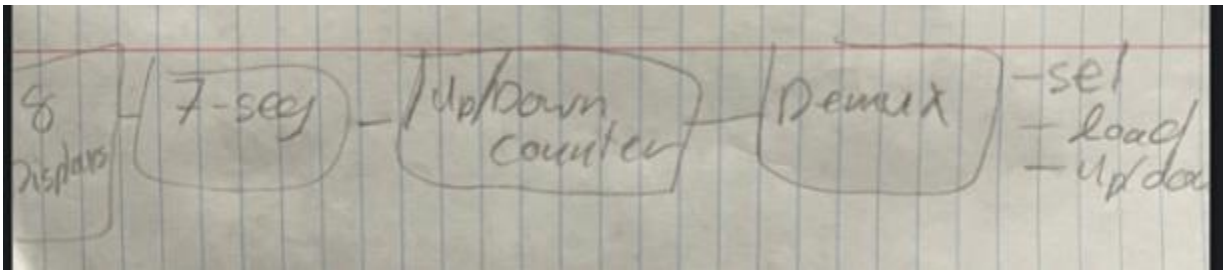
Abstract

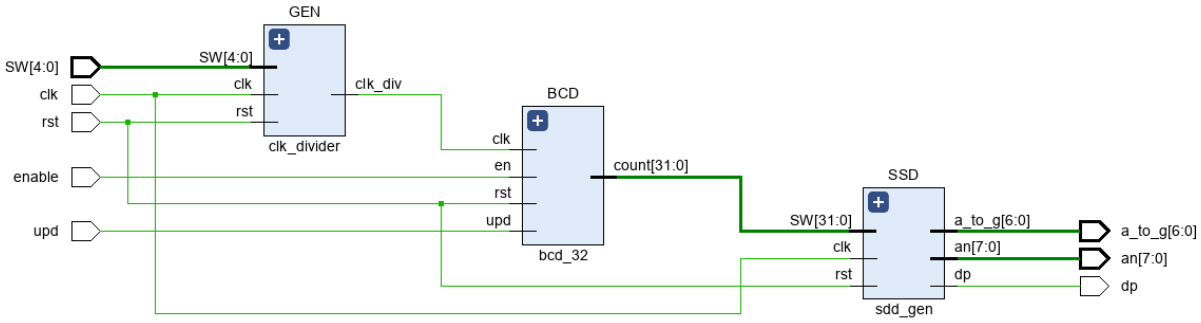
Purpose of the experiment was to reuse previous lab code into a modified version. Every single BCD digit will have an input, which in turn would be taken into a multiplexer connected to four switches .Three bit would be used to control the selections of what would be going inside the injection mode. One button would be the load which will stop and control direction it will be filled in. Unreleasing the load will make the counter 0 and redo the counting depending on the enable.

Theory: Sketch of Design

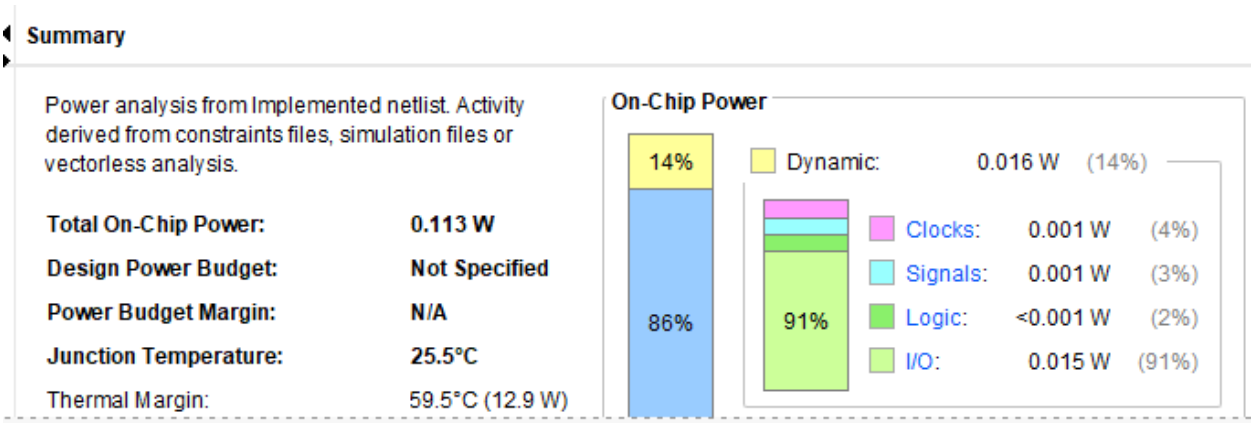


Area/Resources Information

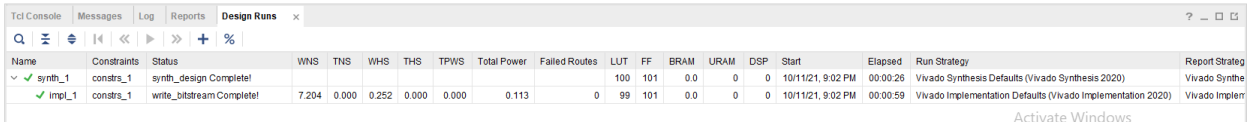
Elaborated Design



Power Usage



Post-Implementation Resource Utilization



Tcl Console | Messages | Log | Reports | Design Runs

Name	Constraints	Status	WNS	TNS	WHS	THS	TPWS	Total Power	Failed Routes	LUT	FF	BRAM	URAM	DSP	Start	Elapsed	Run Strategy	Report Strateg
synth_1	constrs_1	synth_design Complete!								100	101	0.0	0	0	10/11/21, 9:02 PM	00:00:26	Vivado Synthesis Defaults (Vivado Synthesis 2020)	Vivado Synthe
impl_1	constrs_1	write_bitstream Complete!	7.204	0.000	0.252	0.000	0.000	0.113	0	99	101	0.0	0	0	10/11/21, 9:02 PM	00:00:59	Vivado Implementation Defaults (Vivado Implementation 2020)	Vivado Implem