ADICHUNCHANAGIRI UNIVERSITY
Faculty of Engineering Management and Technology
(B G S Institute of Technology)
B G Nagar, Nagamangala Tq, Mandya Dist.

Department of Electronics and Communication Engineering
ARM and EMBEDDED SYSTEMS (22EC62)

Sem	:	6th Semester BE (ECE) A, B, C	ASSIGNMENT - 3 MODULE – 5	Date of Commencement	:	19/05/2025
Staff	:	B S Balaji Sandeep S V Saraswathi J M		Last date for Submission	:	24/05/2025

Question Number	Questions	Cos	Levels
1	What is Hardware and Software co-design? Explain the fundamental design approaches in detail.	CO5	L2
2	Discuss advantages and drawbacks of super loop based firmware design approach.	CO5	L2
3	Explain the assembly language based embedded firmware development with a diagram and mention its advantages and disadvantages.	CO5	L2
4	Differentiate between hard real time system and soft real time system with an example for each.	CO5	L2
5	Define process. Explain in detail the structure, memory organization and state transitions of the process.	CO5	L2

Signature of Staff

Signature of HOD

Dept. of ECE