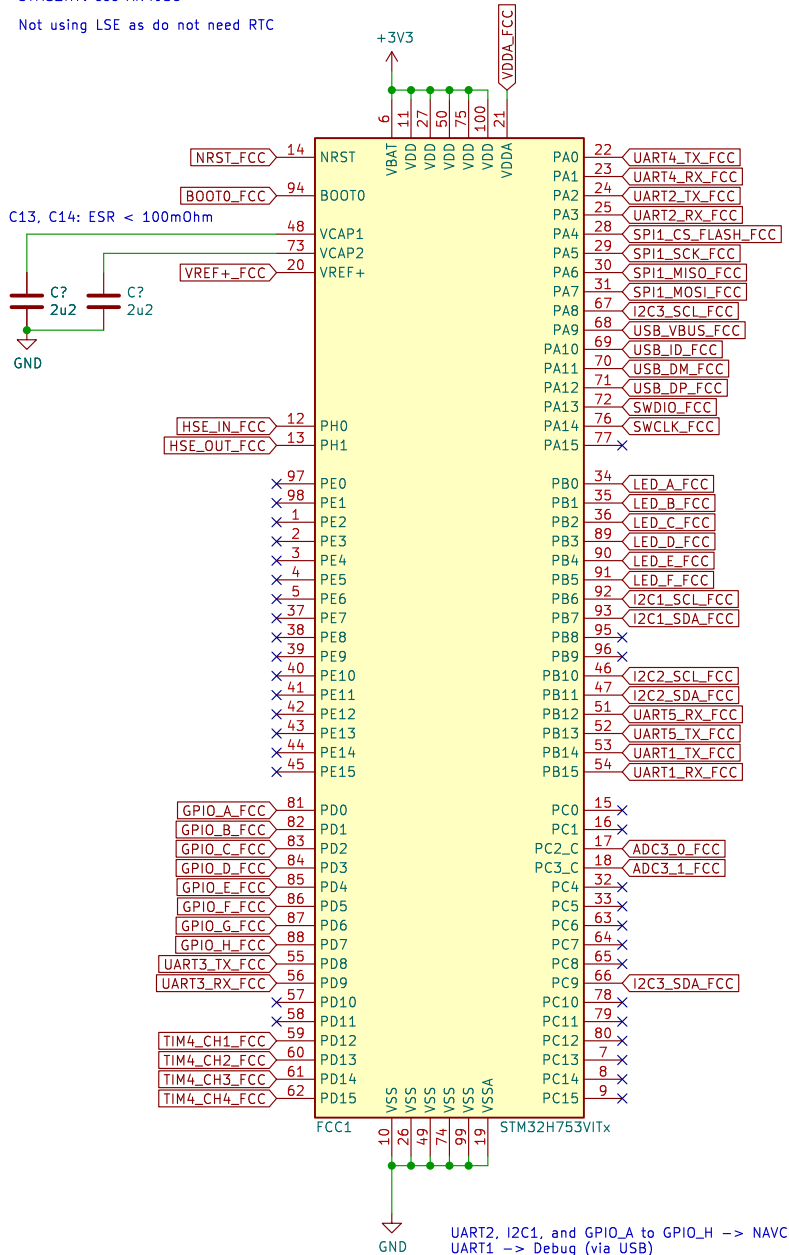


# Flight Control Computer (FCC)

STM32H7: see AN4938

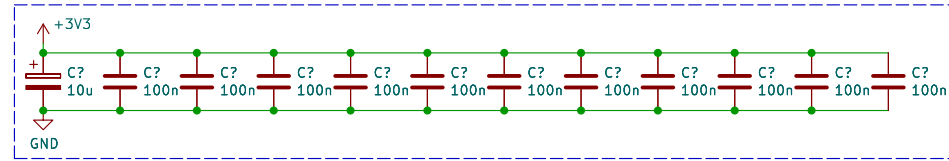
Not using LSE as do not need RTC



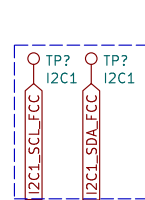
AN2606 (Bootloaders): USART1 Bootloader (PB14/PB15)

UART2, I2C1, and GPIO\_A to GPIO\_H -> NAVC  
UART1 -> Debug (via USB)  
TIM4 -> PWM/PPM input

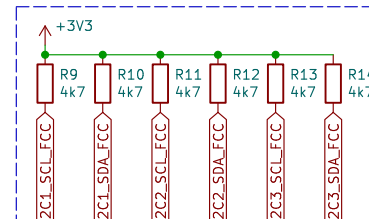
C15, C27, C29 Tantalum



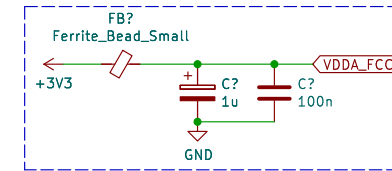
Decoupling Capacitors



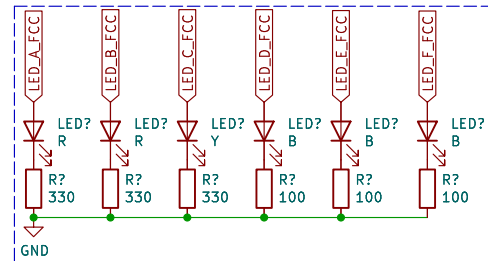
Test Points (I2C2)



I2C Pull-Up Resistors

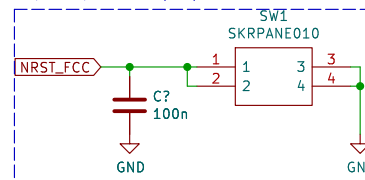


VDDA Filter

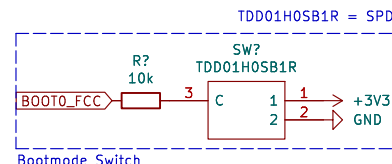


Indicator LEDs

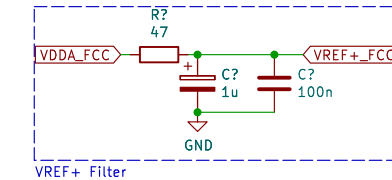
STM32H7 has internal NRST pull-up resistor (40k)



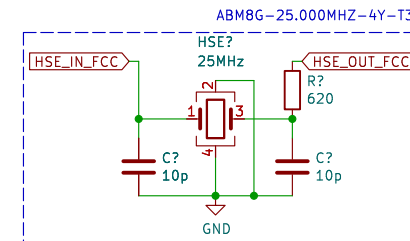
Reset Switch



Bootmode Switch



VREF+ Filter



High Speed External Crystal

$C32 = C33 = 2 * (CL - Cstray)$        $Rext = 1/(2*pi*f*CL2)$   
 $Cstray = \{2pF, 5pf\}$

Sheet: /FlightControlComputer/  
File: FCC.kicad\_sch

**Title: Flight Control Computer**

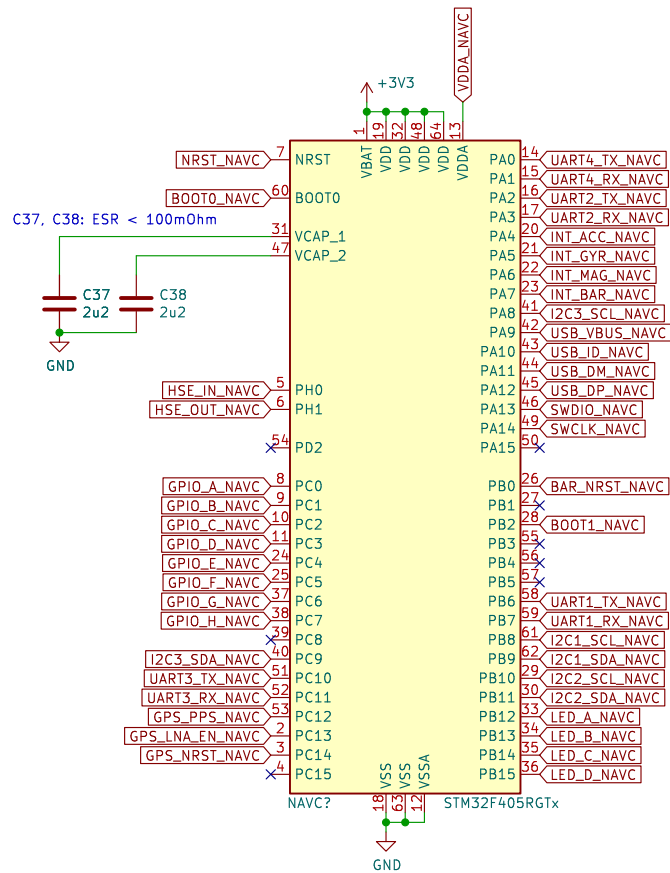
Size: A4      Date: 2025-02-04

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**Rev: 0.1**

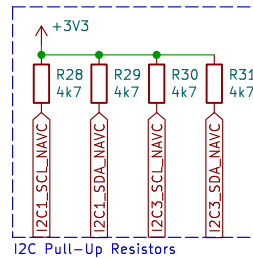
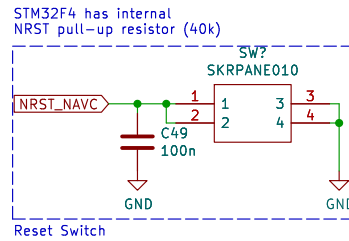
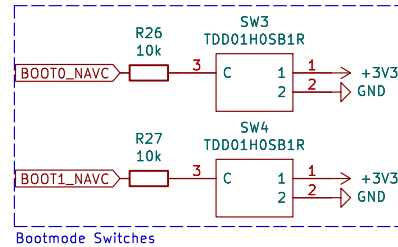
Id: 2/7

# Navigation Computer (NAVC)

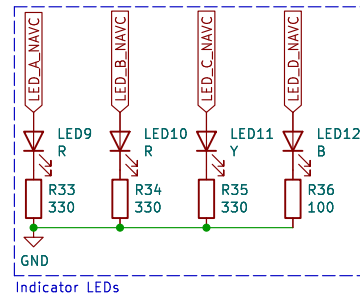


UART2, I2C2, and GPIO\_A to GPIO\_H -> FCC  
UART3 -> Debug (via USB)

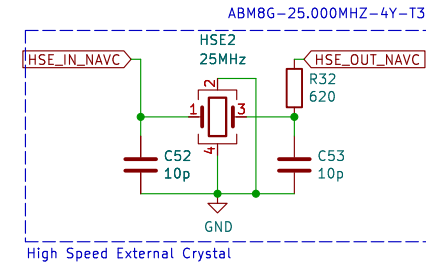
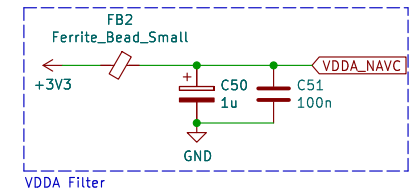
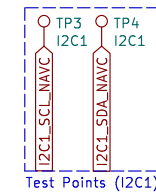
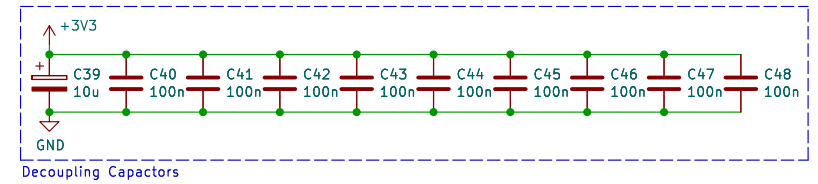
AN2606 (Bootloaders): USART3 Bootloader PC10/PC11



I2C2\_NAVC already pulled up by I2C1\_FCC resistors!



C39, C50 Tantalum



C52 = C53 = 2 \* (CL - Cstray)  
Cstray = {2pF, 5pF}

Rest = 1/(2\*pi\*f\*CL2)

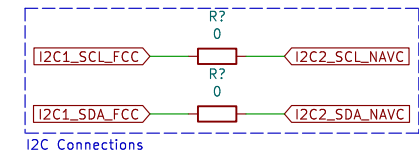
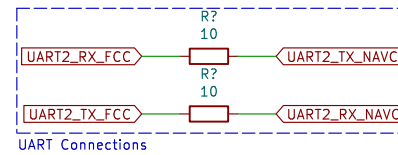
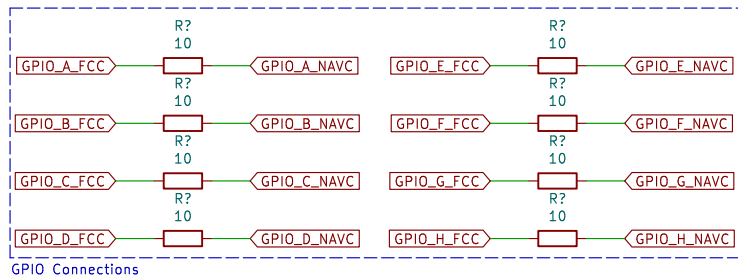
Sheet: /NavigationComputer/  
File: NAVC.kicad\_sch

**Title: Navigation Computer**

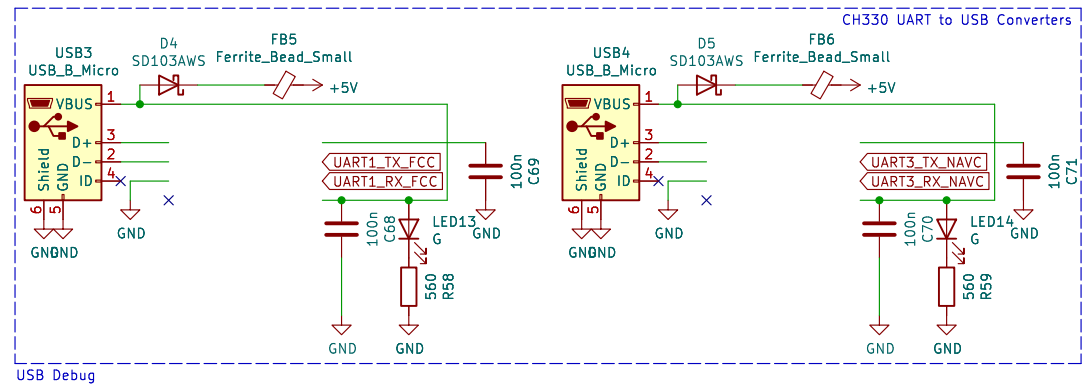
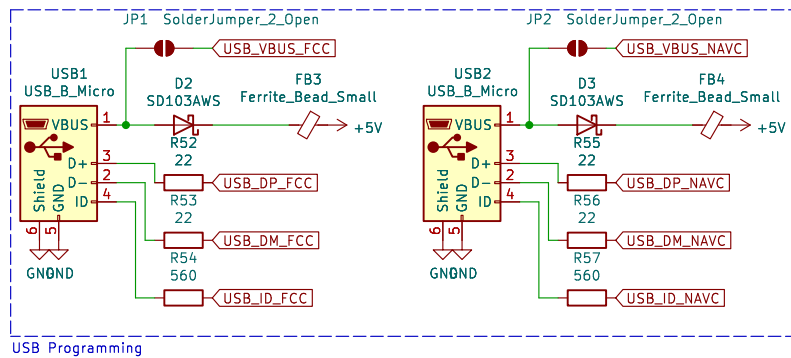
Size: A4 Date: 2025-02-04  
KiCad E.D.A. 8.0.5

Rev: 0.1  
Id: 3/7

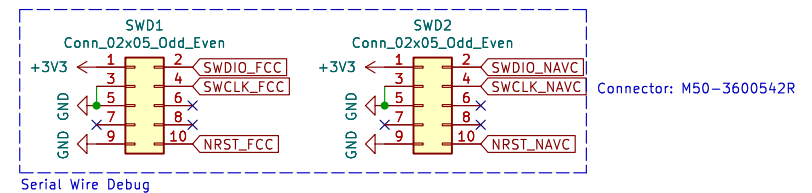
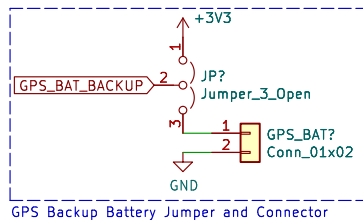
# Connections (FCC <--> NAVC)



## Connectors (Programming & Debug)



## GPS Backup Battery



Sheet: /Connections/  
File: Connections.kicad\_sch

**Title: Connections**

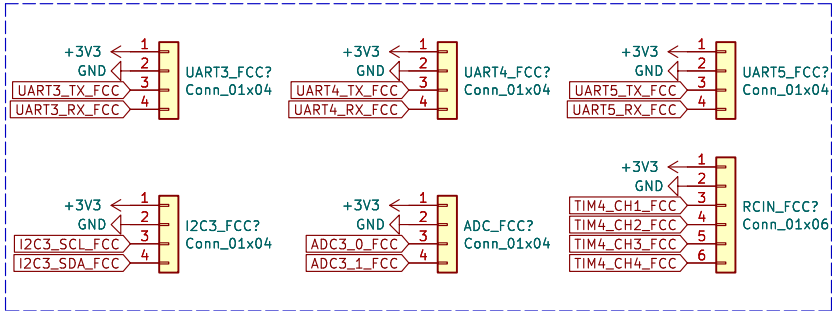
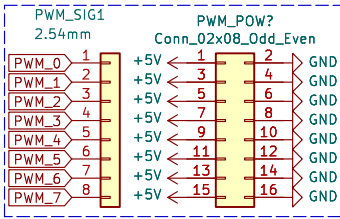
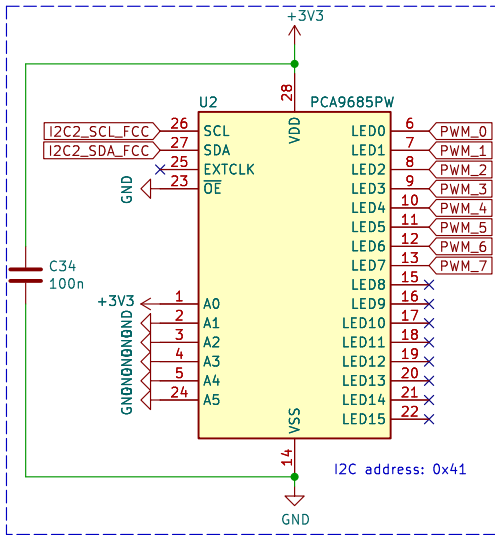
Size: A4 Date: 2025-02-04

KiCad E.D.A. 8.0.5

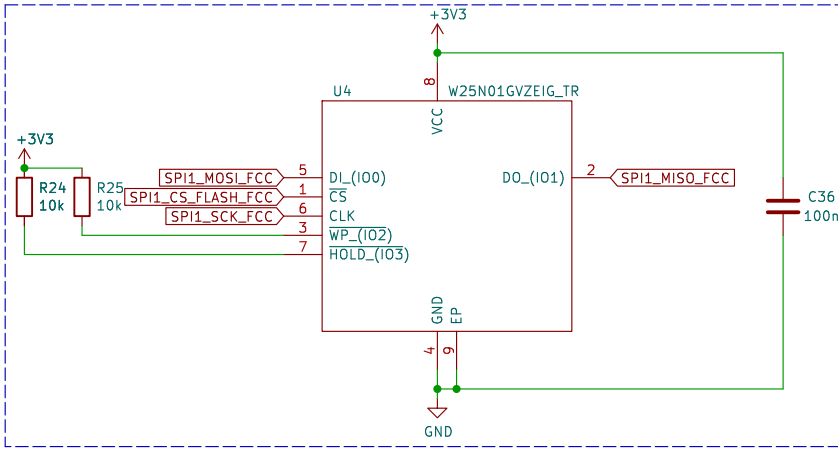
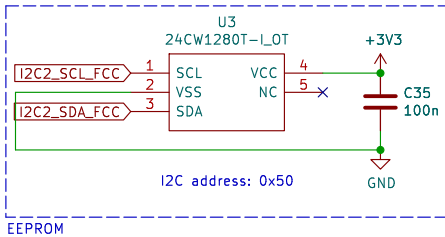
Rev: 0.1

Id: 4/7

# FCC Peripherals



Board To Wire Connectors  
(Molex PicoBlade)



Sheet: /FCCPeripherals/  
File: FCCPeripherals.kicad\_sch

**Title: FCC Peripherals**

Size: A4 Date: 2025-02-04

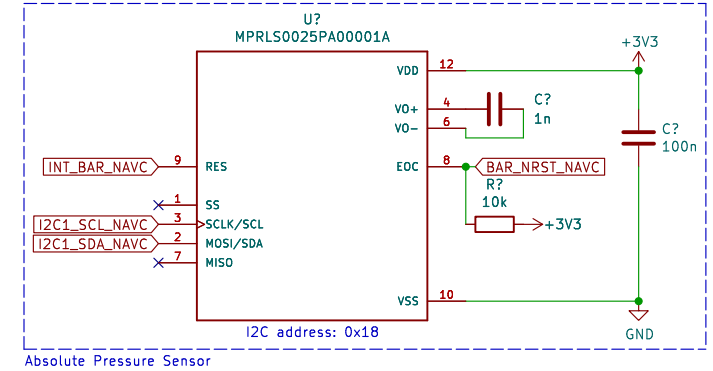
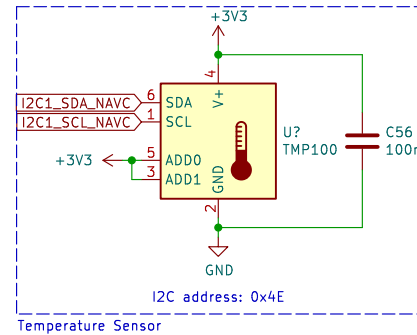
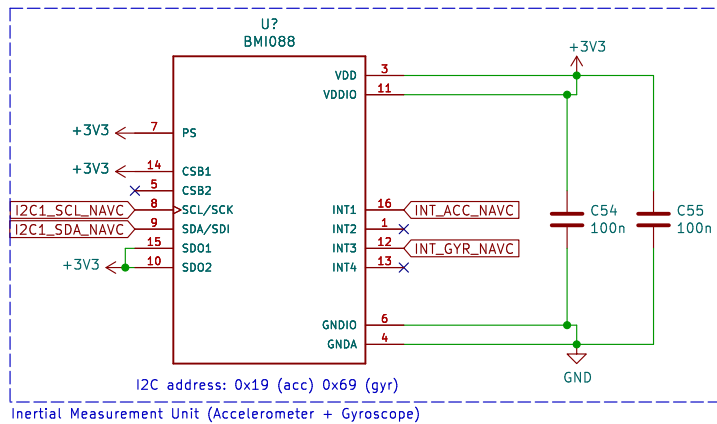
KiCad E.D.A. 8.0.5

Rev: 0.1

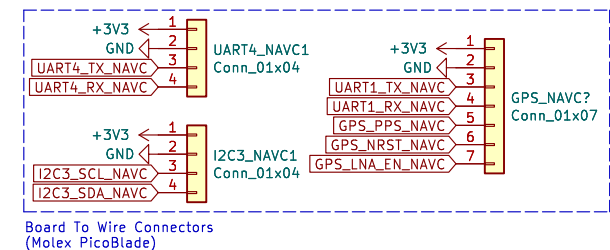
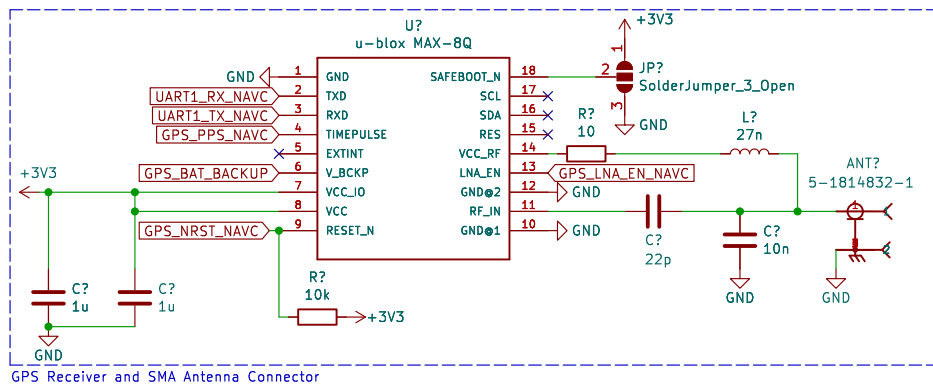
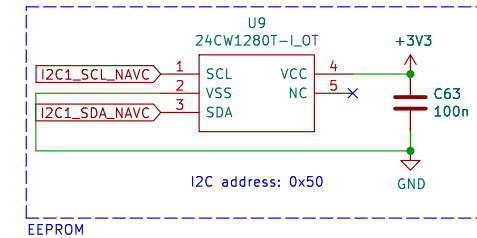
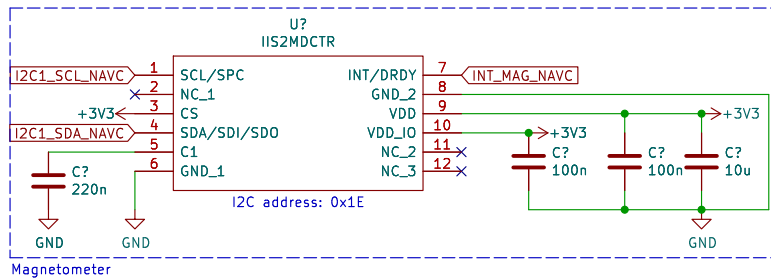
Id: 5/7

# NAVC Peripherals

RESET AND INTERRUPT PINS ARE MIXED UP!!!!



C59 ESR < 200mOhm



Sheet: /NAVCPeripherals/  
File: NAVCPeripherals.kicad\_sch

**Title: NAVC Peripherals**

Size: A4 Date: 2025-02-04

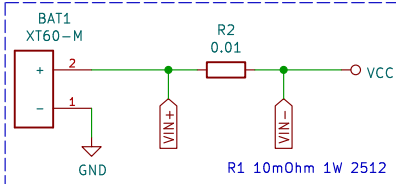
KiCad E.D.A. 8.0.5

Rev: 0.1

Id: 6/7

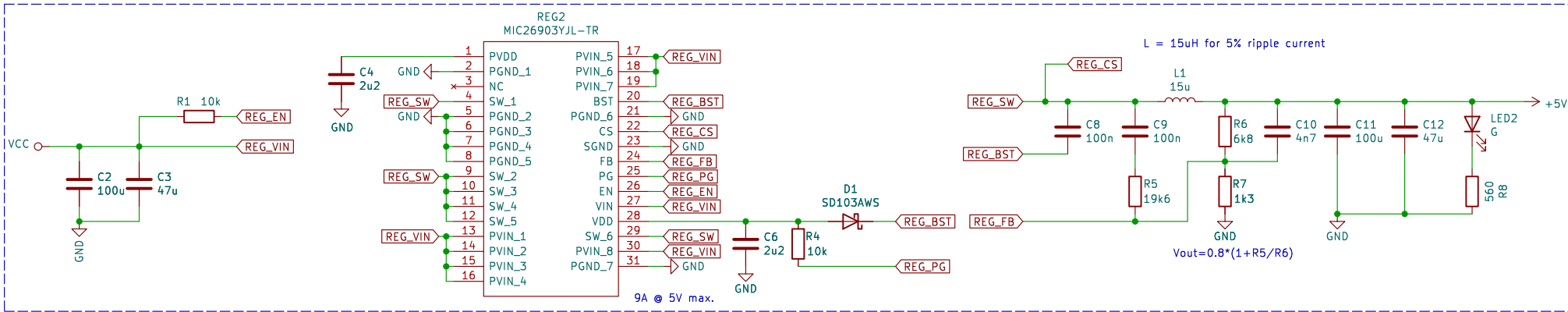
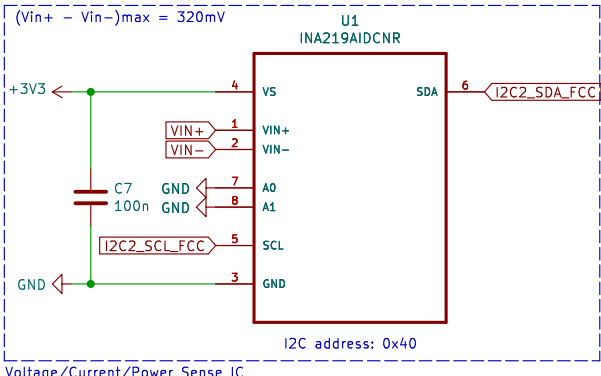
# Power

Maximum input voltage: 26V

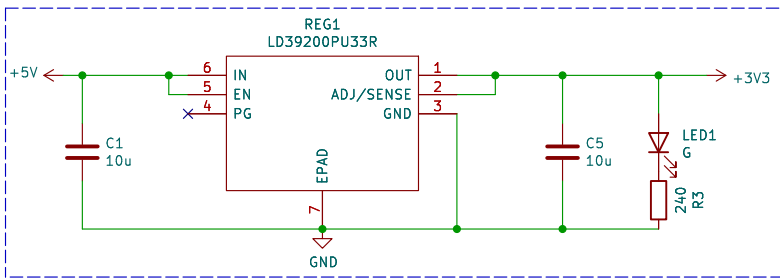


## Battery Input and Current Sense Resistor

C1, C8 are Al Electrolytics  
C2, C9 are 1210 ceramics  
C1, C2 rated at  $\geq 35V$



Keep SGND and PGND separate – connect only at one location!



### 3.3V 2A Regulator (MCUs and Sensors)

Sheet: /Power/  
File: Power.kicad\_sch

**Title:**

Size: A4

Date: 2025-02-04

KiCad E.D.A. 8.0.5

Rev:

Id: 7/7