96

Algo ▼

DS ▼

GATE ▼

CS Subjects ▼

Quizzes ▼

Courses

GBlog

Puzzles

Practice

Memory Hierarchy Design and its Characteristics

Computer Organization | Horizontal microprogrammed Vs Vertical micro-programmed control unit

Computer Organization | RAM vs ROM

Hardwired Vs Microprogrammed Control unit | Set 2

Computer Organization | Read and Write operations in memory

Computer Organization | ALU and Data Path

Computer Organization | Locality of Reference and Cache Operation

Operating System | Memory Interleaving

Difference between Byte Addressable Memory and Word Addressable Memory

2D and 2.5D Memory organization

Memory based Vs Register based addressing modes

Introduction to Parallel Computing

Multilevel Cache Organisation

Computer Organization | Machine Control Instruction

Microprocessor | Timing diagram of MOV instruction

Computer Organization | Instruction Word Size

COMPARE Instructions in 8085

ROTATE Instructions in 8085

Interaction of a Program with Hardware

Computer Organization | Register Allocation

Difference between
Simultaneous and
Hierarchical Access Memory
Organisations

Computer Architecture | Multiprocessor and Multicomputer

Microarchitecture and Instruction Set Architecture

Logical vs Physical Address in Operating System

Students ▼

Google Custom Search

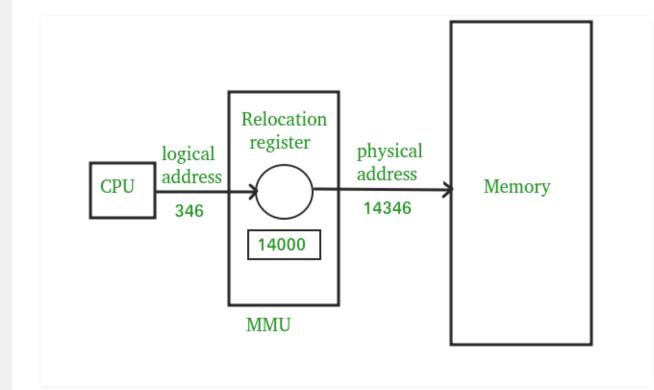
Interview ▼

Languages ▼

Logical Address is generated by CPU while a program is running. The logical address is virtual address as it does not exist physically, therefore, it is also known as Virtual Address. This address is used as a reference to access the physical memory location by CPU. The term Logical Address Space is used for the set of all logical addresses generated by a program's perspective.

The hardware device called Memory-Management Unit is used for mapping logical address to its corresponding physical address.

Physical Address identifies a physical location of required data in a memory. The user never directly deals with the physical address but can access by its corresponding logical address. The user program generates the logical address and thinks that the program is running in this logical address but the program needs physical memory for its execution, therefore, the logical address must be mapped to the physical address by MMU before they are used. The term Physical Address Space is used for all physical addresses corresponding to the logical addresses in a Logical address space.



Mapping virtual-address to physical-addresses

Differences Between Logical and Physical Address in Operating System

- 1. The basic difference between Logical and physical address is that Logical address is generated by CPU in perspective of a program whereas the physical address is a location that exists in the memory unit.
- 2. Logical Address Space is the set of all logical addresses generated by CPU for a program whereas the set of all physical address mapped to corresponding logical addresses is called Physical Address Space.
- 3. The logical address does not exist physically in the memory whereas physical address is a location in the memory that can be accessed physically.
- 4. Identical logical addresses are generated by Compile-time and Load time address binding methods whereas they differs from each other in run-time address binding method. Please refer this for details.
- 5. The logical address is generated by the CPU while the program is running whereas the physical address is computed by the Memory Management Unit (MMU).

Comparison Chart:

Generation

PARAMENTER	LOGICAL ADDRESS	PHYSICAL ADDRESS
Basic	generated by CPU	location in a memory unit
Address Space	Logical Address Space is set of all logical addresses generated by CPU in reference to a program.	Physical Address is set of all physical addresses mapped to the corresponding logical addresses.
Visibility	User can view the logical address of a program.	User can never view physical address of program.

Computed by MMU

generated by the CPU



Most popular in Computer Organization & Architecture

Microprocessor | Externally Initiated Operations

8085 program to sum of two 8 bit numbers without carry

Conventional Computing vs Quantum Computing

Computer Organization | Memory Banking

Computer Organization | Subprogram and its characteristics

Most visited in Difference Between Instruction Set used in simplified instructional Computer (SIC)

Timing diagram of INR M

Subroutine, Subroutine nesting and Stack memory

Instruction Set used in SIC/XE

Register content and Flag status after Instructions

Program for conversion of 32 Bits Single Precision IEEE 754 Floating Point Representation



PARAMENTER	LOGICAL ADDRESS	PHYSICAL ADDRESS
Access	The user can use the	The user can indirectly
	logical address to access	access physical address
	the physical address.	but not directly.

Recommended Posts:

Operating System | Memory management - mapping virtual address to physical addresses

Operating System | Starvation and Aging in Operating Systems

Operating System | Buddy System - Memory allocation technique

Operating System | Semaphores in operating system

Operating System | Introduction of Operating System - Set 1

Operating System | Requirements of memory management system

Operating System | Types of Operating Systems

Operating System | Unix File System

Operating System | Introduction of System Call

Operating System | Kernel I/O Subsystem (I/O System)

Operating System | Thread

Functions of Operating System

Operating System | Segmentation

Operating System | Paging

Operating System | Multithreading



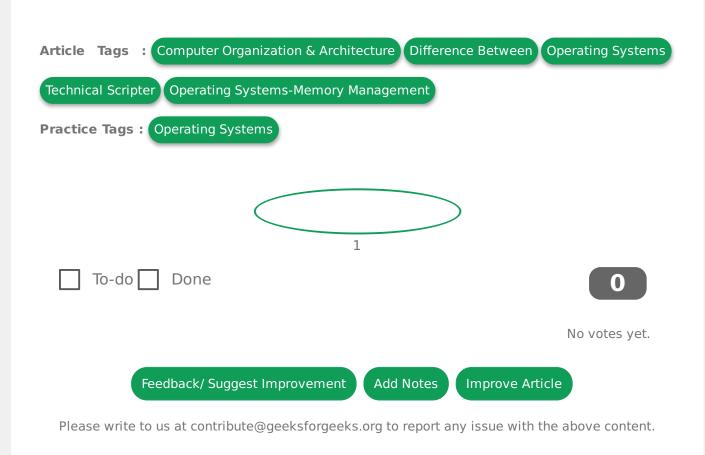
Ankit_Bisht

Check out this Author's contributed articles.

If you like GeeksforGeeks and would like to contribute, you can also write an article using contribute.geeksforgeeks.org or mail your article to contribute@geeksforgeeks.org. See your article appearing on the GeeksforGeeks main page and help other Geeks.

Please Improve this article if you find anything incorrect by clicking on the "Improve Article" button below.

Improved By: VaibhavRai3



Previous

Do you

Count

want to make your own games?

common prime factors of two

Next

Difference between SQL and NoSQL

Differences between IPv4 and IPv6

Difference between HashMap and HashSetv

Stack vs Heap Memory Allocation

Difference between C and C++



Advertise Here

Writing code in comment? Please use ide.geeksforgeeks.org, generate link and share the link here.

Share this post!

GeeksforGeeks
A computer science portal for geeks

5th Floor, A-118, Sector-136, Noida, Uttar Pradesh - 201305 feedback@geeksforgeeks.org COMPANY

About Us Careers Privacy Policy Contact Us **LEARN**

Algorithms
Data Structures
Languages
CS Subjects
Video Tutorials

PRACTICE

Company-wise Topic-wise Contests Subjective Questions **CONTRIBUTE**

Write an Article
Write Interview Experience
Internships
Videos

@geeksforgeeks, Some rights reserved