PUNE INSTITUTE OF COMPUTER TECHNOLOGY, PUNE.

Department of Information Technology

PROJECT REVIEW - II

(Academic Year: 2020-21)

Group Id :		21		Date: 10/12/2020				
Project Title: Distributed Replicated Block Device (DRBD) Network Packet Tracing Module								
Sr. No.	Roll No.	Student Name	Contact Details	Internal / External Guide Details				
1	43110	Bharat Kothari	bharatkothari1008@gmail.com	Guide Name: Prof. Vishal Jaiswal				
2	43132	Animesh Landge	anni.landge@gmail.com	Mentor Name, email & Mobile No. :				
3	43258	Shrijan Vats	shrijanv.123@gmail.com	Mr. Swapnil Ujgare Swapnil.Ujgare@veritas.com				
4	43304	Amod Dhopavkar	amoddhopavkar2@gmail.com					

REVIEW – II CHECKLIST: DESIGN

25 Marks

DESIGN					
1. Are requirements reflected in the system architecture?	Y/ N/NA/NC*				
2. Does the design support both project (product) and project goals?	Y / N / NA / NC*				
3. Does the design address all the issues from the requirements?	Y / N / NA / NC*				
4. Is effective modularity achieved and modules are functionally independent?	Y / N / NA / NC*				
5. Are structural diagrams (Class, Object, etc.) well defined and understood?	Y/ N/NA/NC*				
6. Are all class associations clearly defined and understood? (Is it clear which classes provide which services)?	Y/ N/NA/NC*				
7. Are the classes in the class diagram clear? (What they represent in the architecture design document?)	Y / N / NA / NC*				
8. Is inheritance appropriately used?	Y / N / NA / NC*				
9. Are the multiplicities in the use case diagram depicted in the class diagram?	Y/ N/NA/NC*				
10. Are behavioral diagrams (use case, sequence, activity, etc.) well defined and understood?	Y / N / NA / NC*				
11. Is aggregation/containment (if used) clearly defined and understood?	Y/ N/NA/NC*				
12. Does each case have clearly defined actors and input/output?	Y / N / NA / NC*				
13. Is all concurrent processing (if used) clearly understood and reflected in the sequence diagrams?	Y/ N/NA/NC*				
14. Are all objects used in sequence diagram?	Y / N / NA / NC*				
15. Does the sequence diagram match class diagram?	Y/ N/NA/NC*				
16. Are the symbols used in all diagrams correspond to UML standards?	Y / N / NA / NC*				

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STUDENT PERFORMANCE EVALUATION

Students' Contribution and Performance Particulars			Marks(25M) Group Members			
1.	System Architecture & Literature Survey (Review-I)	Y/ Y I	YY/N	Y Y/N	Y/N	
2.	Project Design (5 M)	5	5	5	5	
3.	Methodology /Algorithms and Project Features (5 M)	4	4	4	4	
4.	Project Planning (2 M)	2	2	2	2	
5.	Basic details of Implementation (5 M)	4	4	4	4	
6.	Presentation Skills (4 M)	4	4	4	4	
7.	Question and Answer (4 M)	4	4	4	4	
8.	Summarization of ultimate findings of the Project	Y/ Y I	Y Y /N	Y Y/N	Y/N	
	Total(25M)	23	23	23	23	

To be filled by internal guide & reviewer(s) only.

* Whether the presentation / evaluation is as per the schedule. : YES / NO (If NO mention the reasons for the same.) **Review – II: Deliverables**

Problem Statement / Title	Modules Split-up
Abstract	 Proposed System
Introduction	• Software Tools / Technologies to be
 Literature Survey (comparison with existing system) 	used
 Methodology 	 Proposed Outcomes
Design / algorithms / techniques used	 Partial Report (Semester – I)
	 Project Plan 2.0 (Gantt Chart)

Name & Signature of evaluation committee -

Prof. Tushar Rane Name of Reviewer 1

Prof. Swapnil Mane Name of Reviewer 2

Prof. Vishal Jaiswal Name of Internal Guide