

DS 200 - Research Methods
Module 3 - Research Seminars

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1 Accelerating Data Science at the Edge Using FPGAs - Dr. Viktor Prasanna - 04 Jan. 2019

2 The Riscy Expedition - Prof. Arvind - 07 Jan. 2019

The talk was about leveraging open and free RISC-V ISA to support modular refinement, OOO (out-of-order) design and its impact on teaching. Speaker motivated about availability of large FPGAs in cloud and open source synthesis tools. Module composition using latency-insensitive (dataflow) framework and its limitations was discussed with an example. Hardware OO design using guarded atomic rules and cooperating Finite State Machines (FSM) was illustrated. The talk ended with the design of a complex superscalar processor design with non-blocking cache coherent memory system using conflict matrix and some case-studies from a related course.

3 Multi-Label Prediction - Dr. Yashaswi Verma - 30 Jan. 2019

4 Simulating Human Vision and Vision Correcting Displays - Prof. Brian Barsky - 21 Mar. 2019

5 Clustering Queries with Similar Intent at Web Scale - Dr. Manoj Agarwal - 05 Apr. 2019

6 Unified analytics using Spark and CarbonData - Raghunandan and Venkata Ramana - 08 Apr. 2019

7 The RAD-Squared Archetypal Pattern for Microservice Architectures - Dr. Zacharia George - 15 Apr. 2019

8 Recovering Temporal Information in Dynamic Networks - Dr. Jithin K Sreedharan - 26 Apr. 2019