```
#include<lpc21xx.h>
#include<board.h>
void timer0(void)__attribute__((interrupt("IRQ")));


void timer0(void)
{
int adcdata;


   //
*T0IR = 0X01;
    while(!(*ADDR&0X80000000));//////check status
of DONE bit

        adcdata = (*ADDR&0X0000FFC0);
        adcdata = adcdata >> 6;
        q_printf("timer 0 interrupt");
        q_printf(" adcdata= %x \n ",adcdata);
        *VICVectAddr = 0X00;    /////Holds ISR add
r of active interrupt. Writing any value indicates
 End of Interrup

}



int main()
{
//////*PINSEL1 = *PINSEL1 & 0XFCFFFFFF;
*PINSEL1 =*PINSEL1 | 0X01000000;///////ADC0.1.....
P0.28 pin as ADC i/p



    *ADCR = 0X01210302;

    *VICVectCntl0 = 0X24; //////VICVectCntlx=VIQ SL
OT 0,Timer0 IRQ(BITS 0 to 4),VIQ (BIT 5)
    *VICIntEnable = 0X10;   //////High bit enables
 FIQ or IRQ classified interrupts(Enable Timer 0 I
```

```
RQ/FIQ Interrupt)
    *T0IR = 0X01;
    *T0MCR = 0X03;
    *T0MR0 = 0X00E4E1C0;
    *T0TCR = 0X01;  /////Enable timer


    *VICVectAddr0 = (unsigned int)timer0; //////Hol
ds ISR address of timer0
    while(1);
    return 0;
}
```