

Assignment 1

C Shruti - EE18BTECH11006

Download all files from

<https://github.com/shruti-chepuri/C-DS/tree/main/Assignment-1>

1 PROBLEM

(Q 12) Consider the following intermediate program in three address code

```
p = a - b
q = p * c
p = u * v
q = p + q
```

Which one of the following corresponds to a static single assignment from the above code?

2 SOLUTION

Answer:

Static single assignment form is a property of an intermediate representation (IR), which requires that each variable be assigned exactly once, and every variable be defined before it is used. Existing variables in the original IR are split into versions, new variables typically indicated by the original name with a subscript, so that every definition gets its own version.

A compiler can be divided into two parts : the front end (analysis part) and the back end (synthesis part). The intermediate code comes in between these two parts. The front end after performing lexical, syntax and semantic analysis produces the intermediate code which is consumed or used or fed into the back end for producing the target code or machine code.

The front end is language specific and the back end is machine specific. In other words the front end of a C compiler is same in two machines but the back end might be different because of the difference in the architecture and instruction set of

the machines.

Static Single Assignment form (SSA form)

According to Static Single Assignment:

- A variable cannot be used more than once in the LHS.
- A variable should be initialized utmost once.

Now keeping in mind the above two rules we have,

$$p_1 = a - b$$

$$q_1 = p_1 * c$$

$$p_2 = u * v \text{ // } p_1 \text{ as already been used once in the LHS}$$

$$q_2 = p_2 + q_1 \text{ // } q_1 \text{ has already been used once before}$$

Thus the SSA form is

$$p_1 = a - b$$

$$q_1 = p_1 * c$$

$$p_2 = u * v$$

$$q_2 = p_2 + q_1$$

Advantages of using SSA form:

Using the SSA opens a bunch of opportunities for compiler optimisations like dead code elimination, strength reduction, register allocation, etc