

## Mid Semester Examination

### Spring Semester

- 1) Find the minterms of the logical expression  $Y = A'B'C' + A'B'C + A'BC + ABC'$ .
- 2) Define Half adder and full adder.
- 3) List basic types of programmable logic devices.
- 4) State the classifications of FET devices.
- 5) What is binary decoder?
- 6) What are the basic digital logic gates?
- 7) Define duality property.
- 8) Define multiplexer?
- 9) What is an essential implicant?
- 10) State the distributive property of Boolean algebra.
- 11) State De Morgan's theorem.
- 12) What are called don't care conditions?
- 13) What is a karnaugh map?
- 14) What is a Logic gate? Give the classification of logic families
- 15) What is priority Encoder?