

OUTPUT WAVEFORM

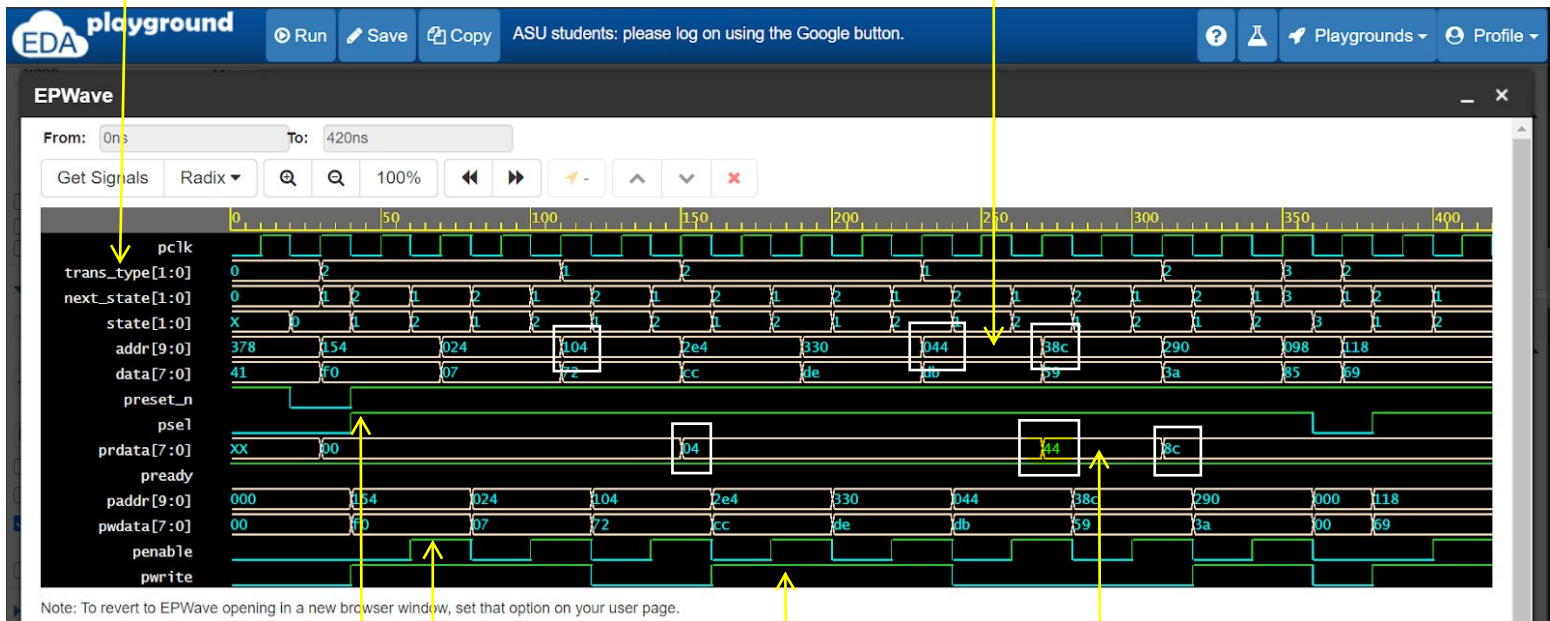
Transaction type

- 0 - Reset
- 1 - Read
- 2 - Write
- 3 - Done

Memory size 1k*32 bits

Address - 10 bits

Data - 8 bits



As per the protocol, every transaction takes atleast 2 cycles.
 Cycle 1 - Start (PSEL high)
 Cycle 2 - Transfer (PENABLE also high)

PWRITE
 1 - Write
 0 - Read

Output is having size 8 bits, and memory is initialized with same value as its positional index. Thus results of read can be verified here.