

Review ID:		ACM Project:		DO-178 Level:	
Review Type:		ACM Subproject:		Rework Effort (hours):	
Produced:				Closure Effort (hours):	
Date   Time		Meeting Duration:		Moderator Closure →	
Date   Time		# Participants:		APPROVED By Qian, Cuiyu at 9:47 am, Nov 21, 2014	
Date   Time		Date Complete:		Audit: Stamp Here	
Telephone   Participant Code:		Review Status: (result of review)			

Work Product Type(s):	Supporting Material(s) / Comments:

U: [ a & A ] [ ^ ^ ]	U: [ a & A ] [ ^ ^ ]
U: [ a & A ] [ ^ ^ ]	U: [ a & A ] [ ^ ^ ]
U: [ a & A ] [ ^ ^ ]	U: [ a & A ] [ ^ ^ ]
U: [ a & A ] [ ^ ^ ]	U: [ a & A ] [ ^ ^ ]

Work Products Under Review		Reuse Scope:			
Problem Report	File Name	File Version	Review Size	Size Units	Approved Version

Participants	Expert Pass-thru	Comment
Name	Function (discipline)/ Responsibility	Review Time (hours)

Assignee's signature (stamp) confirms that a review was performed and any action items and markups were incorporated or dispositioned.

Participant's signature (stamp) confirms participation in the review. A lack of signature (stamp) indicates nonparticipation.

Moderator's signature (stamp) indicates record is complete.

U: [ a & A ] [ ^ ^ ]

U: [ a & A ] [ ^ ^ ]

## Coversheet Continued

[illegible]

**Component Test Procedure (CTP)  
Checklist**

(CTP\_CHECKLIST\_WORD.doc 10/24/07)

ACM Project: \_\_\_\_\_

ACM Sub-Project: \_\_\_\_\_

SCR Number: \_\_\_\_\_

Affected Area: \_\_\_\_\_

**Overview:** CTPs are generated to verify an individual software element or group of elements properly implement requirements the software element(s) trace to. Use this checklist to inspect test cases and associated test procedures, drivers, and stubs against requirements the software element(s) implement. The CTP(s) are verified to conform to standards, and fully test requirements with appropriate structural coverage. The associate tracing data and test coverage analysis/disposition data (if any) is also verified.

**Misc Info** Reference: FMS Test Process C71-5780-043, Section 5.

**Yes No N/A Administrative**

1. Do the CTPs elements follow the standard naming conventions?

CTP\_<A/C>\_<FAREA>\_<FUNC-NAME>.TDF file – CTP Test Definition File

CTP\_< A/C >\_<FAREA>\_< FUNC-NAME >.ZIP file – miscellaneous test related files

CTP\_< A/C >\_<FAREA>\_< FUNC-NAME>.TRT file – CTP Trace file(Core only)

CTP elements configured in the CM tool:

2. Is \*.TDF file – CTP Test Definition File present?
3. Is \*.ZIP file present?
4. Is \*.TRT file – CTP Trace file present (Core only)?

Review Packet information details:

5. Is SCR Number and a copy of the SCR (Sec state) present?
6. Is TDF, TRT(If present), ZIP files with correct generation information present?
7. Support files (SRD, SDD, and Checklist) with Generation information.
8. Does the review packet contain a difference listing of the old test to the new test and are the differences limited to the changes specified in this SCR?
9. Is the version of the material under review and supporting material correct for the SCR(s)?
10. Has the material/version been identified on the cover sheet of the review packet (may reference SCR)?
11. Have all SCR fields (e.g. Analysis/Solution) been filled out properly?

**Yes   No   N/A   TDF (CTP Test Definition File)**

Does the TDF header include the following fields:

12. Does the TDF header include the following fields:
  - Filename
  - Title
  - Author
  - Creation Date
  - Modification History
  - Source
  - Description of TDF
13. Is the SCR number and description updated for this SCR?
14. Does the TDF header include a unique ANCHOR name for this CTP?
15. Is the list of SRD/SDD element references (and their generation numbers) updated and correct? (including formatting of this information)

**Yes   No   N/A   ZIP File (CTP Related Miscellaneous Files)**

16. Does the ZIP file contain the updated necessary test files ?
  - \*.BAT
  - \*.CUL
  - ~~\*.DRV~~ (\*\_D.ADA)
  - ~~\*.VER~~ (\*.RST)
  - \*.RPT
  - Optional files: STB, DSP, and INC (if necessary).
  - Has the \*.CUL file been updated to show the correct span of source code procedures/functions that are being tested by this CTP?

## Component Test Procedure (Ctp) Checklist

<b>Yes</b>	<b>No</b>	<b>N/A</b>	<b>TRT File (Core only)</b>
------------	-----------	------------	-----------------------------

17. Does the TRT header include the following fields:

- Filename
- Title
- Author
- Creation Date
- Modification History
- Is the modification history with date, author, SCR number, and description updated??

18. Has the traceability matrix been updated/verified (trace to the correct requirements)??

<b>Yes</b>	<b>No</b>	<b>N/A</b>	<b>Test Case Design</b>
------------	-----------	------------	-------------------------

19. Are the test case ID numbers present in sequential order?

20. Does the test script have test case descriptions which describe the objectives, intent, and operation for each test case?

21. Are all the allocated requirements tested?

22. If anchor is found to be a bad trace or vague/ambiguous, has it been disposed with a reference SCR.

23. Does the test case description section of each test case identify the specific requirements (SRD anchors) that are being tested?

24. Does the test case description section of each test case identify the specific requirements (SRD anchors) that are supporting requirements?

25. To ensure robust testing, are all test cases inputs set with at least 2 different values?

26. To ensure robust testing, are boundary conditions and tolerances tested where ever applicable?

## Component Test Procedure (Ctp) Checklist

Yes	No	N/A	Test Case Design con't
			27. Coverage Levels – Has every point of entry and exit in the program been invoked at least once?
			28. Coverage Levels – Has every decision in the program taken on all possible outcomes at least once?
			29. Coverage Levels – Has every condition in a decision in the program taken on all possible outcomes at least once?
			30. Coverage Levels – Has every condition in a decision been shown to independently affect that decision's outcome? A condition is shown to independently affect a decision's outcome by varying just that condition while holding fixed all other possible conditions.
			31. Data Coupling – Are there test cases which exercise “data coupling” between software modules (i.e., the dependence of a software component on data not exclusively under the control of that software component)?
			32. Data Coupling – Are there test cases which exercise “control coupling” between software modules (i.e., the manner or degree by which one software component influences the execution of another software component)?
			33. Error Guessing - Do areas in the software known to have complex algorithms have a sufficient number of test cases to ensure they are working as expected?
			34. Error Guessing - Do areas in the software associated with complex requirements have a sufficient number of test cases to ensure they are working as expected?
			35. Outputs - Are all test case outputs measured for at least two different values?
			36. Outputs - Have variables with expected output values been initialized to other values before input to the test process (e.g., If a variable is expected to have an output result of TRUE, is the input state of this variable set to FALSE before executing the test case?)
			37. Coverage Analysis - Are the entire test paths covered as per the structural coverage requirements mandated for Flight Management Systems? If not, are such structural coverage deficiencies dispositioned? If not determined to be a tool problem, then the disposition must reference to an SCR.
			38. Coverage Analysis - For uncovered requirements, is there another test that provides the coverage?
			39. Has the Test name and Anchor required if one exists, been identified? If not, has an SCR been written and the SCR number referenced?
			40. Coverage Analysis – Have all the failures been analyzed and disposed appropriately in the DSP quoting a correct SCR number documenting the reason for the failures.

## Component Test Procedure (Ctp) Checklist

**Yes   No   N/A**

### **Polymorphism Related Issues (C++)**

- 41. Has the code under test been examined for the existence of dynamic dispatch (can be determined by virtual functions in the code or a virtual table in the assembly code)?
- 42. Does each test case appearing in the set of test cases associated with a class appear in the set of test cases associated with each of its subclasses?
- 43. If dynamic dispatch is involved in the execution of a function, is the method separately tested in the context of every concrete class in which it appears, irrespective of whether it is defined by the class or inherited by it?  
An exception is made for simple get and set methods that only assign a value to, or return the value of an attribute or association. Such methods need only be tested once, in the context of the defining class.
- 44. Are errors dispositioned to an SCR or has the test been updated?

**Yes   No   N/A**

### **Other**

- 45. Are all defects identified by the previous questions?

**N N/A Justification Box**

Change Category: PROBLEM  
SCR Status: SEC SCR Status Date: 22-OCT-2014  
Originator: O'Connor, Michael  
Affected Area: TESTS  
Assignee: Jin, Gawain  
Verification Assignee: Xiong, Sarah  
Found in Configuration: A380\_2009\_CR2\_0  
Target Configuration: A3240\_REL2\_TST\_X02

SCR No.: P 58370.01

Date Originated: 20-OCT-2014  
Customer No.:  
Priority: 3

Hardcopy Attachment: None

Planned Impact: Test  
Found During: SYS SPEC DEV/REVIEW  
Aircraft Affected: A340  
Task: N/A  
CR1-F41 Type:

SCR Copied To: < None Entered >  
SCR Copied From: < None Entered >  
SCR Reissued To: < None Entered >  
SCR Reissued From: < None Entered >

Title: ETP location on MFD and ND do not match

## Description:

From ATP squawk 1806: The ETP on ETP MFD page is MARTA/-67.3 and it does not correspond to ND display. ETP is at QUV on the ND and MARTA/-67.2 on ETP page. See pictures AD2\_check\_3 and AD2\_check\_3a. FpIn is available in printer file AD2\_SPP.txt.

Entries of VIA and TO performed From PPG :

Via UN855  
Via UN870 to QUV  
Via UM601  
VIA UT608 to POLOS  
To MARTA  
VIA UN856  
VIA UH300  
Insert airways.

Thus, effectively, there is a disco after XUH300.

-----  
From ATP squawk 1843: New occurrence of wrong ETP computation after a dir to performed to a fix and a disco is strung after. See 3 pictures in S2 Attachments folder (USroute-wrong ETP\*.bmp). This happened during Action 29 of ATP\_A380\_US\_ROUTE.DOC.

SRB Reviewed By: O'Connor, Michael

Date: 21-OCT-2014

## Analysis/Solution:

&lt;Oct-23-2014&gt;&lt;E872867&gt;

Updated CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA for A340 STEP2 CR2 on build ST2099

&lt; Solution field continued &gt;

SCR No. 58370.01

Page 2 of 2



and executed in ITE mode.  
1. TDF(Gen=4)  
1)Updated the SDD/SRD generation as following:  
11\_3\_3.SRD : 68-->73  
11\_2\_1\_1\_7.SRD : 78-->85  
PERF\_BACKGROUND\_EXEC.SDD : 350-->371  
PERF\_OBJECT\_MAN.SDD : 128-->133  
PERF\_UTILITIES.SDD : 117-->126  
2)Updated breakpoints as build changed.  
3)Updated as per SCR 49180.00(FMS2000, A3XX).  
a.Updated TC 35 to verify PERF\_SDD\_3155\_INT completely.  
2. ZIP(Gen=5)  
1) New Rst, Rpt file.  
2) Updated DSP file.

Elements Affected:

Doc.	Element	Generation
TEST	CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.TDF	4
TEST	CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.ZIP	5
ASSIGNEE: Jin, Gawain		Date: 23-OCT-2014
VERIFIER:		Date:
CCB COORDINATOR:		Date:

Closure Category: Fixed/Added  
Project Status: Done  
Addendum:  
Visual Review Info:  
Cert Concern:  
Cust Notification:  
Inservice Incident:  
FDE Distraction:  
Pilot Input:  
Workload Wrkaround:  
Must Fix:  
Score/Comment:  
Cause: N/A  
Closed in Config.: A3240\_REL2\_TRX\_X02

Duplicate SCR No.: 00000.00



Mode: All Lines

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF

1	1				
2	2	FILE	:	CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.TDF	
3	3				
4	4	SOURCE CONFIGURATION	:	ISS (Instruction Set Simulator)	
5	5				
6	6	DESCRIPTION	:	CTP_A340S1A_PERF_BKGND_PUT_BK_DATA Test	
7	7				
8	8	MODIFICATION HISTORY	:		
9	9				
10	10	DATE	SCR #	AUTHOR	DESCRIPTION
11	11	=====	=====	=====	=====
12	12				
13	13	Aug 18, 2010	52527.78	Zhihong Zhai	Initial Development for A340 S1A S1 plan.
14	14				1. Rollover from A320 S1A
15	15				CTP_A320_PERF_BKGND_PUT_BK_DATA(TDF;20,
		» ZIP;21).			
16	16				2. Updated following SRD/SDD generations:
17	17				11_3_3.SRD ; 47 -->
		» 58			
18	18				11_13.SRD ; 18 -->
		» 19			
19	19				11_7.SRD ; 27 -->
		» 28			
20	20				11_14_3.SRD ; 14 -->
		» 16			
21	21				11_14_4.SRD ; 24 -->
		» 25			
22	22				11_2_1_1.SRD ; 75 -->
		» 84			
23	23				11_2_9.SRD ; 14 -->
		» 17			
24	24				11_2_1_1_7.SRD ; 64 -->
		» 71			
25	25				11_2_1_12.SRD ; 18 -->
		» 20			
26	26				11_5_2.SRD ; 41 -->
		» 53			
27	27				11_1.SRD ; 157 -->
		» 176			
28	28				PERF_BACKGROUND_EXEC.SDD ; 280 -->
		» 326			
29	29				PERF_OBJECT_MAN.SDD ; 107 -->

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

30	30	» 116				PERF_UTILITIES.SDD ; 86 -->
31	31	» 104				PERF_MAXIMUM_ALT.SDD ; 27 -->
32	32	» 28				3. Updated as per SCR 49154.01(FMS2000, A3
33	33	» XX)				a). Updated TC 12-14, 17, 35-38 for SDD
34	34					PERF_SDD_3155_INT.
35	35					
36	36		Jul 9,2013	55836.04	Chen Jixing	Update as per A340_55677_04.DRAT on build S
37	37	» 1A120 for A340				Peg 2
38	38					1. Update SRd/SDD generations:
39	39					PERF_BACKGROUND_EXEC.SDD ; 326 --> 3
40	40	» 31				11_5_2.SRD; 53 --> 6
41	41	» 4				2. Updated as per SCR 55677.06(FMS2000, A3
42	42	» XX)				a). Update TC 1, 28, 29 as remove ancho
43	43	» r				PERF_SDD_07059(PERF_SRD_12280, PERF_
44	44	» SRD_12372_INT)				b). Update TC 21, 48, 49, 53,Del TC 52
45	45	» as remove of				anchor PERF_SDD_07063(PERF_SRD_12280
46	46	» ),update TC 54				as delete of vars, update subsequent
47	47	» TC id				after TC 52(here TC id refer to orig
48	48	» inal ID)				c). Delete vars to remove test discrepan
49	49	» cy				d). Update breakpoint line number to rem
50	50	» ove test discrepancy				
51	51	»				
52	52		Aug 26,2014	57231.93	Dun Qing	Update for A340 step2 CR1 on build ST2050.
53	53					1. Update SRd/SDD generations:
54	54	» 50				PERF_BACKGROUND_EXEC.SDD ; 331 --> 3
55	55					PERF_OBJECT_MAN.SDD; 116 -> 128
						PERF_UTILITIES.SDD; 104 -> 117

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

56	56				11_3_3.SRD; 58 -> 68
57	57				11_7.SRD; 28 -> 32
58	58				11_2_1_1.SRD; 84 -> 88
59	59				11_2_1_1_7.SRD; 71 -> 78
60	60				11_2_1_12.SRD; 20 -> 21
61	61				11_1.SRD; 176 -> 183
62	62				Deleted SRD/SDD files:
63	63				11_5_2.SRD;
64	64				11_2_9.SRD;
65	65				2. Updated the breakpoints.
66	66				3. Updated TCs 52,53 to verify PERF_SDD_09
67	67	» 025 as per SCR 55961.36(FMS2000, A3XX)			4. Updated TCs 2,10,13,14,17 as the change
		» d of "Perf_Buffer_Types.Perf_Leg_Type"			
68	68				
69	69	Oct 11,2014	58370.01	Gawain Jin	Updated for A340 STEP2 CR2 on build ST2099
70	70	» .			1.Updated the SDD/SRD generation as follow
71	71	» ing:			
72	72				11_3_3.SRD ; 68-->73
73	73				11_2_1_1_7.SRD ; 78-->85
74	74				PERF_BACKGROUND_EXEC.SDD ; 350-->371
75	75				PERF_OBJECT_MAN.SDD ; 128-->133
76	76				PERF_UTILITIES.SDD ; 117-->126
77	77				2.Updated breakpoints as build changed.
78	78	» X).			3.Updated as per SCR 49180.00(FMS2000, A3X
		» INT completely.			a.Updated TC 35 to verify PERF_SDD_3155_
68	79				
69	80	»			
70		<del>SRD/SDD DETAILS : 11_3_3.SRD ; 68</del>			
81		SRD/SDD DETAILS : 11_3_3.SRD ; 73			
71	82	11_13.SRD ; 19			
72	83	11_7.SRD ; 32			
73	84	11_14_3.SRD ; 16			
74	85	11_14_4.SRD ; 25			
75	86	11_2_1_1.SRD ; 88			
76		<del>11_2_1_1_7.SRD ; 78</del>			
87		11_2_1_1_7.SRD ; 85			
77	88	11_2_1_12.SRD ; 21			
78	89	11_1.SRD ; 183			
79		<del>PERF_BACKGROUND_EXEC.SDD ; 350</del>			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

80		<del>PERF_OBJECT_MAN.SDD</del>	<del>; 128</del>
81		<del>PERF_UTILITIES.SDD</del>	<del>; 117</del>
	90	PERF_BACKGROUND_EXEC.SDD	; 371
	91	PERF_OBJECT_MAN.SDD	; 133
	92	PERF_UTILITIES.SDD	; 126
82	93	PERF_MAXIMUM_ALT.SDD	; 28
83	94	PERF_VDU_UTILS.SDD	; 4
84	95		
85	96	TRACE DETAILS	:
86	97	ANCHOR	: A340_PERF_TEST_2443
87	98	SOURCE	: SDD; PERF_SDD_0421, PERF_SDD_07154,
88	99		PERF_SDD_07394_INT, PERF_SDD_07467_INT, PERF_SDD_07468_INT, PERF_SDD_07469_INT,
89	100		PERF_SDD_07470_INT, PERF_SDD_07471_INT, PERF_SDD_07472_INT, PERF_SDD_07473_INT,
90	101		PERF_SDD_07474_INT, PERF_SDD_07475_INT, PERF_SDD_07476_INT, PERF_SDD_07477_INT,
91	102		PERF_SDD_07479_INT, PERF_SDD_07480_INT, PERF_SDD_07481, PERF_SDD_07482,
92	103		PERF_SDD_07527, PERF_SDD_1826, PERF_SDD_1831, PERF_SDD_2094_INT,
93	104		PERF_SDD_2095_INT, PERF_SDD_2096, PERF_SDD_2109_INT, PERF_SDD_2113_INT,
94	105		PERF_SDD_2158_INT, PERF_SDD_2159_INT, PERF_SDD_2289, PERF_SDD_2407_INT,
95	106		PERF_SDD_2414_INT, PERF_SDD_2417_INT, PERF_SDD_2436, PERF_SDD_2631_INT,
96	107		PERF_SDD_2632_INT, PERF_SDD_3027, PERF_SDD_3052_INT, PERF_SDD_3106_INT,
97	108		PERF_SDD_3107_INT, PERF_SDD_3155_INT, PERF_SDD_3392_INT, PERF_SDD_3393_INT,
98	109		PERF_SDD_3500_INT, PERF_SDD_3501_INT, PERF_SDD_3511_INT, PERF_SDD_3515_INT,
99	110		PERF_SDD_3516_INT, PERF_SDD_3517_INT, PERF_SDD_3518_INT, PERF_SDD_3519_INT,
100	111		PERF_SDD_3520_INT, PERF_SDD_3523_INT, PERF_SDD_3680_INT, PERF_SDD_3739_INT,
101	112		PERF_SDD_3752_INT, PERF_SDD_3968_INT, PERF_SDD_4220_INT, PERF_SDD_4543_INT,
102	113		PERF_SDD_4544_INT, PERF_SDD_5587_INT, PERF_SDD_5614_DR, PERF_SDD_5617_INT,
103	114		PERF_SDD_7018, PERF_SDD_09025
104	115		
105	116	SRD; PERF_SRD_10167_INT, PERF_SRD_10253, PERF_SRD_10333_INT, PERF_SRD_10869,	
106	117	PERF_SRD_12092, PERF_SRD_12093, PERF_SRD_12094, PERF_SRD_12095,	
107	118	PERF_SRD_1544_A3XX, PERF_SRD_2020,	
108	119	PERF_SRD_2045, PERF_SRD_2051, PERF_SRD_2071, PERF_SRD_2087_INT,	
109	120	PERF_SRD_23172_INT, PERF_SRD_23173_INT, PERF_SRD_7463, PERF_SRD_9993,	
110	121	PERF_SRD_9994	
111	122		
112	123	*****	
		» *****	
113	124	INITIALIZATIONS:	
114	125		
115	126	FP_DEF_TOL = 0.001	
116	127		
117	128	define symbol True	:= Standard.True
118	129	define symbol False	:= Standard.False
119	130	define symbol Active	:= Fprequestrec_Types.Active

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

120	131	define symbol Actprimary	:= Airbus_Lgbm.Actprimary
121	132	define symbol Secprimary	:= Airbus_Lgbm.Secprimary
122	133	define symbol Scratchfpln	:= Airbus_Lgbm.Scratchfpln
123	134	define symbol Secondary	:= Fprequestrec_Types.Secondary
124	135	define symbol Cold_Start	:= Apex_Partition_Pkg.Cold_Start
125	136	define symbol Warm_Start	:= Apex_Partition_Pkg.Warm_Start
126	137	define symbol Prim_Fpln_Preds	:= Perf_Int_Base_Tpkg.Prim_Fpln_Preds
127	138	define symbol Optalt	:= Perf_Int_Base_Tpkg.Optimum_altitude
128	139	define symbol Maxalt	:= Perf_Int_Base_Tpkg.Maximum_Altitude
129	140	define symbol Holdactv	:= Perf_Int_Base_Tpkg.Manual_Hold_Preds
130	141	define symbol Fuelpredact	:= Perf_Int_Base_Tpkg.Fuel_Preds
131	142	define symbol Fuelplanact2	:= Perf_Int_Base_Tpkg.Fuel_Plan_Stage2
132	143	define symbol Optimum_step	:= Perf_Int_Base_Tpkg.Optimum_step
133	144	define symbol Goaround	:= Perf_Int_Base_Tpkg.Go_Around_Preds
134	145	define symbol Time_Constraint_Eval	:= Perf_Int_Base_Tpkg.Time_Constraint_Eval
135	146	define symbol Climb	:= Base_Domain_Services_Tpkg.Climb
136	147	define symbol Cruise	:= Base_Domain_Services_Tpkg.Cruise
137	148	define symbol Preflight	:= Base_Domain_Services_Tpkg.Preflight
138	149	define symbol Descent	:= Base_Domain_Services_Tpkg.Descent
139	150	define symbol Single	:= Base_Domain_Services_Tpkg.Single
140	151	define symbol Dual	:= Base_Domain_Services_Tpkg.Dual
141	152	define symbol Firstleg	:= Flight_Pln_Hdr_Types.Firstleg
142	153	define symbol Destwpt	:= Flight_Pln_Hdr_Types.Destwpt
143	154	define symbol Invalid	:= Io_Interface_Tpkg.Entry_Stat_Type'(Io_Interface_Tpkg.Invalid)
144	155	define symbol Valid	:= Io_Interface_Tpkg.Entry_Stat_Type'(Io_Interface_Tpkg.Valid)
145	156	define symbol Master	:= Base_Domain_Services_Tpkg.Master
146	157	define symbol ALTERNATE	:= Perf_Ext_Tpkg.Alternate
147	158	define symbol AF	:= Lateral_Path_Type_Tpkg.AF
148	159	define symbol FA	:= Lateral_Path_Type_Tpkg.FA
149	160	define symbol CLIMBSEG	:= Fmcs_Fp_Guid_Btypes.CLIMBSEG
150	161	define symbol Descentseg	:= Fmcs_Fp_Guid_Btypes.Descentseg
151	162	define symbol Tspnull	:= Flight_Pln_Leg_Types.Tspnull
152	163	define symbol Tsptop	:= Flight_Pln_Leg_Types.Tsptop
153	164	define symbol CAS	:= Fmcs_Base_Types.CAS
154	165	define symbol Mach	:= Fmcs_Base_Types.Mach
155	166		
156	167		
157	168	SUT_VARS	
158	169	-- enumeration types	
159	170	True	
160	171	False	
161	172	Active	
162	173	Secondary	
163	174	Secprimary	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

164	175	Valid
165	176	Scratchfpln
166	177	Invalid
167	178	Cold_Start
168	179	Warm_Start
169	180	Cruise
170	181	Master
171	182	Prim_Fpln_Preds
172	183	Optalt
173	184	Maxalt
174	185	Holdactv
175	186	Fuelpredact
176	187	Fuelplanact2
177	188	Optimum_step
178	189	Data_Storage
179	190	Climb
180	191	Descent
181	192	Single
182	193	Dual
183	194	Preflight
184	195	Goaround
185	196	Firstleg
186	197	Actprimary
187	198	Time_Constraint_Eval
188	199	
189	200	-- variables
190	201	Timer.Start_Time
191	202	Timer.Refresh_Time
192	203	Timer.Average_Refresh_Time
193	204	Timer.Avg_Refresh_Time_Data( )
194	205	Timer.Number_Of_Points
195	206	Ete.Data
196	207	Ete.Valid
197	208	Fprequestrec_Types.Temporary
198	209	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec
199	210	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec
200	211	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec
201	212	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec
202	213	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec
203	214	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
204	215	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress
205	216	Ctp_Perf_bkgnd_put_bk_data.Boot_Status
206	217	Perf_Background_Dpkg.Psfinalalt
207	218	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

208	219	Options_And_Data_Pkg:body.All_Options.Alt_n_Trip_In_Rsv_Enb
209	220	Options_And_Data_Pkg:body.All_Options.Ats_Enable
210	221	Perf_Background_Dpkg.Psfpolfnlful
211	222	Perf_Background_Dpkg.Psfpolfnltme
212	223	Perf_Background_Dpkg.Psfpolfnltg
213	224	Perf_Background_Dpkg.Pslcautoctl
214	225	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel
215	226	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time
216	227	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time
217	228	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Percent
218	229	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Upper_Limit
219	230	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Lower_Limit
220	231	Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest
221	232	Options_And_Data_Pkg:body.All_Options.Cmp_Rsv_In_Flt_Enb
222	233	Perf_Background_Dpkg.Pcfpln
223	234	Fmcs_Partition_Data_Pkg.Ops_Master_Status
224	235	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst
225	236	Perf_Background_Dpkg.Pcfltphase
226	237	Perf_Background_Dpkg.Psfinaldes
227	238	Perf_Background_Dpkg.Vert_Auto_Mode
228	239	Perf_Background_Dpkg.Pcactorsec
229	240	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data
230	241	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode
231	242	Perf_Dpkg.Pstopofcrzfl().Valid
232	243	Perf_Dpkg.Pstopofcrzfl().Data
233	244	Perf_Background_Dpkg.Preds_Output()
234	245	Perf_Background_Dpkg.Pcitin.Itinerary
235	246	Perf_Background_Dpkg.Pcitin.Flight_Plan
236	247	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
237	248	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
238	249	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
239	250	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
240	251	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Data
241	252	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
242	253	Perf_Background_DPkg.Opt_Step_Data.Distodest
243	254	Perf_Background_DPkg.Opt_Step_Data.Timetogo
244	255	Perf_Background_Dpkg.Pshmpreddata.Speed
245	256	Perf_Background_Dpkg.Pshmpreddata.Fuel
246	257	Perf_Background_Dpkg.Pcoptalt.Valid
247	258	Perf_Background_Dpkg.Pcoptalt.Data
248	259	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc
249	260	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.critidx(Firstleg)
250	261	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change
251	262	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

252	263	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr
253	264	Perf_Time_Dpkg:body.Data_Storage().Rta_Control.Valid
254	265	Perf_Background_Dpkg.Pcdestglidx
255	266	Perf_Background_Dpkg.Pctcstrctrl().First_Pass
256	267	Perf_Background_Dpkg.Pcgmttime.Gpc_Time
257	268	Perf_Time_Dpkg:body.Data_Storage().Gmt
258	269	Perf_Background_Dpkg.Psprddataseq
259	270	Perf_Background_Dpkg.Etp_Itin_Ran
260	271	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data().Pack_Vals.Predinprog
261	272	Perf_Dual_Dpkg.Maxalt.Maximum_Maximum_Alt
262	273	Perf_Dual_Dpkg.Maxalt.Gwt
263	274	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data
264	275	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid
265	276	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid
266	277	Perf_background_Dpkg.Maxalt.Gwt
267	278	Perf_background_Dpkg.Maxalt.Num_Engout
268	279	Perf_Dual_Dpkg.Maxalt.Maximum_Alt
269	280	Perf_Dual_Dpkg.Maxalt.Engines_Out
270	281	Perf_Dual_Dpkg.Maxalt.Valid
271	282	Perf_Background_Dpkg.Destination_Data.Efob.Data
272	283	Perf_Background_Dpkg.Destination_Data.Efob.Valid
273	284	Perf_Background_Dpkg.Destination_Data.Ete.Data
274	285	Perf_Background_Dpkg.Destination_Data.Ete.Valid
275	286	Perf_Background_Dpkg.Destination_Data.Firstpass
276	287	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata().Efob.Data
277	288	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata().Efob.Valid
278	289	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata().Ete.Data
279	290	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata().Ete.Valid
280	291	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata().Firstpass
281	292	Perf_Time_Dpkg:body.Data_Storage().Ett_Transfer.Ett.Data
282	293	Perf_Time_Dpkg:body.Data_Storage().Ett_Transfer.Ett.Status
283	294	Perf_Time_Dpkg:body.Data_Storage().Ett_Transfer.Data_Fresh
284	295	Perf_Background_Dpkg.Pctcstrctrl().Transmit
285	296	Perf_Time_Dpkg:body.Data_Storage().Rta_Transfer.Adjcostidx
286	297	Perf_Time_Dpkg:body.Data_Storage().Rta_Transfer.Lastphase
287	298	Perf_Time_Dpkg:body.Data_Storage().Rta_Transfer.Glidx
288	299	Perf_Time_Dpkg:body.Data_Storage().Rta_Transfer.Fpln
289	300	Perf_Time_Dpkg:body.Data_Storage().Rta_Transfer.Valid
290	301	Perf_Time_Dpkg:body.Data_Storage().Rta_Transfer.Eval_Done
291	302	Perf_Time_Dpkg:body.Data_Storage().Rta_Transfer.Env_Limit
292	303	Perf_Time_Dpkg:body.Data_Storage().Rta_Transfer.Flat
293	304	Perf_Time_Dpkg:body.Data_Storage().Rta_Transfer.Flat_Count
294	305	Perf_Time_Dpkg:body.Data_Storage().Prddataseq
295	306	Perf_Time_Dpkg:body.Data_Storage().Display_Asterisk

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

296	307	Perf_Background_Dpkg.Pctcstrctrl().Timeonly
297	308	Perf_Background_Dpkg.Pctcstrctrl().Adjcostidx
298	309	Perf_Background_Dpkg.Pctcstrctrl().Lastphase
299	310	Perf_Background_Dpkg.Pctcstrctrl().Glidx
300	311	Perf_Background_Dpkg.Pctcstrctrl().Valid
301	312	Perf_Background_Dpkg.Pctcstrctrl().Eval_Done
302	313	Perf_Background_Dpkg.Pctcstrctrl().Envelope_Limit
303	314	Perf_Background_Dpkg.Pcperflegs().Included
304	315	Perf_Background_Dpkg.Pcperflegs().Dist
305	316	Perf_Background_Dpkg.Pcstartpt.Dist
306	317	Perf_Background_Dpkg.Pccompett(Active)
307	318	Perf_Background_Dpkg.Pctcstrctrl
308	319	Pseudo_Bp_Pkg.Pb_Act_Cic
309	320	Pseudo_Bp_Pkg.Pb_Calc_Ett
310	321	Perf_Background_Dpkg.Rta.Eval_Done
311	322	Perf_Background_Dpkg.Ett().Data
312	323	Perf_Background_Dpkg.Ett().Status
313	324	Perf_Background_Dpkg.Pctcstridx
314	325	Perf_Background_Dpkg.Rta.Missed
315	326	Bp_Code
316	327	Perf_Background_Dpkg.Pctcstrctrl().Flat
317	328	Perf_Background_Dpkg.Pctcstrctrl().Flat_Count
318	329	Perf_Background_Dpkg.Maxalt.Eo_Maximum_Alt.Valid
319	330	Perf_Flight_Test_Dpkg.Perf_Repack_Data.Maxalt_Valid
320	331	Perf_Flight_Test_Dpkg.Perf_Repack_Data.Max_Maxalt_Valid
321	332	Perf_Flight_Test_Dpkg.Perf_Repack_Data.Engine_Out_Maxalt_Valid
322	333	Data.Maximum_Alt.Valid
323	334	Data.Maximum_Maximum_Alt.Valid
324	335	Data.Eo_Maximum_Alt.Valid
325	336	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Start_Time
326	337	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Average_Refresh_Time
327	338	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data()
328	339	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Number_Of_Points
329	340	Fmcs_Partition_Data_Pkg.Ops_Time.Gpc_Time
330	341	Ops_Timer_Pkg:body.Ops_time.Gpc_Time
331	342	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status
332	343	Perf_Int_Base_Tpkg.Dual_Slave
333	344	Perf_Int_Base_Tpkg.Single
334	345	Perf_Int_Base_Tpkg.Dual_Master
335	346	Perf_Dpkg.Rta_Data_Gathered
336	347	Ctp_Perf_Bkgnd_Put_Bk_Data.Out_Gleg.Spalt1
337	348	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Spalt1
338	349	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg
339	350	Perf_Background_Dpkg.Ats_Enable

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

340	351	Perf_Background_Dpkg.Psrsvaltn
341	352	Perf_Background_Dpkg.Psrsvinflt
342	353	Perf_Background_Dpkg.Psrtersvpctg
343	354	Perf_Background_Dpkg.Psmaxrtersv
344	355	Perf_Background_Dpkg.Psminrtersv
345	356	Perf_Background_Dpkg.Ref_Flight_Plan
346	357	
347	358	Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START
348	359	Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END
349	360	Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START
350	361	Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END
351	362	
352	363	Perf_Background_Dpkg.Offset_Data_Pts().PRDTAS
353	364	Perf_Background_Dpkg.Offset_Data_Pts().Prd_Wind_Mag
354	365	Perf_Background_Dpkg.Offset_Data_Pts().Prd_Wind_True_Brg
355	366	Perf_Background_Dpkg.Offset_Data_Pts().Prdalt
356	367	Perf_Background_Dpkg.Offset_Data_Pts().Prddataseq
357	368	Perf_Background_Dpkg.Offset_Data_Pts().Prdgwttofix
358	369	Perf_Background_Dpkg.Offset_Data_Pts().Fixdistodest
359	370	Perf_Background_Dpkg.Offset_Data_Pts().Fixdtdbias
360	371	Perf_Background_Dpkg.Offset_Data_Pts().Fltphasefix
361	372	Perf_Background_Dpkg.Offset_Data_Pts().Prdterm
362	373	Perf_Background_Dpkg.Offset_Data_Pts().Firstpass
363	374	
364	375	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_Start_Pt.PRDTAS
365	376	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_Start_Pt.Prd_Wind_Mag
366	377	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_Start_Pt.Prd_Wind_True_Brg
367	378	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_Start_Pt.Prddataseq
368	379	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_Start_Pt.Prdalt
369	380	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_Start_Pt.Prdgwttofix
370	381	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_Start_Pt.Fixdistodest
371	382	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_Start_Pt.Fixdtdbias
372	383	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_Start_Pt.Fltphasefix
373	384	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_Start_Pt.Prdterm
374	385	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_Start_Pt.Firstpass
375	386	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_End_Pt.PRDTAS
376	387	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_End_Pt.Prd_Wind_Mag
377	388	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_End_Pt.Prd_Wind_True_Brg
378	389	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_End_Pt.Prddataseq
379	390	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_End_Pt.Prdalt
380	391	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_End_Pt.Prdgwttofix
381	392	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_End_Pt.Fixdistodest
382	393	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_End_Pt.Fixdtdbias
383	394	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_End_Pt.Fltphasefix

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

384 395 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_End_Pt.Prdterm
385 396 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Capture_Path_End_Pt.Firstpass
386 397 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_Start_Pt.PRDTAS
387 398 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_Start_Pt.Prd_Wind_Mag
388 399 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_Start_Pt.Prd_Wind_True_Brg
389 400 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_Start_Pt.Prddataseq
390 401 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_Start_Pt.Prdalt
391 402 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_Start_Pt.Prdgwttofix
392 403 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_Start_Pt.Fixdistodest
393 404 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_Start_Pt.Fixdtdbias
394 405 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_Start_Pt.Fltphasefix
395 406 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_Start_Pt.Prdterm
396 407 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_Start_Pt.Firstpass
397 408 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_End_Pt.PRDTAS
398 409 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_End_Pt.Prd_Wind_Mag
399 410 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_End_Pt.Prd_Wind_True_Brg
400 411 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_End_Pt.Prddataseq
401 412 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_End_Pt.Prdalt
402 413 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_End_Pt.Prdgwttofix
403 414 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_End_Pt.Fixdistodest
404 415 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_End_Pt.Fixdtdbias
405 416 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_End_Pt.Fltphasefix
406 417 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_End_Pt.Prdterm
407 418 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray().Lateral_Offset.Return_Path_End_Pt.Firstpass
408 419 Perf_Dpkg.Optimum_Altitude.Data
409 420 Request_LGB_Called
410 421 Activate_Strategic_Working_Point_List_Called
411 422 Align_Segments_At_Leg_exec
412 423 ReleaseLgb_Called
413 424 Perf_Int_Base_Tpkg.Current_Mode_Hi_Pri
414 425 Perf_Background_Dpkg.Psstepover
415 426 Perf_Background_Dpkg.Pcfinaldest
416 427 Perf_Ext_Tpkg.Alternate
417 428 Perf_Int_Base_Tpkg.Current_Mode_Preds
418 429 Perf_Int_Base_Tpkg.Prim_Fpln_Preds
419 430 Perf_Int_Base_Tpkg.Fuel_Plan_Stage2
420 431 Perf_Ext_Tpkg.Primary
421 432 LOCFP
422 433 Alternate
423 434 Ett_Sys.Data_Fresh
424 435 Send_Ett
425 436 Lgbctrlrec.Clralt.Data
426 437 Lgbctrlrec.Clralt.Valid
427 438

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

428	439	Activate_Tactical_Working_Point_List_Called
429	440	CTP_PERF_BKGND_PUT_BK_DATA.Fpln
430	441	
431	442	Data_Storage.Preds_Complete()
432	443	Perf_Background_Dpkg.Timeconmiss_Updated
433	444	
434	445	Prf_Vdu_Utils:body.Data_Save_In_Progress
435	446	Perf_Vdu_Dpkg.Vdu_Buffer.Buffer_Valid
436	447	Perf_Vdu_Dpkg.Vdu_Buffer.Prddataseq
437	448	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated
438	449	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Crz.Data
439	450	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Crz.Valid
440	451	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Max.Data
441	452	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Max.Valid
442	453	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Rec.Data
443	454	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Rec.Valid
444	455	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Opt.Data
445	456	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Opt.Valid
446	457	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Clr.Data
447	458	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Clr.Valid
448	459	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Tropo.Data
449	460	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Tropo.Valid
450	461	Perf_Background_Dpkg.Pscrzalt.Data
451	462	Perf_Background_Dpkg.Pscrzalt.Valid
452	463	Perf_Background_Dpkg.Pstropoalt
453	464	CTP_PERF_BKGND_PUT_BK_DATA.Clr.Data
454	465	CTP_PERF_BKGND_PUT_BK_DATA.Clr.Valid
455	466	CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec
456	467	CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec
457	468	CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec
458	469	CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec
459	470	CTP_PERF_BKGND_PUT_BK_DATA.Requestlgb_Exec
460	471	CTP_PERF_BKGND_PUT_BK_DATA.Releaselgb_Exec
461	472	CTP_PERF_BKGND_PUT_BK_DATA.Getlgbleg_Exec
462	473	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Num_GLegs
463	474	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.FixIdent
464	475	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Altaacstr
465	476	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Altabcstr
466	477	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Cnstraintspd
467	478	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Targetalt
468	479	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Cstraltlim
469	480	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Spcspd
470	481	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Fpa
471	482	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.FpaVal

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

472	483	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.PathTerm
473	484	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Clbordescstr
474	485	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Altaacstrval
475	486	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Altabcstrval
476	487	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Spcspdval
477	488	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Toosteeppath
478	489	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Fixdistodest
479	490	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Incourse
480	491	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.ISADev
481	492	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.LegDistance
482	493	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Outcourse
483	494	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Prdairstpd
484	495	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Prdalt
485	496	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Prdetatofix
486	497	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Prdgndspd
487	498	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Nextfpn
488	499	AF
489	500	FA
490	501	CLIMBSEG
491	502	Descentseg
492	503	TSPNULL
493	504	TSPtop
494	505	CAS
495	506	Mach
496	507	Perf_Background_Dpkg.Psldistodest
497	508	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().FixIdent
498	509	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Altaacstr
499	510	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Altabcstr
500	511	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Cstrspdlim
501	512	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Targetalt
502	513	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Cstraltlim
503	514	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Spcspd
504	515	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().SpcFpa
505	516	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().FpaVal
506	517	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().PathTerm
507	518	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Clbordescstr
508	519	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Altaacstrval
509	520	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Altabcstrval
510	521	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Spcspdval
511	522	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Toosteeppath
512	523	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Fixdistodest
513	524	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Incourse
514	525	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().ISADev
515	526	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().LegDistance

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

516 527 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Outcourse
517 528 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Prdairspd
518 529 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Prdairspd.VALUE
519 530 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Prdairspd.SPEED_TYPE
520 531 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Prdalt
521 532 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Prdtime
522 533 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data().Prdgndspd
523 534 Perf_Vdu_Dpkg.Vdu_Buffer.Pseudos().Included
524 535 Perf_Vdu_Dpkg.Vdu_Buffer.Pseudos().Dist
525 536 Perf_Background_Dpkg.Pcfirstlegidx
526 537 Perf_Despath_Dpkg.Pcdespath.vga().PACK.DISCON
527 538 Perf_Despath_Dpkg.Pcdespath.VGAINDXLAST
528 539 Perf_Despath_Dpkg.Pcdespath.VGAVALID
529 540 Perf_Vdu_Dpkg.Vdu_Buffer.Despath.vga().PACK.DISCON
530 541 Perf_Vdu_Dpkg.Vdu_Buffer.Despath.VGAINDXLAST
531 542 Perf_Vdu_Dpkg.Vdu_Buffer.Despath.VGAVALID
532 543 CTP_PERF_BKGND_PUT_BK_DATA.Int_To_Str_Exec
533 544 CTP_PERF_BKGND_PUT_BK_DATA.Get_Data_Save_State_Exec
534 545 CTP_PERF_BKGND_PUT_BK_DATA.Data
535 546 CTP_PERF_BKGND_PUT_BK_DATA.Num
536 547 Perf_Vdu_Dpkg.Data_Save
537 548 Perf_Vdu_Tpkg.Active
538 549 Perf_Vdu_Tpkg.Temporary
539 550 Perf_Vdu_Tpkg.None
540 551 Perf_Vdu_Tpkg.Current_Mode
541 552 Perf_Vdu_Tpkg.Secondary3
542 553 Perf_Vdu_Tpkg.Secondary2
543 554 Perf_Vdu_Tpkg.Secondary1
544 555 Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Number_Of_Points
545 556 Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Point_Data().Aircraft_State.Distance_To_Destination
546 557 Perf_Dpkg.Psbias
547 558 Verify_SDD_07059_Invalid
548 559 Verify_SDD_07063_Invalid
549 560 Base_Domain_Services_Tpkg.Spare
550 561
551 562 Common_Lgb:BODY.Header_Control.Clralt.Data
552 563 Common_Lgb:BODY.Header_Control.Clralt.Valid
553 564
554 565 Perf_Dpkg.CDA_Enabled
555 566 Options_And_Data_Pkg:body.All_Options.Cda_Enable
556 567
557 568 END_SUT_VARS
558 569
559 570 DEFAULTS

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```
560 571 Perf_Background_Dpkg.Timeconmiss_Updated := False
561 572 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg := False
562 573 END_DEFAULTS
563 574
564 575 -- NOTES:
565 576 *****
566 577 TESTID: 1
567 578
568 579 Current itinerary is Active Primary Flight plan and preds are output Active flight plan predictions refresh timer is
569 580 updated by calling Prf_Int_Utils.Update_Refresh_Timer.
570 581 (PERF_SDD_3511_INT)
571 582 Aircraft options and data shall be read in from the OPC and AMI databases upon system power-up (Cold Start).
572 583 The following data needs to be obtained:
573 584 Options_And_Data_Pkg.Final_Alt
574 585 Options_And_Data_Pkg.Final_Fuel
575 586 Options_And_Data_Pkg.Fuel_Pred_Final_Time
576 587 Options_And_Data_Pkg.Fuel_Pred_Final_Dest
577 588 Options_And_Data_Pkg.Fuel_Plng_Final_Time
578 589 Options_And_Data_Pkg.Altm_Trip_In_Rsv_Enb
579 590 Options_And_Data_Pkg.Ats_Enable
580 591 Options_And_Data_Pkg.Cmp_Rsv_In_Flt_Enb
581 592 Options_And_Data_Pkg.Route_Reserve_Percent
582 593 Options_And_Data_Pkg.Route_Reserve_Upper_Limit
583 594 Options_And_Data_Pkg.Route_Reserve_Lower_Limit
584 595 (PERF_SDD_2094_INT)
585 596 Itin is a maxalt and partition is in Dual_Slave mode.
586 597 Prf_Int_Utils.Dual_Status returns the master/slave and dual indication via a single data item based
587 598 on IO/OPS status items.
588 599 (PERF_SDD_3523_INT)
589 600 If the current itinerary is Active Primary Flight Plan Predictions, then the last flight level shall be
590 601 sent to IO for output when the flight plan has been completely predicted.
591 602 (PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))
592 603 If the predictions-output (Preds_Output) indication is true for the working flight plan, then indication
593 604 shall be stored to notify EFIS about the finish of predictions (Preds_Complete) for the working flight plan
594 605 by calling the procedure Perf_Interface_Dpkg.Put_Preds_Complete.
595 606 (PERF_SDD_5587_INT)
596 607 Options_And_Data_Pkg.Fuel_Pred_Final_Dest is equal to "P" indicating the final destination is the primary destination.
    » Then
597 608 Perf_Background_Dpkg.Pcfinaldest is set to Primary.
598 609 (PERF_SDD_5614_DR(PERF_SRD_1544_A3XX, PERF_SRD_7463))
599 610
600 611
601 612 -- INPUTS:
602 613
```



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

603	614	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr	:= 0
604	615	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx	:= 2
605	616	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change	:= False
606	617	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	:= False
607	618	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	:= False
608	619	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	:= False
609	620	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	:= False
610	621	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	:= False
611	622	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	:= False
612	623	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)	:= 2
613	624	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest	:= 0.0
614	625	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo	:= 0.0
615	626	Perf_Background_Dpkg.Opt_Step_Data.Distodest	:= 25.0
616	627	Perf_Background_Dpkg.Opt_Step_Data.Timetogo	:= 5.0
617	628	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed	:= 0.0
618	629	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel	:= 0.0
619	630	Perf_Background_Dpkg.Pshmpreddata.Speed	:= 250.0
620	631	Perf_Background_Dpkg.Pshmpreddata.Fuel	:= 50.0
621	632	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid	:= False
622	633	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data	:= 0.0
623	634	Perf_Background_Dpkg.Pcoptalt.Valid	:= True
624	635	Perf_Background_Dpkg.Pcoptalt.Data	:= 19000.0
625	636	Fmcs_Partition_Data_Pkg.Ops_Master_Status	:= Master
626	637	Ctp_Perf_bkgnd_put_bk_data.Boot_Status	:= Cold_Start
627	638	Perf_Background_Dpkg.Preds_Output(Active)	:= True
628	639	Perf_Background_Dpkg.Psfinalalt	:= 0.0
629	640	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt	:= 5000
630	641	Perf_Background_Dpkg.Psfpolfnlful	:= 0.0
631	642	Perf_Background_Dpkg.Psfpolfnltme	:= 0.0
632	643	Perf_Background_Dpkg.Psfpolfnltg	:= 0.0
633	644	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel	:= 40
634	645	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	:= 50
635	646	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Percent	:= 100.0
636	647	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Upper_Limit	:= 4.0
637	648	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Lower_Limit	:= 1.0
638	649	Options_And_Data_Pkg:body.All_Options.Ats_Enable	:= True
639	650	Options_And_Data_Pkg:body.All_Options.Alt_Trip_In_Rsv_Enb	:= True
640	651	Options_And_Data_Pkg:body.All_Options.Cmp_Rsv_In_Flt_Enb	:= True
641	652	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	:= 60
642	653	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	:= True
643	654	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	:= True
644	655	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	:= True
645	656	Perf_Background_Dpkg.Pcfpln	:= Actprimary
646	657	Perf_Background_Dpkg.Pcfltphase	:= Preflight

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

647 658 Perf_Background_Dpkg.Psfinaldes := True
648 659 Perf_Background_Dpkg.Vert_Auto_Mode := True
649 660 Perf_Background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
650 661 Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
651 662 Perf_Background_Dpkg.Maxalt.Gwt := 150000.0
652 663 Perf_Background_Dpkg.Maxalt.Num_Engout := 0
653 664 Perf_Background_Dpkg.Etp_Itin_Ran := True
654 665 Perf_Background_Dpkg.Maxalt.Maximum_Alt.Valid := False
655 666 Perf_Background_Dpkg.Maximum_Maximum_Alt.Valid := False
656 667 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
657 668 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
658 669 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
659 670 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
660 671 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
661 672 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
662 673 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
663 674 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
664 675 Perf_Background_Dpkg.Psprddataseq := 3
665 676 Perf_Dpkg.Pstopofcrzfl(Active).Data := 10.0
666 677 Perf_Dpkg.Pstopofcrzfl(Active).Valid := True
667 678 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
668 679 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
669 680 Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest := "P"
670 681 CTP_PERF_BKGND_PUT_BK_DATA.Du_Status := Perf_Int_Base_Tpkg.Dual_Master
671 682 Perf_Background_Dpkg.Ats_Enable := False
672 683 Perf_Background_Dpkg.Psrsvaltn := False
673 684 Perf_Background_Dpkg.Psrsvinflt := False
674 685 Perf_Background_Dpkg.Psrtersvpctg := 0.0
675 686 Perf_Background_Dpkg.Psmaxrtersv := 0.0
676 687 Perf_Background_Dpkg.Psminrtersv := 0.0
677 688 Perf_Background_Dpkg.Pcfinaldest := Perf_Ext_Tpkg.Alternate
678 689
679 690 CTP_PERF_BKGND_PUT_BK_DATA.Fpln := Active
680 691 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
681 692 #Change := True
682 693 #go
683 694 #end
684 695
685 696 #sba Prf_Int_Utils."Update_Refresh_Timer":BODY before_end
686 697 #go
687 698 Timer.Start_Time = 0
688 699 Timer.Refresh_Time = 0.0
689 700 Timer.Average_Refresh_Time = 0.0
690 701 Timer.Number_Of_Points = 1

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

691 702 Timer.Avg_Refresh_Time_Data(1) = 0.0
692 703
693 704 #sba Perf_Etp_Dpkg.Put_Predinprog before_end
694 705 #go
695 706 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
696 707
697 708 #sba Perf_Interface_Dpkg."Put_Preds_Complete":BODY before_end
698 709 #go
699 710 Data_Storage.Preds_Complete(Fpln) = True
700 711
701 712 #delb/all
702 713
703 714 !run_test()
704 715
705 716 -- OUTPUTS
706 717
707 718 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr = 0
708 719 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = True
709 720 Perf_Background_Dpkg.Psffinalalt = 5000.0
710 721 Perf_Background_Dpkg.Psfpolfnlful = 40.0
711 722 Perf_Background_Dpkg.Psfpolfnltme = 50.0
712 723 Perf_Background_Dpkg.Psfpolfnltg = 60.0
713 724 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec = False
714 725 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec = False
715 726 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec = False
716 727 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec = False
717 728 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec = False
718 729 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec = False
719 730 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
720 731 CTP_PERF_BKGND_PUT_BK_DATA.Du_Status = Perf_Int_Base_Tpkg.Single
721 732 Perf_Background_Dpkg.Ats_Enable = True
722 733 Perf_Background_Dpkg.Psrsvaltn = True
723 734 Perf_Background_Dpkg.Psrsvinflt = True
724 735 Perf_Background_Dpkg.Psrtersvpctg = 1.0
725 736 Perf_Background_Dpkg.Psmaxrtersv = 4.0
726 737 Perf_Background_Dpkg.Psminrtersv = 1.0
727 738 Perf_Background_Dpkg.Preds_Output(Active) = True
728 739 Perf_Background_Dpkg.Pcfinaldest = Perf_Ext_Tpkg.Primary
729 740
730 741
731 742
732 743 TESTID: 2
733 744
734 745 Initialization occurs for a warm start. Also, itin is active preds and a change occurs that causes interruption of pr

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

735      746 » eds
736      747 so no output is made.
737      748 (PERF_SDD_2631_INT,PERF_SDD_2159_INT,PERF_SDD_4543_INT,PERF_SDD_2158_INT,
738      749 PERF_SDD_2289(PERF_SRD_10253,PERF_SRD_10333_INT,PERF_SRD_12092,PERF_SRD_12093,
739      750 PERF_SRD_12094,PERF_SRD_12095,PERF_SRD_9993,PERF_SRD_9994), PERF_SDD_2094_INT)
740      751 Itin is a maxalt and partition is in Dual_Slave mode.
741      752 Prf_Int_Utills.Dual_Status is a function that shall return the master/slave and dual indication via a single data item
742      753 » based
743      754 on IO/OPS status items.
744      755 (PERF_SDD_3523_INT)
745      756 The last flight level shall be sent to IO for output when the flight plan has been completely predicted.
746      757 (PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))
747      758 If the scratch flight plan is not being used, the predictions-output indication shall be set
748      759 according to Table 11.14-4.
749      760 In this case Predictions_Output is set to TRUE
750      761 (PERF_SDD_4544_INT)
751      762
752      763 -- INPUTS:
753      764
754      765 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
755      766 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
756      767 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
757      768 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
758      769 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
759      770 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
760      771 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
761      772 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
762      773 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
763      774 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
764      775 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
765      776 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
766      777 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
767      778 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
768      779 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
769      780 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
770      781 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
771      782 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
772      783 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
773      784 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
774      785 Perf_Background_Dpkg.Pcoptalt.Valid := True
775      786 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
776      787 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

777 788 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
778 789 Perf_Background_Dpkg.Preds_Output(Active) := True
779 790 Perf_Background_Dpkg.Psfinalalt := 0.0
780 791 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
781 792 Perf_Background_Dpkg.Psfpolfnlful := 0.0
782 793 Perf_Background_Dpkg.Psfpolfnltme := 0.0
783 794 Perf_Background_Dpkg.Psfpolfnltg := 0.0
784 795 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
785 796 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
786 797 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
787 798 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Percent := 100.0
788 799 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Upper_Limit := 4.0
789 800 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Lower_Limit := 1.0
790 801 Options_And_Data_Pkg:body.All_Options.Ats_Enable := True
791 802 Options_And_Data_Pkg:body.All_Options.Alt_Trip_In_Rsv_Enb := True
792 803 Options_And_Data_Pkg:body.All_Options.Cmp_Rsv_In_Flt_Enb := True
793 804 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
794 805 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
795 806 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
796 807 Perf_Background_Dpkg.Pcfpln := Scratchfpln
797 808 Perf_Background_Dpkg.Pcfltphase := Cruise
798 809 Perf_Background_Dpkg.Psfinaldes := True
799 810 Perf_Background_Dpkg.Vert_Auto_Mode := True
800 811 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
801 812 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
802 813 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
803 814 Perf_background_Dpkg.Maxalt.Num_Engout := 0
804 815 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
805 816 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
806 817 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
807 818 Perf_Dpkg.Pstopofcrzfl(Active).Valid := True
808 819 Perf_Dpkg.Pstopofcrzfl(Active).Data := 10.0
809 820 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
810 821 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
811 822 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
812 823 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
813 824 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
814 825 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
815 826 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
816 827 Perf_Background_Dpkg.Psprddataseq := 3
817 828 Perf_Background_Dpkg.Etp_Itin_Ran := False
818 829 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
819 830 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
820 831 Ctp_Perf_Bkgnd_Put_Bk_Data.Out_Gleg.Spalt1 := 0.0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

821	832	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Spalt1 := 2.0
822	833	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status := Perf_Int_Base_Tpkg.Dual_Master
823	834	Perf_Background_Dpkg.Ats_Enable := False
824	835	Perf_Background_Dpkg.Psrsvaltl := False
825	836	Perf_Background_Dpkg.Psrsvinflt := False
826	837	Perf_Background_Dpkg.Psrtersvpctg := 0.0
827	838	Perf_Background_Dpkg.Psmaxrtersv := 0.0
828	839	Perf_Background_Dpkg.Psminrtersv := 0.0
829	840	
830	841	#sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
831	842	#Change := False
832	843	#go
833	844	#end
834	845	#delb/all
835		<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #412</del>
	846	<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #414</del>
836	847	#go
837	848	#Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
838	849	#Chk_Idx := Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx
839	850	#delb/all
840	851	!run_test()
841	852	
842	853	-- OUTPUTS
843	854	
844	855	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr = 36
845	856	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = True
846	857	Perf_Background_Dpkg.Psfinalalt = 0.0
847	858	Perf_Background_Dpkg.Psfpolfnlful = 0.0
848	859	Perf_Background_Dpkg.Psfpolfnltme = 0.0
849	860	Perf_Background_Dpkg.Psfpolfnltg = 0.0
850	861	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec = False
851	862	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec = False
852	863	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec = False
853	864	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec = False
854	865	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec = False
855	866	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec = False
856	867	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt = 2
857	868	Ctp_Perf_Bkgnd_Put_Bk_Data.Out_Gleg.Spalt1 = 2.0
858	869	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = True
859	870	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status = Perf_Int_Base_Tpkg.Single
860	871	Perf_Background_Dpkg.Ats_Enable = False
861	872	Perf_Background_Dpkg.Psrsvaltl = False
862	873	Perf_Background_Dpkg.Psrsvinflt = False
863	874	Perf_Background_Dpkg.Psrtersvpctg = 0.0

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

864      875 Perf_Background_Dpkg.Psmaxrtersv = 0.0
865      876 Perf_Background_Dpkg.Psminrtersv = 0.0
866      877 Perf_Background_Dpkg.Preds_Output(Active) = True
867      878
868      879
869      880 TESTID: 3
870      881
871      882 Verification when FM in in dual mode and when Itin is a maxalt then maximum altitude data from Master FM is imposed
872      883 to Slave FM which keeps the Max Alt data synchronised between two FMs.
873      884 (PERF_SDD_2096 (PERF_SRD_2020))
874      885
875      886
876      887 -- INPUTS:
877      888
878      889 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
879      890 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
880      891 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
881      892 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
882      893 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
883      894 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
884      895 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
885      896 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
886      897 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
887      898 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
888      899 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
889      900 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
890      901 Perf_Etp_Dpkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
891      902 Perf_Background_Dpkg.Opt_Step_Data.Distodest := 25.0
892      903 Perf_Background_Dpkg.Opt_Step_Data.Timetogo := 5.0
893      904 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
894      905 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
895      906 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
896      907 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
897      908 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
898      909 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
899      910 Perf_Background_Dpkg.Pcoptalt.Valid := True
900      911 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
901      912 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
902      913 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
903      914 Perf_Background_Dpkg.Preds_Output(Active) := True
904      915 Perf_Background_Dpkg.Psfinalalt := 0.0
905      916 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
906      917 Perf_Background_Dpkg.Psfpolfnlful := 0.0
907      918 Perf_Background_Dpkg.Psfpolfnltme := 0.0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

908 919 Perf_Background_Dpkg.Psfpolfnltg := 0.0
909 920 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
910 921 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
911 922 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
912 923 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
913 924 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
914 925 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
915 926 Perf_Background_Dpkg.Pcfpln := Actprimary
916 927 Perf_Background_Dpkg.Pcfltphase := Cruise
917 928 Perf_Background_Dpkg.Psfinaldes := True
918 929 Perf_Background_Dpkg.Vert_Auto_Mode := True
919 930 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
920 931 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
921 932 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
922 933 Perf_background_Dpkg.Maxalt.Num_Engout := 0
923 934 Perf_Background_Dpkg.Etp_Itin_Ran := True
924 935 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
925 936 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
926 937 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
927 938 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
928 939 Perf_Background_Dpkg.Pcitin.Itinerary := Maxalt
929 940 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
930 941 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
931 942 Perf_Background_Dpkg.Pcgmttime.Gpc_Time := 2
932 943 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
933 944 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
934 945 Perf_Background_Dpkg.Psprddataseq := 3
935 946 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
936 947 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
937 948 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
938 949 #Change := False
939 950 #go
940 951 #end
941 952 #delb/all
942 953
943 954 !run_test()
944 955
945 956 -- OUTPUTS
946 957
947 958 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = True
948 959 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec = False
949 960 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec = False
950 961 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec = False
951 962 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec = False

```



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

952 963 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec = False
953 964 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec = False
954 965 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
955 966
956 967
957 968 TESTID: 4
958 969
959 970 Itin is a maxalt and partition is in dual mode so information needs to be passed from Master to Slave.
960 971 Verify Maximum Alt data is transimitted from master to slave.
961 972 (PERF_SDD_2096 (PERF_SRD_2020))
962 973 This trnsmission of data is done by the procedure Prf_Maxalt_Dpkg.Transmit_Dual_Data.
963 974 (PERF_SDD_2417_INT)
964 975 The validity flags for Max Max Alt, Rec Max Alt and Eng Out Max Alt shall be output on the Flight Test Bus
965 976 in re-packed format whenever Prf_Maxalt_Dpkg.Put_Maximum_Altitude_Data is called.
966 977 (PERF_SDD_3680_INT,PERF_SDD_2414_INT,PERF_SDD_2407_INT)
967 978 Prf_Int_Utils.Dual_Status is a function that shall return the master/slave and dual indication via a single data item
    » based
968 979 on IO/OPS status items.
969 980 (PERF_SDD_3523_INT)
970 981
971 982
972 983 -- INPUTS:
973 984
974 985 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
975 986 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
976 987 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
977 988 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
978 989 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
979 990 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
980 991 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
981 992 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
982 993 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
983 994 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
984 995 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
985 996 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
986 997 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
987 998 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
988 999 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
989 1000 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
990 1001 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
991 1002 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
992 1003 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
993 1004 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
994 1005 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

995 1006 Perf_Background_Dpkg.Pcoptalt.Valid := True
996 1007 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
997 1008 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
998 1009 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
999 1010 Perf_Background_Dpkg.Preds_Output(Active) := True
1000 1011 Perf_Background_Dpkg.Psfinalalt := 0.0
1001 1012 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
1002 1013 Perf_Background_Dpkg.Psfpolfnlful := 0.0
1003 1014 Perf_Background_Dpkg.Psfpolfnltme := 0.0
1004 1015 Perf_Background_Dpkg.Psfpolfnltg := 0.0
1005 1016 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
1006 1017 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
1007 1018 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
1008 1019 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
1009 1020 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
1010 1021 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
1011 1022 Perf_Background_Dpkg.Pcfpln := Actprimary
1012 1023 Perf_Background_Dpkg.Pcfltphase := Cruise
1013 1024 Perf_Background_Dpkg.Psfinaldes := True
1014 1025 Perf_Background_Dpkg.Vert_Auto_Mode := True
1015 1026 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
1016 1027 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
1017 1028 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
1018 1029 Perf_background_Dpkg.Maxalt.Num_Engout := 1
1019 1030 Perf_Background_Dpkg.Etp_Itin_Ran := True
1020 1031 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := True
1021 1032 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := True
1022 1033 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Dual
1023 1034 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
1024 1035 Perf_Background_Dpkg.Pcitin.Itinerary := Maxalt
1025 1036 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
1026 1037 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
1027 1038 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
1028 1039 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
1029 1040 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
1030 1041 Perf_Background_Dpkg.Psprddataseq := 3
1031 1042 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
1032 1043 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
1033 1044 Perf_Background_Dpkg.Maxalt.Maximum_Alt.Valid := True
1034 1045 Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := True
1035 1046 Perf_Background_Dpkg.Maxalt.Eo_Maximum_Alt.Valid := True
1036 1047 Perf_Flight_Test_Dpkg.Perf_Repack_Data.Maxalt_Valid := False
1037 1048 Perf_Flight_Test_Dpkg.Perf_Repack_Data.Max_Maxalt_Valid := False
1038 1049 Perf_Flight_Test_Dpkg.Perf_Repack_Data.Engine_Out_Maxalt_Valid := False

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1039 1050 Perf_Dual_Dpkg.Maxalt.Maximum_Alt := 0.0
1040 1051 Perf_Dual_Dpkg.Maxalt.Maximum_Maximum_Alt := 0.0
1041 1052 Perf_Dual_Dpkg.Maxalt.Gwt := 0.0
1042 1053 Perf_Dual_Dpkg.Maxalt.Engines_Out := 0
1043 1054 Perf_Dual_Dpkg.Maxalt.Valid := False
1044 1055 CTP_PERF_BKGND_PUT_BK_DATA.Du_Status := Perf_Int_Base_Tpkg.Single
1045 1056
1046 1057 --!
1047 1058 --! Break point setup to check the saving of the maxalt validity flags.
1048 1059 --!
1049 1060 # sba Prf_Maxalt_Dpkg.Put_Maximum_Altitude_Data before_end begin
1050 1061   Perf_Flight_Test_Dpkg.Perf_Repack_Data.Maxalt_Valid = Data.Maximum_Alt.Valid
1051 1062   Perf_Flight_Test_Dpkg.Perf_Repack_Data.Max_Maxalt_Valid = Data.Maximum_Maximum_Alt.Valid
1052 1063   Perf_Flight_Test_Dpkg.Perf_Repack_Data.Engine_Out_Maxalt_Valid = Data.Eo_Maximum_Alt.Valid
1053 1064 # go
1054 1065 # end
1055 1066 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
1056 1067 #Change := False
1057 1068 #go
1058 1069 #end
1059 1070 #delb/all
1060 1071
1061 1072 !run_test()
1062 1073
1063 1074 -- OUTPUTS
1064 1075
1065 1076 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = True
1066 1077 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec = False
1067 1078 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec = False
1068 1079 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec = False
1069 1080 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec = False
1070 1081 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec = False
1071 1082 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec = False
1072 1083 Perf_Dual_Dpkg.Maxalt.Maximum_Alt = 50000.0
1073 1084 Perf_Dual_Dpkg.Maxalt.Maximum_Maximum_Alt = 55000.0
1074 1085 Perf_Dual_Dpkg.Maxalt.Gwt = 150000.0
1075 1086 Perf_Dual_Dpkg.Maxalt.Engines_Out = 1
1076 1087 Perf_Dual_Dpkg.Maxalt.Valid = True
1077 1088 CTP_PERF_BKGND_PUT_BK_DATA.Putperflag = False
1078 1089 CTP_PERF_BKGND_PUT_BK_DATA.Du_Status = Perf_Int_Base_Tpkg.Dual_Master
1079 1090
1080 1091
1081 1092 TESTID: 5
1082 1093

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1083 1094 Itin is Fuelpredact.Block fuel has not become pilot entered then following routines Put_Pcaltnpreds, Put_Pctriptime,
1084 1095 Put_Final_Fuel, and Put_Route_Reserve are called to output data for display.
1085 1096 PERF_SDD_1826(PERF_SRD_10167_INT), PERF_SDD_1831(PERF_SRD_10167_INT)
1086 1097
1087 1098
1088 1099 -- INPUTS:
1089 1100
1090 1101 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
1091 1102 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
1092 1103 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
1093 1104 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
1094 1105 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
1095 1106 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
1096 1107 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
1097 1108 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
1098 1109 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
1099 1110 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
1100 1111 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
1101 1112 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
1102 1113 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
1103 1114 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
1104 1115 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
1105 1116 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
1106 1117 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
1107 1118 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
1108 1119 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
1109 1120 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
1110 1121 Perf_Background_Dpkg.Pcoptalt.Valid := True
1111 1122 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
1112 1123 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
1113 1124 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
1114 1125 Perf_Background_Dpkg.Preds_Output(Active) := True
1115 1126 Perf_Background_Dpkg.Psfinalalt := 0.0
1116 1127 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
1117 1128 Perf_Background_Dpkg.Psfpolfnlful := 0.0
1118 1129 Perf_Background_Dpkg.Psfpolfnltme := 0.0
1119 1130 Perf_Background_Dpkg.Psfpolfnltg := 0.0
1120 1131 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
1121 1132 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
1122 1133 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
1123 1134 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
1124 1135 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
1125 1136 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
1126 1137 Perf_Background_Dpkg.Pcfpln := Actprimary

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1127 1138 Perf_Background_Dpkg.Pcfltphase := Cruise
1128 1139 Perf_Background_Dpkg.Psfinaldes := True
1129 1140 Perf_Background_Dpkg.Vert_Auto_Mode := True
1130 1141 Perf_Background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
1131 1142 Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
1132 1143 Perf_Background_Dpkg.Maxalt.Gwt := 150000.0
1133 1144 Perf_Background_Dpkg.Maxalt.Num_Engout := 0
1134 1145 Perf_Background_Dpkg.Etp_Itin_Ran := True
1135 1146 Perf_Background_Dpkg.Maxalt.Maximum_Alt.Valid := False
1136 1147 Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
1137 1148 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
1138 1149 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
1139 1150 Perf_Background_Dpkg.Pcitin.Itinerary := Fuelpredact
1140 1151 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
1141 1152 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
1142 1153 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
1143 1154 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
1144 1155 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
1145 1156 Perf_Background_Dpkg.Psprddataseq := 3
1146 1157 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
1147 1158 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
1148 1159 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
1149 1160 #Change := False
1150 1161 #go
1151 1162 #end
1152 1163 #delb/all
1153 1164
1154 1165 !run_test()
1155 1166
1156 1167 -- OUTPUTS
1157 1168
1158 1169 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = True
1159 1170 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec = True
1160 1171 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec = True
1161 1172 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec = False
1162 1173 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec = True
1163 1174 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec = True
1164 1175 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec = False
1165 1176 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
1166 1177
1167 1178
1168 1179 TESTID: 6
1169 1180
1170 1181 Itin is Fuelplanact2. Block fuel has not become pilot entered then following routines Put_Pcaltnpreds, Put_Pctriptime,

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1171 1182 Put_Final_Fuel, and Put_Route_Reserve are called to output data for display. Block Fuel is also outputed for display
1172 1183 via Put_Block_Fuel given not pilot entered.
1173 1184 PERF_SDD_1826(PERF_SRD_10167_INT), PERF_SDD_1831(PERF_SRD_10167_INT)
1174 1185
1175 1186
1176 1187 -- INPUTS:
1177 1188
1178 1189 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
1179 1190 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
1180 1191 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
1181 1192 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
1182 1193 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
1183 1194 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
1184 1195 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
1185 1196 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
1186 1197 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
1187 1198 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
1188 1199 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
1189 1200 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
1190 1201 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
1191 1202 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
1192 1203 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
1193 1204 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
1194 1205 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
1195 1206 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
1196 1207 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
1197 1208 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
1198 1209 Perf_Background_Dpkg.Pcoptalt.Valid := True
1199 1210 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
1200 1211 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
1201 1212 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
1202 1213 Perf_Background_Dpkg.Preds_Output(Active) := True
1203 1214 Perf_Background_Dpkg.Psfinalalt := 0.0
1204 1215 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
1205 1216 Perf_Background_Dpkg.Psfpolfnlful := 0.0
1206 1217 Perf_Background_Dpkg.Psfpolfnltme := 0.0
1207 1218 Perf_Background_Dpkg.Psfpolfnltg := 0.0
1208 1219 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
1209 1220 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
1210 1221 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
1211 1222 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
1212 1223 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
1213 1224 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
1214 1225 Perf_Background_Dpkg.Pcfpln := Actprimary

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1215 1226 Perf_Background_Dpkg.Pcfltphase := Cruise
1216 1227 Perf_Background_Dpkg.Psfinaldes := True
1217 1228 Perf_Background_Dpkg.Vert_Auto_Mode := True
1218 1229 Perf_Background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
1219 1230 Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
1220 1231 Perf_Background_Dpkg.Maxalt.Gwt := 150000.0
1221 1232 Perf_Background_Dpkg.Maxalt.Num_Engout := 0
1222 1233 Perf_Background_Dpkg.Etp_Itin_Ran := True
1223 1234 Perf_Background_Dpkg.Maxalt.Maximum_Alt.Valid := False
1224 1235 Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
1225 1236 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
1226 1237 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
1227 1238 Perf_Background_Dpkg.Pcitin.Itinerary := Fuelplanact2
1228 1239 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
1229 1240 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
1230 1241 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
1231 1242 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
1232 1243 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
1233 1244 Perf_Background_Dpkg.Psprddataseq := 3
1234 1245 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
1235 1246 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
1236 1247 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
1237 1248 #Change := False
1238 1249 #go
1239 1250 #end
1240 1251 #delb/all
1241 1252
1242 1253 !run_test()
1243 1254
1244 1255 -- OUTPUTS
1245 1256
1246 1257 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = True
1247 1258 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec = True
1248 1259 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec = True
1249 1260 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec = True
1250 1261 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec = True
1251 1262 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec = True
1252 1263 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec = False
1253 1264 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
1254 1265
1255 1266
1256 1267 TESTID: 7
1257 1268
1258 1269 Itin is Optalt so opt crz alt is outputed for display via Put_Cdoptalt.

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1259 1270 (PERF_SDD_2109_INT)
1260 1271 The current itinerary is Optimum Altitude Predictions, optimum cruise altitude is copied to Perf interface Optimum alt
      » itude.
1261 1272 (PERF_SDD_4220_INT)
1262 1273
1263 1274
1264 1275 -- INPUTS:
1265 1276
1266 1277 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
1267 1278 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
1268 1279 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
1269 1280 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
1270 1281 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
1271 1282 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
1272 1283 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
1273 1284 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
1274 1285 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
1275 1286 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
1276 1287 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
1277 1288 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
1278 1289 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
1279 1290 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
1280 1291 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
1281 1292 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
1282 1293 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
1283 1294 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
1284 1295 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
1285 1296 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
1286 1297 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
1287 1298 Perf_Background_Dpkg.Pcoptalt.Valid := True
1288 1299 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
1289 1300 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
1290 1301 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
1291 1302 Perf_Background_Dpkg.Preds_Output(Active) := True
1292 1303 Perf_Background_Dpkg.Psfinalalt := 0.0
1293 1304 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
1294 1305 Perf_Background_Dpkg.Psfpolfnlful := 0.0
1295 1306 Perf_Background_Dpkg.Psfpolfnltme := 0.0
1296 1307 Perf_Background_Dpkg.Psfpolfnltg := 0.0
1297 1308 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
1298 1309 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
1299 1310 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
1300 1311 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
1301 1312 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True

```



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1302 1313 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
1303 1314 Perf_Background_Dpkg.Pcfpln := Actprimary
1304 1315 Perf_Background_Dpkg.Pcfltphase := Cruise
1305 1316 Perf_Background_Dpkg.Psfinaldes := True
1306 1317 Perf_Background_Dpkg.Vert_Auto_Mode := True
1307 1318 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
1308 1319 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
1309 1320 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
1310 1321 Perf_background_Dpkg.Maxalt.Num_Engout := 0
1311 1322 Perf_Background_Dpkg.Etp_Itin_Ran := True
1312 1323 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
1313 1324 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
1314 1325 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
1315 1326 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
1316 1327 Perf_Background_Dpkg.Pcitin.Itinerary := Optalt
1317 1328 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
1318 1329 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
1319 1330 Perf_Background_Dpkg.Pcgmttime.Gpc_Time := 2
1320 1331 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
1321 1332 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
1322 1333 Perf_Background_Dpkg.Psprddataseq := 3
1323 1334 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
1324 1335 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
1325 1336 Perf_Dpkg.Optimum_Altitude.Data := 0.0
1326 1337 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
1327 1338 #Change := False
1328 1339 #go
1329 1340 #end
1330 1341 #delb/all
1331 1342
1332 1343 !run_test()
1333 1344
1334 1345 -- OUTPUTS
1335 1346
1336 1347 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = True
1337 1348 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec = False
1338 1349 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec = False
1339 1350 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec = False
1340 1351 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec = False
1341 1352 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec = False
1342 1353 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest = 0.0
1343 1354 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo = 0.0
1344 1355 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec = False
1345 1356 Ctp_Perf_bkgnd_put_bk_data.Pshmpredata.Speed = 0.0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1346 1357 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel = 0.0
1347 1358 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid = True
1348 1359 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data = 19000.0
1349 1360 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
1350 1361 Perf_Dpkg.Optimum_Altitude.Data = 19000.0
1351 1362
1352 1363
1353 1364 TESTID: 8
1354 1365
1355 1366 Itin is optimum step so optimum step data is outputed for display via Put_Optimum_Step
1356 1367 (PERF_SDD_2113_INT)
1357 1368
1358 1369
1359 1370 -- INPUTS:
1360 1371 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
1361 1372 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
1362 1373 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
1363 1374 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
1364 1375 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
1365 1376 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
1366 1377 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
1367 1378 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
1368 1379 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
1369 1380 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
1370 1381 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
1371 1382 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
1372 1383 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
1373 1384 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
1374 1385 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
1375 1386 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
1376 1387 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
1377 1388 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
1378 1389 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
1379 1390 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
1380 1391 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
1381 1392 Perf_Background_Dpkg.Pcoptalt.Valid := True
1382 1393 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
1383 1394 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
1384 1395 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
1385 1396 Perf_Background_Dpkg.Preds_Output(Active) := True
1386 1397 Perf_Background_Dpkg.Psfinalalt := 0.0
1387 1398 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
1388 1399 Perf_Background_Dpkg.Psfpolfnlful := 0.0
1389 1400 Perf_Background_Dpkg.Psfpolfnltme := 0.0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1390 1401 Perf_Background_Dpkg.Psfpolfnltg := 0.0
1391 1402 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
1392 1403 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
1393 1404 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
1394 1405 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
1395 1406 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
1396 1407 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
1397 1408 Perf_Background_Dpkg.Pcfpln := Actprimary
1398 1409 Perf_Background_Dpkg.Pcfltphase := Cruise
1399 1410 Perf_Background_Dpkg.Psfinaldes := True
1400 1411 Perf_Background_Dpkg.Vert_Auto_Mode := True
1401 1412 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
1402 1413 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
1403 1414 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
1404 1415 Perf_background_Dpkg.Maxalt.Num_Engout := 0
1405 1416 Perf_Background_Dpkg.Etp_Itin_Ran := True
1406 1417 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
1407 1418 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
1408 1419 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
1409 1420 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
1410 1421 Perf_Background_Dpkg.Pcitin.Itinerary := Optimum_step
1411 1422 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
1412 1423 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
1413 1424 Perf_Background_Dpkg.Pcgmttime.Gpc_Time := 2
1414 1425 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
1415 1426 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
1416 1427 Perf_Background_Dpkg.Psprddataseq := 3
1417 1428 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
1418 1429 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
1419 1430 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
1420 1431 #Change := False
1421 1432 #go
1422 1433 #end
1423 1434 #delb/all
1424 1435
1425 1436 !run_test()
1426 1437
1427 1438 -- OUTPUTS
1428 1439
1429 1440 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = True
1430 1441 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec = False
1431 1442 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec = False
1432 1443 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec = False
1433 1444 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec = False

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1434 1445 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec = False
1435 1446 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest = 25.0
1436 1447 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo = 5.0
1437 1448 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec = False
1438 1449 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed = 0.0
1439 1450 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel = 0.0
1440 1451 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid = False
1441 1452 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data = 0.0
1442 1453 CTP_PERF_BKGND_PUT_BK_DATA.Putperflag = False
1443 1454
1444 1455
1445 1456
1446 1457 TESTID: 9
1447 1458
1448 1459 Itin is Manual Hold Predictions so hold predictions shall be output to CDU and LG via Put_Hm_Preds.
1449 1460 (PERF_SDD_2436 (PERF_SRD_2071,PERF_SRD_2087_INT))
1450 1461
1451 1462
1452 1463 -- INPUTS:
1453 1464
1454 1465 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
1455 1466 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
1456 1467 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
1457 1468 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
1458 1469 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
1459 1470 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
1460 1471 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
1461 1472 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
1462 1473 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
1463 1474 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
1464 1475 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
1465 1476 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
1466 1477 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
1467 1478 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
1468 1479 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
1469 1480 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
1470 1481 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
1471 1482 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
1472 1483 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
1473 1484 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
1474 1485 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
1475 1486 Perf_Background_Dpkg.Pcoptalt.Valid := True
1476 1487 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
1477 1488 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1478 1489 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
1479 1490 Perf_Background_Dpkg.Preds_Output(Active) := True
1480 1491 Perf_Background_Dpkg.Psfinalalt := 0.0
1481 1492 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
1482 1493 Perf_Background_Dpkg.Psfpolfnlful := 0.0
1483 1494 Perf_Background_Dpkg.Psfpolfnltme := 0.0
1484 1495 Perf_Background_Dpkg.Psfpolfnltg := 0.0
1485 1496 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
1486 1497 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
1487 1498 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
1488 1499 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
1489 1500 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
1490 1501 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
1491 1502 Perf_Background_Dpkg.Pcfpln := Actprimary
1492 1503 Perf_Background_Dpkg.Pcfltphase := Cruise
1493 1504 Perf_Background_Dpkg.Psfinaldes := True
1494 1505 Perf_Background_Dpkg.Vert_Auto_Mode := True
1495 1506 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
1496 1507 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
1497 1508 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
1498 1509 Perf_background_Dpkg.Maxalt.Num_Engout := 0
1499 1510 Perf_Background_Dpkg.Etp_Itin_Ran := True
1500 1511 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
1501 1512 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
1502 1513 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
1503 1514 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
1504 1515 Perf_Background_Dpkg.Pcitin.Itinerary := Holdactv
1505 1516 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
1506 1517 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
1507 1518 Perf_Background_Dpkg.Pcgmttime.Gpc_Time := 2
1508 1519 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
1509 1520 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
1510 1521 Perf_Background_Dpkg.Psprddataseq := 3
1511 1522 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
1512 1523 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
1513 1524 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
1514 1525 #Change := False
1515 1526 #go
1516 1527 #end
1517 1528 #delb/all
1518 1529
1519 1530 !run_test()
1520 1531
1521 1532 -- OUTPUTS

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1522 1533 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = True
1523 1534 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec = False
1524 1535 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec = False
1525 1536 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec = False
1526 1537 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec = False
1527 1538 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec = False
1528 1539 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest = 0.0
1529 1540 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo = 0.0
1530 1541 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec = True
1531 1542 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed = 250.0
1532 1543 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel = 50.0
1533 1544 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid = False
1534 1545 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data = 0.0
1535 1546 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
1536 1547
1537 1548
1538 1549 TESTID: 10
1539 1550
1540 1551 Itin is Secprim so legs are outputed to buffers.
1541 1552 (PERF_SDD_2631_INT,PERF_SDD_2159_INT,PERF_SDD_4543_INT,PERF_SDD_2158_INT)
1542 1553 If the scratch flight plan is not being used, the predictions-output indication shall be set
1543 1554 according to Table 11.14-4.
1544 1555 In this case predictions-output is set to true
1545 1556 (PERF_SDD_4544_INT)
1546 1557
1547 1558
1548 1559
1549 1560 -- INPUTS:
1550 1561
1551 1562 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
1552 1563 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
1553 1564 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
1554 1565 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
1555 1566 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
1556 1567 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
1557 1568 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
1558 1569 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
1559 1570 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
1560 1571 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
1561 1572 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
1562 1573 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
1563 1574 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
1564 1575 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
1565 1576 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1566 1577 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
1567 1578 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
1568 1579 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
1569 1580 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
1570 1581 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
1571 1582 Perf_Background_Dpkg.Pcoptalt.Valid := True
1572 1583 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
1573 1584 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
1574 1585 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
1575 1586 Perf_Background_Dpkg.Preds_Output(Active) := True
1576 1587 Perf_Background_Dpkg.Psfinalalt := 0.0
1577 1588 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
1578 1589 Perf_Background_Dpkg.Psfpolfnlful := 0.0
1579 1590 Perf_Background_Dpkg.Psfpolfnltme := 0.0
1580 1591 Perf_Background_Dpkg.Psfpolfnltg := 0.0
1581 1592 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
1582 1593 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
1583 1594 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
1584 1595 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
1585 1596 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
1586 1597 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
1587 1598 Perf_Background_Dpkg.Pcfpln := Scratchfpln
1588 1599 Perf_Background_Dpkg.Pcfltphase := Cruise
1589 1600 Perf_Background_Dpkg.Psfinaldes := True
1590 1601 Perf_Background_Dpkg.Vert_Auto_Mode := True
1591 1602 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
1592 1603 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
1593 1604 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
1594 1605 Perf_background_Dpkg.Maxalt.Num_Engout := 0
1595 1606 Perf_Background_Dpkg.Etp_Itin_Ran := True
1596 1607 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
1597 1608 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
1598 1609 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
1599 1610 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
1600 1611 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
1601 1612 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
1602 1613 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
1603 1614 Perf_Background_Dpkg.Pcgmttime.Gpc_Time := 2
1604 1615 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
1605 1616 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
1606 1617 Perf_Background_Dpkg.Psprddataseq := 3
1607 1618 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
1608 1619 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
1609 1620

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

1610	1621	#sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
1611	1622	#Change := False
1612	1623	#go
1613	1624	#end
1614	1625	#delb/all
1615	1626	
1616		#sba PRF_BKGND_PKG.PUT_BK_DATA #412
	1627	#sba PRF_BKGND_PKG.PUT_BK_DATA #414
1617	1628	#go
1618	1629	#Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
1619	1630	#Chk_Idx := Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx
1620	1631	#delb/all
1621	1632	#sba Perf_Etp_Dpkg.Put_Predinprog before_end
1622	1633	#go
1623	1634	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
1624	1635	
1625	1636	!run_test()
1626	1637	
1627	1638	-- OUTPUTS
1628	1639	
1629	1640	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr = 36
1630	1641	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = True
1631	1642	Perf_Background_Dpkg.Psfinalalt = 0.0
1632	1643	Perf_Background_Dpkg.Psfpolfnlful = 0.0
1633	1644	Perf_Background_Dpkg.Psfpolfnltme = 0.0
1634	1645	Perf_Background_Dpkg.Psfpolfnltg = 0.0
1635	1646	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec = False
1636	1647	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec = False
1637	1648	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec = False
1638	1649	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec = False
1639	1650	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec = False
1640	1651	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec = False
1641	1652	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = True
1642	1653	Perf_Background_Dpkg.Preds_Output(Active) = True
1643	1654	
1644	1655	
1645	1656	TESTID: 11
1646	1657	
1647	1658	Itin is Fuelplansec but preds are not outputed for display because put_block_fuel is given as pilot entered.
1648	1659	PERF_SDD_1826(PERF_SRD_10167_INT), PERF_SDD_1831(PERF_SRD_10167_INT)
1649	1660	
1650	1661	
1651	1662	-- INPUTS:
1652	1663	



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1653 1664 Perf_Background_Dpkg.Vert_Auto_Mode := True
1654 1665 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
1655 1666 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
1656 1667 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := True
1657 1668 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
1658 1669 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
1659 1670 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
1660 1671 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
1661 1672 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
1662 1673 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
1663 1674 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
1664 1675 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
1665 1676 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
1666 1677 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
1667 1678 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
1668 1679 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
1669 1680 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
1670 1681 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
1671 1682 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
1672 1683 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
1673 1684 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
1674 1685 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
1675 1686 Perf_Background_Dpkg.Pcoptalt.Valid := True
1676 1687 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
1677 1688 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
1678 1689 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
1679 1690 Perf_Background_Dpkg.Preds_Output(Active) := True
1680 1691 Perf_Background_Dpkg.Psfinalalt := 0.0
1681 1692 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
1682 1693 Perf_Background_Dpkg.Psfpolfnlful := 0.0
1683 1694 Perf_Background_Dpkg.Psfpolfnltme := 0.0
1684 1695 Perf_Background_Dpkg.Psfpolfnltg := 0.0
1685 1696 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
1686 1697 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
1687 1698 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
1688 1699 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
1689 1700 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
1690 1701 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
1691 1702 Perf_Background_Dpkg.Pcfpln := Actprimary
1692 1703 Perf_Background_Dpkg.Pcfltphase := Cruise
1693 1704 Perf_Background_Dpkg.Psfinaldes := True
1694 1705 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
1695 1706 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
1696 1707 Perf_background_Dpkg.Maxalt.Gwt := 150000.0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1697 1708 Perf_background_Dpkg.Maxalt.Num_Engout := 0
1698 1709 Perf_Background_Dpkg.Etp_Itin_Ran := True
1699 1710 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
1700 1711 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
1701 1712 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
1702 1713 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
1703 1714 Perf_Background_Dpkg.Pcitin.Itinerary := Fuelplanact2
1704 1715 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
1705 1716 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
1706 1717 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
1707 1718 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
1708 1719 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
1709 1720 Perf_Background_Dpkg.Psprddataseq := 3
1710 1721 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
1711 1722 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
1712 1723 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
1713 1724 #Change := False
1714 1725 #go
1715 1726 #end
1716 1727 #delb/all
1717 1728
1718 1729 !run_test()
1719 1730
1720 1731 -- OUTPUTS
1721 1732 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr = 0
1722 1733 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = True
1723 1734 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec = False
1724 1735 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec = False
1725 1736 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec = False
1726 1737 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec = False
1727 1738 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec = False
1728 1739 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec = False
1729 1740 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
1730 1741
1731 1742
1732 1743 TESTID: 12
1733 1744
1734 1745 Itin is active primary but Src_Idx does not equal the Chk_Idx so information is not outputed.
1735 1746 (PERF_SDD_2631_INT)
1736 1747 The ETP predictions-in-progress flag shall be set to false if all of the following conditions are met
1737 1748     1)the current itinerary is the Active Primary Flight Plan Predictions
1738 1749     2)the ETP-itinerary-has-run flag is True
1739 1750 The ETP-itinerary-has-run flag is then reset to false.
1740 1751 (PERF_SDD_3155_INT)

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1741 1752
1742 1753 If the current itinerary is Active Primary Flight Plan Predictions, then the last Cruise flight level
1743 1754 shall be sent to IO for output when the flight plan has been completely predicted.
1744 1755 (PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))
1745 1756
1746 1757 If the scratch flight plan is not being used, the predictions-output indication shall be set
1747 1758 according to Table 11.14-4.
1748 1759 In this case Predictions_Output is set to TRUE
1749 1760 (PERF_SDD_4544_INT)
1750 1761
1751 1762
1752 1763
1753 1764 -- INPUTS:
1754 1765
1755 1766 Perf_Background_Dpkg.Pcactorsec := Fprequestrec_Types.Temporary
1756 1767 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
1757 1768 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
1758 1769 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
1759 1770 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
1760 1771 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
1761 1772 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
1762 1773 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
1763 1774 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
1764 1775 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
1765 1776 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
1766 1777 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
1767 1778 Perf_Etp_Dpkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
1768 1779 Perf_Background_Dpkg.Opt_Step_Data.Distodest := 25.0
1769 1780 Perf_Background_Dpkg.Opt_Step_Data.Timetogo := 5.0
1770 1781 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
1771 1782 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
1772 1783 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
1773 1784 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
1774 1785 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
1775 1786 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
1776 1787 Perf_Background_Dpkg.Pcoptalt.Valid := True
1777 1788 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
1778 1789 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
1779 1790 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
1780 1791 Perf_Background_Dpkg.Preds_Output(Active) := True
1781 1792 Perf_Background_Dpkg.Psfinalalt := 0.0
1782 1793 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
1783 1794 Perf_Background_Dpkg.Psfpolfnlful := 0.0
1784 1795 Perf_Background_Dpkg.Psfpolfnltme := 0.0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

1785	1796	Perf_Background_Dpkg.Psfpolfnltg := 0.0
1786	1797	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
1787	1798	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
1788	1799	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
1789	1800	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
1790	1801	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
1791	1802	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
1792	1803	Perf_Background_Dpkg.Pcfpln := ScratchFpln
1793	1804	Perf_Background_Dpkg.Pcfltphase := Cruise
1794	1805	Perf_Background_Dpkg.Psfinaldes := True
1795	1806	Perf_Background_Dpkg.Vert_Auto_Mode := True
1796	1807	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
1797	1808	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
1798	1809	Perf_background_Dpkg.Maxalt.Gwt := 150000.0
1799	1810	Perf_background_Dpkg.Maxalt.Num_Engout := 0
1800	1811	Perf_Background_Dpkg.Etp_Itin_Ran := True
1801	1812	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
1802	1813	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
1803	1814	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
1804	1815	Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
1805	1816	Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
1806	1817	Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
1807	1818	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
1808	1819	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
1809	1820	Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
1810	1821	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
1811	1822	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
1812	1823	Perf_Background_Dpkg.Psprddataseq := 3
1813	1824	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
1814	1825	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
1815	1826	
1816	1827	
1817	1828	#sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
1818	1829	#Change := False
1819	1830	#go
1820	1831	#end
1821	1832	#delb/all
1822		<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #412</del>
	1833	#sba PRF_BKGND_PKG.PUT_BK_DATA #414
1823	1834	#go
1824	1835	#Chk_Idx := 0
1825	1836	
1826	1837	!run_test()
1827	1838	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1828 1839 -- OUTPUTS
1829 1840 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr = 0
1830 1841 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = False
1831 1842 Perf_Background_Dpkg.Etp_Itin_Ran = False
1832 1843 Perf_Background_Dpkg.Psfinalalt = 0.0
1833 1844 Perf_Background_Dpkg.Psfpolfnlful = 0.0
1834 1845 Perf_Background_Dpkg.Psfpolfnltme = 0.0
1835 1846 Perf_Background_Dpkg.Psfpolfnltg = 0.0
1836 1847 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
1837 1848 Perf_Background_Dpkg.Preds_Output(Active) = True
1838 1849 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec = False
1839 1850 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec = False
1840 1851 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec = False
1841 1852
1842 1853 TESTID: 13
1843 1854
1844 1855 Itin is active primary and flight phase is descent which is after cruise so 4 different perf legs are not outputed.
1845 1856 (PERF_SDD_2631_INT,PERF_SDD_2159_INT,PERF_SDD_4543_INT,PERF_SDD_2158_INT,
1846 1857 PERF_SDD_2289(PERF_SRD_10253,PERF_SRD_10333_INT,PERF_SRD_12092,PERF_SRD_12093,
1847 1858 PERF_SRD_12094,PERF_SRD_12095,PERF_SRD_9993,PERF_SRD_9994))
1848 1859 The ETP predictions-in-progress flag shall be set to false if all of the following conditions are met
1849 1860 1)the current itinerary is the Active Primary Flight Plan Predictions
1850 1861 2)the ETP-itinerary-has-run flag is true
1851 1862 The ETP-itinerary-has-run flag is then reset to false.
1852 1863 (PERF_SDD_3155_INT)
1853 1864
1854 1865 If the current itinerary is Active Primary Flight Plan Predictions, then the last Cruise flight level
1855 1866 shall be sent to IO for output when the flight plan has been completely predicted.
1856 1867 (PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))
1857 1868
1858 1869 -- INPUTS:
1859 1870
1860 1871
1861 1872 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
1862 1873 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
1863 1874 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
1864 1875 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
1865 1876 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
1866 1877 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
1867 1878 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
1868 1879 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
1869 1880 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
1870 1881 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
1871 1882 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1872 1883 Perf_Background_Dpkg.Opt_Step_Data.Distodest := 25.0
1873 1884 Perf_Background_Dpkg.Opt_Step_Data.Timetogo := 5.0
1874 1885 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
1875 1886 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
1876 1887 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
1877 1888 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
1878 1889 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
1879 1890 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
1880 1891 Perf_Background_Dpkg.Pcoptalt.Valid := True
1881 1892 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
1882 1893 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
1883 1894 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
1884 1895 Perf_Background_Dpkg.Preds_Output(Active) := True
1885 1896 Perf_Background_Dpkg.Psfinalalt := 0.0
1886 1897 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
1887 1898 Perf_Background_Dpkg.Psfpolfnlful := 0.0
1888 1899 Perf_Background_Dpkg.Psfpolfnltme := 0.0
1889 1900 Perf_Background_Dpkg.Psfpolfnltg := 0.0
1890 1901 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
1891 1902 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
1892 1903 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
1893 1904 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
1894 1905 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
1895 1906 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
1896 1907 Perf_Background_Dpkg.Pcfpln := Scratchfpln
1897 1908 Perf_Background_Dpkg.Pcfltphase := Descent
1898 1909 Perf_Background_Dpkg.Psfinaldes := True
1899 1910 Perf_Background_Dpkg.Vert_Auto_Mode := True
1900 1911 Perf_Background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
1901 1912 Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
1902 1913 Perf_Background_Dpkg.Maxalt.Gwt := 150000.0
1903 1914 Perf_Background_Dpkg.Maxalt.Num_Engout := 0
1904 1915 Perf_Background_Dpkg.Etp_Itin_Ran := True
1905 1916 Perf_Background_Dpkg.Maxalt.Maximum_Alt.Valid := False
1906 1917 Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
1907 1918 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
1908 1919 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
1909 1920 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
1910 1921 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
1911 1922 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
1912 1923 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
1913 1924 Perf_Background_Dpkg.Pcgmttime.Gpc_Time := 2
1914 1925 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
1915 1926 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

1916	1927	Perf_Background_Dpkg.Psprddataseq := 3
1917	1928	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
1918	1929	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
1919	1930	#sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
1920	1931	#Change := False
1921	1932	#go
1922	1933	#end
1923	1934	#delb/all
1924		<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #412</del>
	1935	<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #414</del>
1925	1936	#go
1926	1937	#Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
1927	1938	#Chk_Idx := Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx
1928	1939	#delb/all
1929	1940	#sba Perf_Etp_Dpkg.Put_Predinprog After_elab
1930	1941	#go
1931	1942	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
1932	1943	
1933	1944	!run_test()
1934	1945	
1935	1946	-- OUTPUTS
1936	1947	
1937	1948	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr = 32
1938	1949	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = False
1939	1950	Perf_Background_Dpkg.Etp_Itin_Ran = False
1940	1951	Perf_Background_Dpkg.Psfinalalt = 0.0
1941	1952	Perf_Background_Dpkg.Psfpolfnlful = 0.0
1942	1953	Perf_Background_Dpkg.Psfpolfnltme = 0.0
1943	1954	Perf_Background_Dpkg.Psfpolfnltg = 0.0
1944	1955	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = True
1945	1956	
1946	1957	
1947	1958	
1948	1959	TESTID: 14
1949	1960	
1950	1961	Itin is active primary and Vert Auto Mode is not engaged so 2 different perf legs are not outputted.
1951	1962	(PERF_SDD_2631_INT,PERF_SDD_2159_INT,PERF_SDD_4543_INT,PERF_SDD_2158_INT,
1952	1963	PERF_SDD_2289(PERF_SRD_10253,PERF_SRD_10333_INT,PERF_SRD_12092,PERF_SRD_12093,
1953	1964	PERF_SRD_12094,PERF_SRD_12095,PERF_SRD_9993,PERF_SRD_9994))
1954	1965	The ETP predictions-in-progress flag shall be set to false if all of the following conditions are met
1955	1966	1)the current itinerary is the Active Primary Flight Plan Predictions
1956	1967	2)the ETP-itinerary-has-run flag is true
1957	1968	The ETP-itinerary-has-run flag is then reset to false.
1958	1969	(PERF_SDD_3155_INT)

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

1959 1970
1960 1971 If the current itinerary is Active Primary Flight Plan Predictions, then the last Cruise flight level
1961 1972 shall be sent to IO for output when the flight plan has been completely predicted.
1962 1973 (PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))
1963 1974
1964 1975 -- INPUTS:
1965 1976
1966 1977
1967 1978 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
1968 1979 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
1969 1980 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
1970 1981 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
1971 1982 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
1972 1983 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
1973 1984 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
1974 1985 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
1975 1986 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
1976 1987 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
1977 1988 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
1978 1989 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
1979 1990 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
1980 1991 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
1981 1992 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
1982 1993 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
1983 1994 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
1984 1995 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
1985 1996 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
1986 1997 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
1987 1998 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
1988 1999 Perf_Background_Dpkg.Pcoptalt.Valid := True
1989 2000 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
1990 2001 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
1991 2002 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
1992 2003 Perf_Background_Dpkg.Preds_Output(Active) := True
1993 2004 Perf_Background_Dpkg.Psfinalalt := 0.0
1994 2005 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
1995 2006 Perf_Background_Dpkg.Psfpolfnlful := 0.0
1996 2007 Perf_Background_Dpkg.Psfpolfnltme := 0.0
1997 2008 Perf_Background_Dpkg.Psfpolfnltg := 0.0
1998 2009 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
1999 2010 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
2000 2011 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
2001 2012 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
2002 2013 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True

```



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

2003	2014	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
2004	2015	Perf_Background_Dpkg.Pcfpln := Scratchfpln
2005	2016	Perf_Background_Dpkg.Pcfltphase := Cruise
2006	2017	Perf_Background_Dpkg.Psfinaldes := True
2007	2018	Perf_Background_Dpkg.Vert_Auto_Mode := False
2008	2019	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
2009	2020	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
2010	2021	Perf_background_Dpkg.Maxalt.Gwt := 150000.0
2011	2022	Perf_background_Dpkg.Maxalt.Num_Engout := 0
2012	2023	Perf_Background_Dpkg.Etp_Itin_Ran := True
2013	2024	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
2014	2025	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
2015	2026	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
2016	2027	Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
2017	2028	Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
2018	2029	Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
2019	2030	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
2020	2031	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
2021	2032	Perf_Background_Dpkg.Pcgmttime.Gpc_Time := 2
2022	2033	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
2023	2034	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
2024	2035	Perf_Background_Dpkg.Psprddataseq := 3
2025	2036	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
2026	2037	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
2027	2038	#sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
2028	2039	#Change := False
2029	2040	#go
2030	2041	#end
2031	2042	#delb/all
2032	2043	
2033		<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #412</del>
	2044	<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #414</del>
2034	2045	#go
2035	2046	#Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
2036	2047	#Chk_Idx := Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx
2037	2048	#delb/all
2038		<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #459</del>
	2049	<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #461</del>
2039	2050	#go
2040	2051	#Src_Idx := 1
2041	2052	#delb/all
2042	2053	#sba Perf_Etp_Dpkg.Put_Predinprog After_elab
2043	2054	#go
2044	2055	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2045 2056
2046 2057 !run_test()
2047 2058
2048 2059 -- OUTPUTS
2049 2060
2050 2061 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr = 34
2051 2062 Perf_Etp_Dpkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = False
2052 2063 Perf_Background_Dpkg.Etp_Itin_Ran = False
2053 2064 Perf_Background_Dpkg.Psfinalalt = 0.0
2054 2065 Perf_Background_Dpkg.Psfpolfnlful = 0.0
2055 2066 Perf_Background_Dpkg.Psfpolfnltme = 0.0
2056 2067 Perf_Background_Dpkg.Psfpolfnltg = 0.0
2057 2068 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = True
2058 2069
2059 2070
2060 2071 TESTID: 15
2061 2072
2062 2073 Itin is active primary but preds are invalidated during processing. Processing terminates at this point.
2063 2074 (PERF_SDD_2632_INT,PERF_SDD_4543_INT)
2064 2075
2065 2076
2066 2077 -- INPUTS:
2067 2078
2068 2079 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
2069 2080 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
2070 2081 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
2071 2082 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
2072 2083 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
2073 2084 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
2074 2085 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
2075 2086 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
2076 2087 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
2077 2088 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
2078 2089 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
2079 2090 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
2080 2091 Perf_Etp_Dpkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
2081 2092 Perf_Background_Dpkg.Opt_Step_Data.Distodest := 25.0
2082 2093 Perf_Background_Dpkg.Opt_Step_Data.Timetogo := 5.0
2083 2094 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
2084 2095 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
2085 2096 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
2086 2097 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
2087 2098 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
2088 2099 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2089 2100 Perf_Background_Dpkg.Pcoptalt.Valid := True
2090 2101 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
2091 2102 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
2092 2103 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
2093 2104 Perf_Background_Dpkg.Preds_Output(Active) := True
2094 2105 Perf_Background_Dpkg.Psfinalalt := 0.0
2095 2106 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
2096 2107 Perf_Background_Dpkg.Psfpolfnlful := 0.0
2097 2108 Perf_Background_Dpkg.Psfpolfnltme := 0.0
2098 2109 Perf_Background_Dpkg.Psfpolfnltg := 0.0
2099 2110 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
2100 2111 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
2101 2112 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
2102 2113 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
2103 2114 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
2104 2115 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
2105 2116 Perf_Background_Dpkg.Pcfpln := Scratchfpln
2106 2117 Perf_Background_Dpkg.Pcfltphase := Cruise
2107 2118 Perf_Background_Dpkg.Psfinaldes := True
2108 2119 Perf_Background_Dpkg.Vert_Auto_Mode := True
2109 2120 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
2110 2121 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
2111 2122 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
2112 2123 Perf_background_Dpkg.Maxalt.Num_Engout := 0
2113 2124 Perf_Background_Dpkg.Etp_Itin_Ran := True
2114 2125 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
2115 2126 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
2116 2127 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
2117 2128 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
2118 2129 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
2119 2130 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
2120 2131 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := True
2121 2132 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
2122 2133 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
2123 2134 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
2124 2135 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
2125 2136 Perf_Background_Dpkg.Psprddataseq := 3
2126 2137 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
2127 2138 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
2128 2139 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
2129 2140 #Change := False
2130 2141 #go
2131 2142 #end
2132 2143 #delb/all

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2133 2144
2134 2145 !run_test()
2135 2146
2136 2147 -- OUTPUTS
2137 2148
2138 2149 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr = 0
2139 2150 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = True
2140 2151 Perf_Background_Dpkg.Psfinalalt = 0.0
2141 2152 Perf_Background_Dpkg.Psfpolfnlful = 0.0
2142 2153 Perf_Background_Dpkg.Psfpolfnltme = 0.0
2143 2154 Perf_Background_Dpkg.Psfpolfnltg = 0.0
2144 2155 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec = False
2145 2156 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec = False
2146 2157 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec = False
2147 2158 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec = False
2148 2159 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec = False
2149 2160 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec = False
2150 2161 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
2151 2162
2152 2163
2153 2164 TESTID: 16
2154 2165
2155 2166 Itin is active primary but preds are invalidated during processing. Processing terminates at this point.
2156 2167 (PERF_SDD_2632_INT,PERF_SDD_4543_INT)
2157 2168 The predictions-output indication boolean, which indicates the successful copy of the Scratch LGB and Scratch
2158 2169 Perf Buffers to their appropriate reference buffers, shall be set according to Table 11.14-3
2159 2170 In this case predictions-output indication is set to false as the prediction interruptions.
2160 2171 (PERF_SDD_3752_INT)
2161 2172
2162 2173
2163 2174 -- INPUTS:
2164 2175
2165 2176 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
2166 2177 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
2167 2178 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
2168 2179 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
2169 2180 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
2170 2181 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
2171 2182 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
2172 2183 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
2173 2184 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
2174 2185 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
2175 2186 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
2176 2187 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2177 2188 Perf_Etp_Dpkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
2178 2189 Perf_Background_Dpkg.Opt_Step_Data.Distodest := 25.0
2179 2190 Perf_Background_Dpkg.Opt_Step_Data.Timetogo := 5.0
2180 2191 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
2181 2192 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
2182 2193 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
2183 2194 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
2184 2195 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
2185 2196 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
2186 2197 Perf_Background_Dpkg.Pcoptalt.Valid := True
2187 2198 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
2188 2199 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
2189 2200 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
2190 2201 Perf_Background_Dpkg.Preds_Output(Active) := False
2191 2202 Perf_Background_Dpkg.Psfinalalt := 0.0
2192 2203 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
2193 2204 Perf_Background_Dpkg.Psfpolfnlful := 0.0
2194 2205 Perf_Background_Dpkg.Psfpolfnltme := 0.0
2195 2206 Perf_Background_Dpkg.Psfpolfnltg := 0.0
2196 2207 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
2197 2208 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
2198 2209 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
2199 2210 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
2200 2211 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
2201 2212 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
2202 2213 Perf_Background_Dpkg.Pcfpln := Actprimary
2203 2214 Perf_Background_Dpkg.Pcfltphase := Cruise
2204 2215 Perf_Background_Dpkg.Psfinaldes := True
2205 2216 Perf_Background_Dpkg.Vert_Auto_Mode := True
2206 2217 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
2207 2218 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
2208 2219 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
2209 2220 Perf_background_Dpkg.Maxalt.Num_Engout := 0
2210 2221 Perf_Background_Dpkg.Etp_Itin_Ran := True
2211 2222 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
2212 2223 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
2213 2224 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
2214 2225 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
2215 2226 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
2216 2227 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
2217 2228 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := True
2218 2229 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
2219 2230 Perf_Background_Dpkg.Pcgmttime.Gpc_Time := 2
2220 2231 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2221 2232 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
2222 2233 Perf_Background_Dpkg.Psprddataseq := 3
2223 2234 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
2224 2235 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
2225 2236 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
2226 2237 #Change := False
2227 2238 #go
2228 2239 #end
2229 2240 #delb/all
2230 2241
2231 2242 !run_test()
2232 2243
2233 2244 -- OUTPUTS
2234 2245
2235 2246 Perf_Background_Dpkg.Preds_Output(Active) = False
2236 2247 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr = 0
2237 2248 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = True
2238 2249 Perf_Background_Dpkg.Psffinalalt = 0.0
2239 2250 Perf_Background_Dpkg.Psfpolfnlful = 0.0
2240 2251 Perf_Background_Dpkg.Psfpolfnltme = 0.0
2241 2252 Perf_Background_Dpkg.Psfpolfnltg = 0.0
2242 2253 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec = False
2243 2254 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec = False
2244 2255 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec = False
2245 2256 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec = False
2246 2257 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec = False
2247 2258 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec = False
2248 2259 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
2249 2260
2250 2261
2251 2262 TESTID: 17
2252 2263
2253 2264 Itin is active primary and flight phase is climb which is before cruise so 3 different perf legs are not outputed.
2254 2265 (PERF_SDD_2631_INT,PERF_SDD_4543_INT,PERF_SDD_2159_INT,PERF_SDD_2158_INT,
2255 2266 PERF_SDD_2289(PERF_SRD_10253,PERF_SRD_10333_INT,PERF_SRD_12092,PERF_SRD_12093,
2256 2267 PERF_SRD_12094,PERF_SRD_12095,PERF_SRD_9993,PERF_SRD_9994))
2257 2268 The predictions-output indication boolean, which indicates the successful copy of the Scratch LGB and Scratch
2258 2269 Perf Buffers to their appropriate reference buffers, shall be set according to Table 11.14-3
2259 2270 In this case predictions-output indication is set to True.
2260 2271 (PERF_SDD_3752_INT)
2261 2272 The ETP predictions-in-progress flag shall be set to false if all of the following conditions are met
2262 2273 1)the current itinerary is the Active Primary Flight Plan Predictions
2263 2274 2)the ETP-itinerary-has-run flag is true
2264 2275 The ETP-itinerary-has-run flag is then reset to false.

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2265 2276 (PERF_SDD_3155_INT)
2266 2277
2267 2278 If the current itinerary is Active Primary Flight Plan Predictions, then the last Cruise flight level
2268 2279 shall be sent to IO for output when the flight plan has been completely predicted.
2269 2280 (PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))
2270 2281
2271 2282 -- INPUTS:
2272 2283
2273 2284
2274 2285 Perf_Background_Dpkg.Pcactorsec := Active
2275 2286 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
2276 2287 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
2277 2288 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
2278 2289 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
2279 2290 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
2280 2291 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
2281 2292 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
2282 2293 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
2283 2294 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
2284 2295 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
2285 2296 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
2286 2297 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
2287 2298 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
2288 2299 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
2289 2300 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
2290 2301 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
2291 2302 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
2292 2303 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
2293 2304 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
2294 2305 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
2295 2306 Perf_Background_Dpkg.Pcoptalt.Valid := True
2296 2307 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
2297 2308 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
2298 2309 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
2299 2310 Perf_Background_Dpkg.Preds_Output(Active) := False
2300 2311 Perf_Background_Dpkg.Psfinalalt := 0.0
2301 2312 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
2302 2313 Perf_Background_Dpkg.Psfpolfnlful := 0.0
2303 2314 Perf_Background_Dpkg.Psfpolfnltme := 0.0
2304 2315 Perf_Background_Dpkg.Psfpolfnltg := 0.0
2305 2316 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
2306 2317 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
2307 2318 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
2308 2319 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

2309	2320	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
2310	2321	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
2311	2322	Perf_Background_Dpkg.Pcfpln := Scratchfpln
2312	2323	Perf_Background_Dpkg.Pcfltphase := Climb
2313	2324	Perf_Background_Dpkg.Psfinaldes := True
2314	2325	Perf_Background_Dpkg.Vert_Auto_Mode := True
2315	2326	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
2316	2327	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
2317	2328	Perf_background_Dpkg.Maxalt.Gwt := 150000.0
2318	2329	Perf_background_Dpkg.Maxalt.Num_Engout := 0
2319	2330	Perf_Background_Dpkg.Etp_Itin_Ran := True
2320	2331	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
2321	2332	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
2322	2333	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
2323	2334	Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
2324	2335	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
2325	2336	Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
2326	2337	Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
2327	2338	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
2328	2339	Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
2329	2340	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
2330	2341	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
2331	2342	Perf_Background_Dpkg.Psprddataseq := 3
2332	2343	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
2333	2344	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
2334	2345	
2335	2346	#sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
2336	2347	#Change := False
2337	2348	#go
2338	2349	#end
2339	2350	#delb/all
2340		<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #412</del>
	2351	#sba PRF_BKGND_PKG.PUT_BK_DATA #414
2341	2352	#go
2342	2353	#Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
2343	2354	#Chk_Idx := Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx
2344	2355	#delb/all
2345	2356	
2346	2357	!run_test()
2347	2358	
2348	2359	-- OUTPUTS
2349	2360	
2350	2361	Perf_Background_Dpkg.Preds_Output(Active) = True
2351	2362	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr = 33



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2352 2363 Perf_Etp_Dpkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = False
2353 2364 Perf_Background_Dpkg.Etp_Itin_Ran = False
2354 2365 Perf_Background_Dpkg.Psfinalalt = 0.0
2355 2366 Perf_Background_Dpkg.Psfpolfnlful = 0.0
2356 2367 Perf_Background_Dpkg.Psfpolfnltme = 0.0
2357 2368 Perf_Background_Dpkg.Psfpolfnltg = 0.0
2358 2369 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = True
2359 2370
2360 2371
2361 2372 TESTID: 18
2362 2373
2363 2374 Initialization occurs for a cold start. Also, itin is active preds and no change occurs that causes an interruption o
    » n
2364 2375 preds.Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest checked for Alternate option.
2365 2376 (PERF_SDD_2094_INT)
2366 2377 Options_And_Data_Pkg.Fuel_Pred_Final_Dest is equal to "A" and Perf_Background_Dpkg.Pcfinaldest is set to Alternate.
2367 2378 (PERF_SDD_5614_DR(PERF_SRD_1544_A3XX, PERF_SRD_7463))
2368 2379
2369 2380
2370 2381 -- INPUTS:
2371 2382
2372 2383 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
2373 2384 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
2374 2385 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
2375 2386 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
2376 2387 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
2377 2388 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
2378 2389 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
2379 2390 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
2380 2391 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
2381 2392 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
2382 2393 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
2383 2394 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
2384 2395 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
2385 2396 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
2386 2397 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
2387 2398 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
2388 2399 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
2389 2400 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
2390 2401 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
2391 2402 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
2392 2403 Perf_Background_Dpkg.Pcoptalt.Valid := True
2393 2404 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
2394 2405 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2395 2406 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Cold_Start
2396 2407 Perf_Background_Dpkg.Preds_Output(Active) := True
2397 2408 Perf_Background_Dpkg.Psfinalalt := 0.0
2398 2409 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
2399 2410 Perf_Background_Dpkg.Psfpolfnlful := 0.0
2400 2411 Perf_Background_Dpkg.Psfpolfnltme := 0.0
2401 2412 Perf_Background_Dpkg.Psfpolfnltg := 0.0
2402 2413 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
2403 2414 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
2404 2415 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
2405 2416 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Percent := 100.0
2406 2417 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Upper_Limit := 4.0
2407 2418 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Lower_Limit := 1.0
2408 2419 Options_And_Data_Pkg:body.All_Options.Ats_Enable := True
2409 2420 Options_And_Data_Pkg:body.All_Options.Alt_Trip_In_Rsv_Enb := True
2410 2421 Options_And_Data_Pkg:body.All_Options.Cmp_Rsv_In_Flt_Enb := True
2411 2422 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
2412 2423 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
2413 2424 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := True
2414 2425 Perf_Background_Dpkg.Pcfpln := Actprimary
2415 2426 Perf_Background_Dpkg.Pcfltphase := Preflight
2416 2427 Perf_Background_Dpkg.Psfinaldes := True
2417 2428 Perf_Background_Dpkg.Vert_Auto_Mode := True
2418 2429 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
2419 2430 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
2420 2431 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
2421 2432 Perf_background_Dpkg.Maxalt.Num_Engout := 0
2422 2433 Perf_Background_Dpkg.Etp_Itin_Ran := True
2423 2434 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
2424 2435 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
2425 2436 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
2426 2437 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
2427 2438 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
2428 2439 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
2429 2440 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
2430 2441 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
2431 2442 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
2432 2443 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
2433 2444 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
2434 2445 Perf_Background_Dpkg.Psprddataseq := 3
2435 2446 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
2436 2447 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
2437 2448 Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest := "A"
2438 2449 Perf_Background_Dpkg.Ats_Enable := False

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2439 2450 Perf_Background_Dpkg.Psrsvaltn := False
2440 2451 Perf_Background_Dpkg.Psrsvinflt := False
2441 2452 Perf_Background_Dpkg.Psrtersvpctg := 0.0
2442 2453 Perf_Background_Dpkg.Psmaxrtersv := 0.0
2443 2454 Perf_Background_Dpkg.Psminrtersv := 0.0
2444 2455 Perf_Background_Dpkg.Pcfinaldest := Perf_Ext_Tpkg.Primary
2445 2456
2446 2457 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
2447 2458 #Change := True
2448 2459 #go
2449 2460 #end
2450 2461 #delb/all
2451 2462 #sba Perf_Etp_Dpkg.Put_Predinprog After_elab
2452 2463 #go
2453 2464 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
2454 2465
2455 2466 !run_test()
2456 2467
2457 2468 -- OUTPUTS
2458 2469
2459 2470 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr = 0
2460 2471 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = False
2461 2472 Perf_Background_Dpkg.Psfinalalt = 5000.0
2462 2473 Perf_Background_Dpkg.Psfpolfnlful = 40.0
2463 2474 Perf_Background_Dpkg.Psfpolfnltme = 50.0
2464 2475 Perf_Background_Dpkg.Psfpolfnltg = 60.0
2465 2476 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec = False
2466 2477 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec = False
2467 2478 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec = False
2468 2479 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec = False
2469 2480 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec = False
2470 2481 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec = False
2471 2482 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
2472 2483 Perf_Background_Dpkg.Ats_Enable = True
2473 2484 Perf_Background_Dpkg.Psrsvaltn = True
2474 2485 Perf_Background_Dpkg.Psrsvinflt = True
2475 2486 Perf_Background_Dpkg.Pcfinaldest = Alternate
2476 2487
2477 2488
2478 2489 TESTID: 19
2479 2490
2480 2491 Initialization occurs for a cold start. Also, itin is active preds and no change occurs that causes an interruption o
» n
2481 2492 preds.Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest checked for other than Primary and Alternate option.

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2482 2493 (PERF_SDD_2094_INT)
2483 2494 Options_And_Data_Pkg.Fuel_Pred_Final_Dest is other than "P" and "A" and Perf_Background_Dpkg.Pcfinaldest is set to Alt
      » ernate.
2484 2495 (PERF_SDD_5614_DR(PERF_SRD_1544_A3XX, PERF_SRD_7463))
2485 2496
2486 2497
2487 2498 -- INPUTS:
2488 2499
2489 2500 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
2490 2501 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
2491 2502 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
2492 2503 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
2493 2504 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
2494 2505 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
2495 2506 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
2496 2507 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
2497 2508 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
2498 2509 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
2499 2510 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
2500 2511 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
2501 2512 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
2502 2513 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
2503 2514 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
2504 2515 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
2505 2516 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
2506 2517 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
2507 2518 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
2508 2519 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
2509 2520 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
2510 2521 Perf_Background_Dpkg.Pcoptalt.Valid := True
2511 2522 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
2512 2523 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
2513 2524 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Cold_Start
2514 2525 Perf_Background_Dpkg.Preds_Output(Active) := True
2515 2526 Perf_Background_Dpkg.Psfinalalt := 0.0
2516 2527 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
2517 2528 Perf_Background_Dpkg.Psfpolfnlful := 0.0
2518 2529 Perf_Background_Dpkg.Psfpolfnltme := 0.0
2519 2530 Perf_Background_Dpkg.Psfpolfnltg := 0.0
2520 2531 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
2521 2532 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
2522 2533 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
2523 2534 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Percent := 100.0
2524 2535 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Upper_Limit := 4.0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2525 2536 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Lower_Limit := 1.0
2526 2537 Options_And_Data_Pkg:body.All_Options.Ats_Enable := True
2527 2538 Options_And_Data_Pkg:body.All_Options.Alt_Trip_In_Rsv_Enb := True
2528 2539 Options_And_Data_Pkg:body.All_Options.Cmp_Rsv_In_Flt_Enb := True
2529 2540 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
2530 2541 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
2531 2542 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := True
2532 2543 Perf_Background_Dpkg.Pcfpln := Actprimary
2533 2544 Perf_Background_Dpkg.Pcfltphase := Preflight
2534 2545 Perf_Background_Dpkg.Psfinaldes := True
2535 2546 Perf_Background_Dpkg.Vert_Auto_Mode := True
2536 2547 Perf_Background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
2537 2548 Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
2538 2549 Perf_Background_Dpkg.Maxalt.Gwt := 150000.0
2539 2550 Perf_Background_Dpkg.Maxalt.Num_Engout := 0
2540 2551 Perf_Background_Dpkg.Etp_Itin_Ran := True
2541 2552 Perf_Background_Dpkg.Maxalt.Maximum_Alt.Valid := False
2542 2553 Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
2543 2554 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
2544 2555 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
2545 2556 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
2546 2557 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
2547 2558 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
2548 2559 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
2549 2560 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
2550 2561 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
2551 2562 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
2552 2563 Perf_Background_Dpkg.Psprddataseq := 3
2553 2564 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
2554 2565 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
2555 2566 Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest := "I"
2556 2567 Perf_Background_Dpkg.Ats_Enable := False
2557 2568 Perf_Background_Dpkg.Psrsvaltn := False
2558 2569 Perf_Background_Dpkg.Psrsvinflt := False
2559 2570 Perf_Background_Dpkg.Psrtersvpctg := 0.0
2560 2571 Perf_Background_Dpkg.Psmaxrtersv := 0.0
2561 2572 Perf_Background_Dpkg.Psminrtersv := 0.0
2562 2573 Perf_Background_Dpkg.Pcfinaldest := Perf_Ext_Tpkg.Primary
2563 2574
2564 2575 !run_test()
2565 2576
2566 2577 -- OUTPUTS
2567 2578
2568 2579 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr = 0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2569 2580 Perf_Etp_Dpkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = False
2570 2581 Perf_Background_Dpkg.Psffinalalt = 5000.0
2571 2582 Perf_Background_Dpkg.Psfpolfnlful = 40.0
2572 2583 Perf_Background_Dpkg.Psfpolfnltme = 50.0
2573 2584 Perf_Background_Dpkg.Psfpolfnltg = 60.0
2574 2585 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec = False
2575 2586 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec = False
2576 2587 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec = False
2577 2588 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec = False
2578 2589 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec = False
2579 2590 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec = False
2580 2591 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
2581 2592 Perf_Background_Dpkg.Ats_Enable = True
2582 2593 Perf_Background_Dpkg.Psrsvaltm = True
2583 2594 Perf_Background_Dpkg.Psrsvinflt = True
2584 2595 Perf_Background_Dpkg.Psrtersvpctg = 1.0
2585 2596 Perf_Background_Dpkg.Psmaxrtersv = 4.0
2586 2597 Perf_Background_Dpkg.Psminrtersv = 1.0
2587 2598 Perf_Background_Dpkg.Pcfinaldest = Perf_Ext_Tpkg.Alternate
2588 2599
2589 2600
2590 2601 TESTID: 20
2591 2602
2592 2603 Time Constraint Processing :
2593 2604 Cost Index computation is for Active fpln TIME CSTR.
2594 2605 Performance Cost index is ready for release to the system, the RTA working and control data have been output
2595 2606 through the Perf RTA object manager.
2596 2607 (PERF_SDD_3520_INT).
2597 2608 Time Constraint Control data is stored out to the object manager after each pass of Predictions
2598 2609 (PERF_SDD_3106_INT).
2599 2610
2600 2611
2601 2612 -- INPUTS:
2602 2613
2603 2614 Perf_Background_Dpkg.Pcitin.Itinerary := Time_Constraint_Eval
2604 2615 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
2605 2616 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Adjcostidx := 10.0
2606 2617 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Lastphase := Descent
2607 2618 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Glidx := 100
2608 2619 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Fpln := Secondary
2609 2620 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Valid := False
2610 2621 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Eval_Done := False
2611 2622 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Env_Limit := False
2612 2623 Perf_Background_Dpkg.Pctcstrctrl(Active).Adjcostidx := 20.0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2613 2624 Perf_Background_Dpkg.Pctcstrctrl(Active).Lastphase := Cruise
2614 2625 Perf_Background_Dpkg.Pctcstrctrl(Active).Glidx := 2
2615 2626 Perf_Background_Dpkg.Pcactorsec := Active
2616 2627 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
2617 2628 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
2618 2629 Perf_Background_Dpkg.Pctcstrctrl(Active).Envelope_Limit := True
2619 2630 Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit := True
2620 2631 #sba Dev_Drv_Srv_Pkg."Queue_Event":BODY after_elab
2621 2632 #go
2622 2633 Bp_Code = Pseudo_Bp_Pkg.Pb_Act_Cic
2623 2634
2624 2635 !run_test()
2625 2636
2626 2637 -- OUTPUTS
2627 2638
2628 2639 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Adjcostidx = 20.0
2629 2640 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Lastphase = Cruise
2630 2641 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Glidx = 2
2631 2642 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Fpln = Active
2632 2643 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Valid = True
2633 2644 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Eval_Done = True
2634 2645 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Env_Limit = True
2635 2646 Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit = False
2636 2647 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid = True
2637 2648 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
2638 2649
2639 2650
2640 2651 TESTID: 21
2641 2652
2642 2653 Itin is Go Around, destination ETE and EFOB have been output to CDCK for display on the FPLN page.
2643 2654 (PERF_SDD_3392_INT)
2644 2655 GMT time snapshot taken at the beginning of the pass of predictions has been output to CDCK.
2645 2656 (PERF_SDD_3393_INT,PERF_SDD_3052_INT)
2646 2657 Predictied time to the primary destination and it's validity has been processed for use by the I/O function.
2647 2658 (PERF_SDD_3027 (PERF_SRD_10869))
2648 2659
2649 2660
2650 2661
2651 2662 -- INPUTS:
2652 2663
2653 2664 Perf_Background_Dpkg.Pcitin.Itinerary := Goaround
2654 2665 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
2655 2666 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
2656 2667 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2657 2668 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
2658 2669 Perf_Background_Dpkg.Destination_Data.Efob.Data := 20.0
2659 2670 Perf_Background_Dpkg.Destination_Data.Efob.Valid := True
2660 2671 Perf_Background_Dpkg.Destination_Data.Ete.Data := 50.0
2661 2672 Perf_Background_Dpkg.Destination_Data.Ete.Valid := True
2662 2673 Perf_Background_Dpkg.Destination_Data.Firstpass := True
2663 2674 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Data := 0.0
2664 2675 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Valid := False
2665 2676 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Ete.Data := 0.0
2666 2677 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Ete.Valid := False
2667 2678 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Firstpass := False
2668 2679
2669 2680 #sba Prf_Int_Utills."Put_Dest_Eta":BODY before_end
2670 2681 #go
2671 2682 Ete.Valid = True
2672 2683 Ete.Data = 50.0
2673 2684
2674 2685 !run_test()
2675 2686
2676 2687 -- OUTPUTS
2677 2688
2678 2689 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt = 2
2679 2690 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Data = 20.0
2680 2691 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Valid = True
2681 2692 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Ete.Data = 50.0
2682 2693 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Ete.Valid = True
2683 2694 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Firstpass = True
2684 2695 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
2685 2696
2686 2697
2687 2698 TESTID: 22
2688 2699
2689 2700 The indication to CDCK that the asterisk may be displayed on the FPLN A page shall be set True,
2690 2701 when the CI modification is complete and missed/made is updated in LGB (Flight phase is not Transitioned to Descent)
2691 2702 (PERF_SDD_3516_INT).
2692 2703 RTA Prddataseq Counter is set when evaluation of the time constraint has completed and Processing is not on the first
2693 2704 pass through the flight plan after a change
2694 2705 (PERF_SDD_3107_INT).
2695 2706 The transmit status of the RTA control data is not reset to False
2696 2707 (PERF_SDD_3519_INT).
2697 2708 ETT data have been transmitted from the slave FM to the Master when
2698 2709 Current Fm is not the master FM in the dual Configuration
2699 2710 A valid ETT has been computed on this pass of predictions.
2700 2711 (PERF_SDD_3518_INT).
```



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2701 2712 Time constraint working data is output
2702 2713 ETT data output processing has been performed
2703 2714 (PERF_SDD_3515_INT,PERF_SDD_3516_INT,PERF_SDD_2095_INT).
2704 2715
2705 2716
2706 2717 -- INPUTS:
2707 2718
2708 2719 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
2709 2720 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
2710 2721 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
2711 2722 Perf_Background_Dpkg.Timeconmiss_Updated := True
2712 2723 Perf_Background_Dpkg.Pcfpln := Actprimary
2713 2724 Perf_Background_Dpkg.Pcfltphase := Climb
2714 2725 Perf_Background_Dpkg.Pccompett(Active) := True
2715 2726 Perf_Background_Dpkg.Ett(Active).Data := 20.0
2716 2727 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data := 10.0
2717 2728 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status := Invalid
2718 2729 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh := False
2719 2730 Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk := False
2720 2731 Perf_Background_Dpkg.Ett(Active).Status := Valid
2721 2732 Perf_Background_Dpkg.Pctcstridx := 1
2722 2733 Perf_Background_Dpkg.Pcdestglidx := 0
2723 2734 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
2724 2735 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
2725 2736 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
2726 2737 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
2727 2738 Perf_Background_Dpkg.Pcgmttime.Gpc_Time := 2
2728 2739 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
2729 2740 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
2730 2741 Perf_Background_Dpkg.Psprddataseq := 3
2731 2742 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
2732 2743 Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit := True
2733 2744 Perf_Background_Dpkg.Rta.Eval_Done := True
2734 2745 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
2735 2746 Perf_Background_Dpkg.Pctcstrctrl(Active).Timeonly := False
2736 2747 Perf_Background_Dpkg.Rta.Missed := True
2737 2748 Perf_Background_Dpkg.Pcperflegs(18).Included := False
2738 2749 Perf_Background_Dpkg.Pcperflegs(18).Dist := 400.0
2739 2750 Perf_Background_Dpkg.Pcstartpt.Dist := 600.0
2740 2751 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
2741 2752 #Change := False
2742 2753 #go
2743 2754 #end
2744 2755 #sba Dev_Drv_Srvc_Pkg."Queue_Event":BODY after_elab

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2745 2756 #go
2746 2757 Bp_Code = Pseudo_Bp_Pkg.Pb_Calc_Ett
2747 2758
2748 2759 !run_test()
2749 2760
2750 2761 -- OUTPUTS
2751 2762
2752 2763 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid      = True
2753 2764 Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk      = True
2754 2765 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq            = 3
2755 2766 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data  = 20.0
2756 2767 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status = Valid
2757 2768 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh = True
2758 2769 Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit = True
2759 2770 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt = 2
2760 2771 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
2761 2772
2762 2773
2763 2774 TESTID: 23
2764 2775
2765 2776 The indication to CDCK that the asterisk may be displayed on the FPLN A page shall be set True
2766 2777 when Flight phase has transitioned to Descent.(PERF_SDD_3516_INT).
2767 2778 ETT data have been transmitted from the slave FM to the Master when
2768 2779 - Current Fm is not the master FM in the dual Configuration
2769 2780 - A valid ETT has been computed on this pass of predictions.(PERF_SDD_3518_INT).
2770 2781 RTA control data has not been transmitted from the slave FM to the Master
2771 2782 (PERF_SDD_3517_INT).
2772 2783 The transmit status of the RTA control data is not reset to False
2773 2784 (PERF_SDD_3519_INT).
2774 2785 Time constraint working data is output
2775 2786 ETT data output processing has been performed
2776 2787 (PERF_SDD_3515_INT).
2777 2788
2778 2789
2779 2790 -- INPUTS:
2780 2791
2781 2792 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
2782 2793 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
2783 2794 Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk := False
2784 2795 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
2785 2796 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
2786 2797 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
2787 2798 Perf_Background_Dpkg.Pctcstridx := 1
2788 2799 Perf_Background_Dpkg.Pcdestglidx := 0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2789 2800 Perf_Background_Dpkg.Pctcstrctrl(Active).Timeonly := False
2790 2801 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := False
2791 2802 Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit := True
2792 2803 Perf_Background_Dpkg.Rta.Missed := True
2793 2804 Perf_Background_Dpkg.Pcperflegs(18).Included := False
2794 2805 Perf_Background_Dpkg.Pcperflegs(18).Dist := 400.0
2795 2806 Perf_Background_Dpkg.Pcstartpt.Dist := 600.0
2796 2807 Perf_Background_Dpkg.Pcfltphase := Descent
2797 2808 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
2798 2809 Perf_Background_Dpkg.Pcfpln := Actprimary
2799 2810 Perf_Background_Dpkg.Pccompett(Active) := True
2800 2811 Perf_Background_Dpkg.Ett(Active).Data := 30.0
2801 2812 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data := 5.0
2802 2813 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status := Invalid
2803 2814 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh := False
2804 2815 Perf_Background_Dpkg.Ett(Active).Status := Valid
2805 2816 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
2806 2817 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
2807 2818 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
2808 2819 #Change := False
2809 2820 #go
2810 2821 #end
2811 2822 #sba Dev_Drv_Srv_Pkg."Queue_Event":BODY after_elab
2812 2823 #go
2813 2824 Bp_Code = Pseudo_Bp_Pkg.Pb_Calc_Ett
2814 2825
2815 2826 !run_test()
2816 2827
2817 2828 -- OUTPUTS
2818 2829 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid = True
2819 2830 Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk = True
2820 2831 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data = 30.0
2821 2832 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status = Valid
2822 2833 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh = True
2823 2834 Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit = True
2824 2835 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt = 2
2825 2836 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
2826 2837
2827 2838
2828 2839 TESTID: 24
2829 2840
2830 2841 The indication to CDCK that the asterisk may be displayed on the FPLN A page shall be set True
2831 2842 when A/C is within 40 NM point when T/D is included
2832 2843 (PERF_SDD_3516_INT).

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2833 2844 RTA control data has not been transmitted from the slave FM to the Master
2834 2845 (PERF_SDD_3517_INT).
2835 2846 The transmit status of the RTA control data is not reset to False
2836 2847 (PERF_SDD_3519_INT).
2837 2848 Time constraint working data is output
2838 2849 ETT data has not been transmitted from the slave FM to the Master
2839 2850 (PERF_SDD_3518_INT).
2840 2851 Itin is a maxalt and partition is in Dual_Slave mode.
2841 2852 This test case is written to cover the sdd anchor PERF_SDD_3523_INT.
2842 2853 Prf_Int_Utils.Dual_Status is a function that shall return the master/slave and dual
2843 2854 indication via a single data item based on IO/OPS status items.
2844 2855 (PERF_SDD_3523_INT)
2845 2856
2846 2857
2847 2858 -- INPUTS:
2848 2859
2849 2860 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
2850 2861 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
2851 2862 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Dual
2852 2863 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
2853 2864 Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk := False
2854 2865 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
2855 2866 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
2856 2867 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
2857 2868 Perf_Background_Dpkg.Pctcstridx := 1
2858 2869 Perf_Background_Dpkg.Pcdestglidx := 0
2859 2870 Perf_Background_Dpkg.Pctcstrctrl(Active).Timeonly := True
2860 2871 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := False
2861 2872 Perf_Background_Dpkg.Pcfltphase := Cruise
2862 2873 Perf_Background_Dpkg.Rta.Missed := False
2863 2874 Perf_Background_Dpkg.Pcperflegs(18).Included := True
2864 2875 Perf_Background_Dpkg.Pcperflegs(18).Dist := 600.0
2865 2876 Perf_Background_Dpkg.Pcstartpt.Dist := 600.0
2866 2877 Perf_Background_Dpkg.Pccompett(Active) := False
2867 2878 Perf_Background_Dpkg.Rta.Eval_Done := True
2868 2879 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
2869 2880
2870 2881 Perf_Background_Dpkg.Ett(Active).Data := 20.0
2871 2882 Perf_Background_Dpkg.Ett(Active).Status := Valid
2872 2883 Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit := True
2873 2884 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data := 5.0
2874 2885 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status := Invalid
2875 2886 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh := False
2876 2887 CTP_PERF_BKGND_PUT_BK_DATA.Du_Status := Perf_Int_Base_Tpkg.Dual_Slave

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2877 2888 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
2878 2889 #Change := False
2879 2890 #go
2880 2891 #end
2881 2892
2882 2893 !run_test()
2883 2894
2884 2895 -- OUTPUTS
2885 2896
2886 2897 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid      = True
2887 2898 Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk      = True
2888 2899 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt = 2
2889 2900 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data = 5.0
2890 2901 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status = Invalid
2891 2902 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh = False
2892 2903 Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit = True
2893 2904 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
2894 2905 CTP_PERF_BKGND_PUT_BK_DATA.Du_Status = Perf_Int_Base_Tpkg.Dual_Master
2895 2906
2896 2907
2897 2908 TESTID: 25
2898 2909
2899 2910 RTA Prddataseq Counter is set when evaluation of the time constraint has completed and Processing is not on the first
2900 2911 pass through the flight plan after a change
2901 2912 (PERF_SDD_3107_INT).
2902 2913 RTA control data have been transmitted from the slave FM to the Master when
2903 2914 - Current Fm is not the master FM in the dual Configuration
2904 2915 - CI adjustment has resulte in the RTA being made during this pass of predictions.
2905 2916 (PERF_SDD_3517_INT).
2906 2917 ETT data has not been transmitted from the slave FM to the Master
2907 2918 (PERF_SDD_3518_INT).
2908 2919 The transmit status of the RTA control data is reset to False
2909 2920 (PERF_SDD_3519_INT).
2910 2921 Time constraint working data is output
2911 2922 RTA data output processing has been performed
2912 2923 (PERF_SDD_3515_INT,PERF_SDD_3516_INT).
2913 2924
2914 2925 The indication to CDCK that the asterisk may be displayed on the FPLN A page shall be set True when any of the followi
    » ng
2915 2926 conditions are met: (here A/C is within 40 NM point when T/D is included.)
2916 2927 - The CI modification is complete and missed/made is updated in LGB
2917 2928 - Flight phase has transitioned to Descent
2918 2929 - T/D pseudo-waypoint is not included and a destination exists
2919 2930 (This indicates that the A/C has sequenced the T/D but not started down)

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2920 2931 - A/C is within 40 NM point when T/D is included.
2921 2932 (PERF_SDD_3516_INT)
2922 2933 Background Performance shall signal Demand processing that Background Performance has gathered
2923 2934 Flight Plan predicted data for the currently entered RTA.
2924 2935 (PERF_SDD_3739_INT)
2925 2936
2926 2937
2927 2938 -- INPUTS:
2928 2939
2929 2940 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
2930 2941 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
2931 2942 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
2932 2943 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
2933 2944 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
2934 2945 Perf_Background_Dpkg.Pcfpln := Actprimary
2935 2946 Perf_Background_Dpkg.Pcfltphase := Climb
2936 2947 Perf_Background_Dpkg.Psfinaldes := True
2937 2948 Perf_Background_Dpkg.Pccompett(Active) := False
2938 2949 Perf_Background_Dpkg.Ett(Active).Data := 20.0
2939 2950 Perf_Background_Dpkg.Pctcstridx := 1
2940 2951 Perf_Background_Dpkg.Pcdestglidx := 0
2941 2952 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
2942 2953 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
2943 2954 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
2944 2955 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
2945 2956 Perf_Background_Dpkg.Pcgmttime.Gpc_Time := 2
2946 2957 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
2947 2958 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
2948 2959 Perf_Background_Dpkg.Psprddataseq := 3
2949 2960 Perf_Background_Dpkg.Pcperfleqs(18).Included := True
2950 2961 Perf_Background_Dpkg.Pcperfleqs(18).Dist := 4000.0
2951 2962 Perf_Background_Dpkg.Pcstartpt.Dist := 600.0
2952 2963 Perf_Background_Dpkg.Ett(Active).Data := 20.0
2953 2964 Perf_Background_Dpkg.Ett(Active).Status := Valid
2954 2965 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
2955 2966 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Adjcostidx := 10.0
2956 2967 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Lastphase := Descent
2957 2968 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Glidx := 100
2958 2969 Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk := False
2959 2970 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Fpln := Secondary
2960 2971 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Valid := False
2961 2972 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Eval_Done := False
2962 2973 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Env_Limit := False
2963 2974 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Flat := False

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

2964 2975 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Flat_Count := 5
2965 2976 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data := 5.0
2966 2977 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status := Invalid
2967 2978 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh := False
2968 2979 Perf_Background_Dpkg.Pctcstrctrl(Active).Adjcostidx := 20.0
2969 2980 Perf_Background_Dpkg.Pctcstrctrl(Active).Lastphase := Cruise
2970 2981 Perf_Background_Dpkg.Pctcstrctrl(Active).Glidx := 2
2971 2982 Perf_Background_Dpkg.Pctcstrctrl(Active).Flat := True
2972 2983 Perf_Background_Dpkg.Pctcstrctrl(Active).Flat_Count := 4
2973 2984 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
2974 2985 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
2975 2986 Perf_Background_Dpkg.Pctcstrctrl(Active).Envelope_Limit := True
2976 2987 Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit := True
2977 2988 Perf_Background_Dpkg.Pctcstrctrl(Active).Timeonly := True
2978 2989 Perf_Background_Dpkg.Rta.Eval_Done := False
2979 2990 Perf_Background_Dpkg.Pcactorsec := Active
2980 2991 Perf_Background_Dpkg.Rta.Missed := True
2981 2992 Perf_Dpkg.Rta_Data_Gathered := False
2982 2993 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
2983 2994 #Change := False
2984 2995 #go
2985 2996 #end
2986 2997
2987 2998 #sba Dev_Drv_Srvc_Pkg."Queue_Event":BODY after_elab
2988 2999 #go
2989 3000 Bp_Code = Pseudo_Bp_Pkg.Pb_Act_Cic
2990 3001
2991 3002 !run_test()
2992 3003
2993 3004 -- OUTPUTS
2994 3005
2995 3006 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Env_Limit = True
2996 3007 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Eval_Done = True
2997 3008 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Valid = True
2998 3009 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Glidx = 2
2999 3010 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Lastphase = Cruise
3000 3011 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Adjcostidx = 20.0
3001 3012 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Flat_Count = 4
3002 3013 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Flat = True
3003 3014 Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit = False
3004 3015 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Fpln = Active
3005 3016 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid = True
3006 3017 Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk = True
3007 3018 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq = 3

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3008 3019 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt = 2
3009 3020 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data = 5.0
3010 3021 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status = Invalid
3011 3022 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh = False
3012 3023 Perf_Dpkg.Rta_Data_Gathered = True
3013 3024 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
3014 3025
3015 3026
3016 3027 TESTID: 26
3017 3028
3018 3029 The indication to CDCK that the asterisk may be displayed on the FPLN A page shall be set True when
3019 3030 - T/D pseudo-waypoint is not included and a destination exists
3020 3031   (This indicates that the A/C has sequenced the T/D but not started down)
3021 3032   (PERF_SDD_3516_INT)
3022 3033 RTA control data have been transmitted from the slave FM to the Master when Current Fm is not
3023 3034 the master FM in the dual Configuration CI adjustment has resulted in the RTA being made
3024 3035 during this pass of predictions.(PERF_SDD_3517_INT).
3025 3036 RTA Prddataseq Counter is set when evaluation of the time constraint has completed and Processing is
3026 3037 not on the first pass through the flight plan after a change
3027 3038 (PERF_SDD_3107_INT).
3028 3039 The transmit status of the RTA control data is reset to False
3029 3040 (PERF_SDD_3519_INT).
3030 3041 ETT data has not been transmitted from the slave FM to the Master
3031 3042 (PERF_SDD_3518_INT).
3032 3043 Time constraint working data is output
3033 3044 RTA data output processing has been performed
3034 3045 (PERF_SDD_3515_INT).
3035 3046
3036 3047
3037 3048 -- INPUTS:
3038 3049
3039 3050 Perf_Background_Dpkg.Rta.Missed := True
3040 3051 Perf_Background_Dpkg.Pcperflgls(18).Included := False
3041 3052 Perf_Background_Dpkg.Pcperflgls(18).Dist := 400.0
3042 3053 Perf_Background_Dpkg.Pcstartpt.Dist := 600.0
3043 3054 Perf_Background_Dpkg.Pccompett(Active) := False
3044 3055 Perf_Background_Dpkg.Pctcstridx := 1
3045 3056 Perf_Background_Dpkg.Pcdestglidx := 1
3046 3057 Perf_Background_Dpkg.Rta.Eval_Done := False
3047 3058 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
3048 3059 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
3049 3060 Perf_Background_Dpkg.Pcfpln := Actprimary
3050 3061 Perf_Background_Dpkg.Etp_Itin_Ran := False
3051 3062 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2

```



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3052 3063 Perf_Background_Dpkg.Psprddataseq := 3
3053 3064 Perf_Background_Dpkg.Pcfltphase := Climb
3054 3065 Perf_Background_Dpkg.Pctcstrctrl(Active).Timeonly := True
3055 3066 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
3056 3067 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
3057 3068 Perf_Background_Dpkg.Pctcstrctrl(Active).Adjcostidx := 20.0
3058 3069 Perf_Background_Dpkg.Pctcstrctrl(Active).Lastphase := Cruise
3059 3070 Perf_Background_Dpkg.Pctcstrctrl(Active).Glidx := 2
3060 3071 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
3061 3072 Perf_Background_Dpkg.Pctcstrctrl(Active).Envelope_Limit := True
3062 3073 Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit := True
3063 3074 Perf_Background_Dpkg.Ett(Active).Data := 20.0
3064 3075 Perf_Background_Dpkg.Ett(Active).Status := Valid
3065 3076 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
3066 3077 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
3067 3078 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
3068 3079 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
3069 3080 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
3070 3081 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
3071 3082 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Adjcostidx := 50.0
3072 3083 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Lastphase := Descent
3073 3084 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Glidx := 100
3074 3085 Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk := False
3075 3086 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Fpln := Secondary
3076 3087 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Valid := False
3077 3088 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Eval_Done := False
3078 3089 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Env_Limit := False
3079 3090 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data := 5.0
3080 3091 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status := Invalid
3081 3092 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh := False
3082 3093 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
3083 3094 #Change := False
3084 3095 #go
3085 3096 #end
3086 3097 #sba Dev_Drv_Srvc_Pkg."Queue_Event":BODY after_elab
3087 3098 #go
3088 3099 Bp_Code = Pseudo_Bp_Pkg.Pb_Act_Cic
3089 3100
3090 3101 !run_test()
3091 3102
3092 3103 -- OUTPUTS
3093 3104
3094 3105 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Env_Limit = True
3095 3106 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Eval_Done = True

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3096 3107 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Valid      = True
3097 3108 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Glidx      = 2
3098 3109 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Lastphase    = Cruise
3099 3110 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Adjcostidx    = 20.0
3100 3111 Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit                  = False
3101 3112 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Fpln         = Active
3102 3113 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid = True
3103 3114 Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk = True
3104 3115 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq = 3
3105 3116 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt = 2
3106 3117 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data  = 5.0
3107 3118 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status = Invalid
3108 3119 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh = False
3109 3120 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
3110 3121
3111 3122
3112 3123 TESTID: 27
3113 3124
3114 3125 CDCK is not indicated to display the Asterisk, the indication to CDCK that the asterisk
3115 3126 may be displayed on the FPLN A page shall be set False, when none of the following conditions are met:
3116 3127 - The CI modification is complete and missed/made is updated in LGB
3117 3128 - Flight phase has transitioned to Descent
3118 3129 - T/D pseudo-waypoint is not included and a destination exists
3119 3130 (This indicates that the A/C has sequenced the T/D but not started down)
3120 3131 - A/C is within 40 NM point when T/D is included.
3121 3132 (PERF_SDD_3516_INT, PERF_SDD_07394_INT)
3122 3133
3123 3134 RTA control data has not been transmitted from the slave FM to the Master
3124 3135 (PERF_SDD_3517_INT).
3125 3136 The transmit status of the RTA control data is not reset to False
3126 3137 (PERF_SDD_3519_INT).
3127 3138 ETT data have been transmitted from the slave FM to the Master when
3128 3139 - Current Fm is not the master FM in the dual Configuration
3129 3140 - A valid ETT has been computed on this pass of predictions.
3130 3141 (PERF_SDD_3518_INT).
3131 3142 Time constraint working data is output
3132 3143 ETT data output processing has been performed
3133 3144 (PERF_SDD_3515_INT).
3134 3145
3135 3146
3136 3147 -- INPUTS:
3137 3148
3138 3149 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
3139 3150 Perf_Background_Dpkg.Pcfpln := Actprimary

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3140 3151 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
3141 3152 Perf_Background_Dpkg.Pcfltphase := Climb
3142 3153 Perf_Background_Dpkg.Rta.Missed := True
3143 3154 Perf_Background_Dpkg.Rta.Eval_Done := True
3144 3155 Perf_Background_Dpkg.Pcperflegs(18).Included := False
3145 3156 Perf_Background_Dpkg.Pcperflegs(18).Dist := 400.0
3146 3157 Perf_Background_Dpkg.Pcstartpt.Dist := 600.0
3147 3158 Perf_Background_Dpkg.Pccompett(Active) := True
3148 3159 Perf_Background_Dpkg.Pctcstridx := 1
3149 3160 Perf_Background_Dpkg.Pcdestglidx := 0
3150 3161 Perf_Background_Dpkg.Ett(Active).Data := 20.0
3151 3162 Perf_Background_Dpkg.Ett(Active).Status := Valid
3152 3163 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := False
3153 3164 Perf_Background_Dpkg.Pctcstrctrl(Active).Timeonly := True
3154 3165 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
3155 3166 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
3156 3167 Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit := True
3157 3168 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
3158 3169 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
3159 3170 Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk := True
3160 3171 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data := 5.0
3161 3172 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status := Invalid
3162 3173 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh := False
3163 3174 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
3164 3175 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
3165 3176 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
3166 3177 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
3167 3178 #Change := False
3168 3179 #go
3169 3180 #end
3170 3181 #sba Dev_Drv_Srv_Pkg."Queue_Event":BODY after_elab
3171 3182 #go
3172 3183 Bp_Code = Pseudo_Bp_Pkg.Pb_Calc_Ett
3173 3184
3174 3185 !run_test()
3175 3186
3176 3187 -- OUTPUTS
3177 3188
3178 3189 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid = True
3179 3190 Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk = False
3180 3191 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data = 20.0
3181 3192 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status = Valid
3182 3193 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh = True
3183 3194 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt = 2

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3184 3195 Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit      = True
3185 3196 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg                  = False
3186 3197
3187 3198
3188 3199 TESTID: 28
3189 3200
3190 3201 This test case is same as test case 2 except for new input variables that have been added to test
3191 3202 the anchor PERF_SDD_3500_INT. In the procedure Prf_Int_Utills.Update_Refresh_Timer updates the passed-in timer's
3192 3203 record data. The passed in timer's refresh time shall be set to the difference between the current FM time
3193 3204 and the timer's reference start time, and the timer's reference start time set equal to the current FM time.
3194 3205 (PERF_SDD_3500_INT)
3195 3206
3196 3207 This test case is also written to cover the anchor PERF_SDD_3501_INT. A running average of the most recent refresh
3197 3208 time data points (up to five) shall be computed and stored in the passed-in timer's record data, along with the actual
3198 3209 refresh time data points (up to five) used to compute the average.
3199 3210 (PERF_SDD_3501_INT)
3200 3211
3201 3212
3202 3213 -- INPUTS:
3203 3214
3204 3215 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
3205 3216 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
3206 3217 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
3207 3218 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
3208 3219 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
3209 3220 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
3210 3221 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
3211 3222 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
3212 3223 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
3213 3224 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
3214 3225 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
3215 3226 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
3216 3227 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
3217 3228 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
3218 3229 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
3219 3230 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
3220 3231 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
3221 3232 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
3222 3233 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
3223 3234 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
3224 3235 Perf_Background_Dpkg.Pcoptalt.Valid := True
3225 3236 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
3226 3237 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
3227 3238 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3228 3239 Perf_Background_Dpkg.Preds_Output(Active) := True
3229 3240 Perf_Background_Dpkg.Psfinalalt := 0.0
3230 3241 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
3231 3242 Perf_Background_Dpkg.Psfpolfnlful := 0.0
3232 3243 Perf_Background_Dpkg.Psfpolfnltme := 0.0
3233 3244 Perf_Background_Dpkg.Psfpolfnltg := 0.0
3234 3245 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
3235 3246 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
3236 3247 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
3237 3248 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
3238 3249 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
3239 3250 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
3240 3251 Perf_Background_Dpkg.Pcfpln := Actprimary
3241 3252 Perf_Background_Dpkg.Pcfltphase := Cruise
3242 3253 Perf_Background_Dpkg.Psfinaldes := True
3243 3254 Perf_Background_Dpkg.Vert_Auto_Mode := True
3244 3255 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
3245 3256 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
3246 3257 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
3247 3258 Perf_background_Dpkg.Maxalt.Num_Engout := 0
3248 3259 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
3249 3260 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
3250 3261 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
3251 3262 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
3252 3263 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
3253 3264 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
3254 3265 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
3255 3266 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
3256 3267 Perf_Background_Dpkg.Pcgmttime.Gpc_Time := 2
3257 3268 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
3258 3269 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
3259 3270 Perf_Background_Dpkg.Psprddataseq := 3
3260 3271 Perf_Background_Dpkg.Etp_Itin_Ran := False
3261 3272 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
3262 3273 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
3263 3274
3264 3275 CTP_PERF_BKGND_PUT_BK_DATA.Fpln := Active
3265 3276
3266 3277 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Number_Of_Points := 3
3267 3278 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(1) := 4.0
3268 3279 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(2) := 3.0
3269 3280 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(3) := 2.0
3270 3281 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Average_Refresh_Time := 0.0
3271 3282

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3272 3283 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Start_Time := 0
3273 3284 Fmcs_Partition_Data_Pkg.Ops_Time.Gpc_Time := 20
3274 3285 Ops_Timer_Pkg:body.Ops_time.Gpc_Time := 30
3275 3286 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
3276 3287 #Change := False
3277 3288 #go
3278 3289 #end
3279 3290 # sba Prf_Int_Utils.Update_Refresh_Timer before_end
3280 3291 # go
3281 3292 Timer.Start_Time = 30
3282 3293 Timer.Refresh_Time = 1.00000E-03
3283 3294 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Average_Refresh_Time = 2.25025E+00
3284 3295 !run_test()
3285 3296
3286 3297 -- OUTPUT
3287 3298
3288 3299
3289 3300 TESTID: 29
3290 3301
3291 3302 This test case is same as test case 2 except for new input variables that have been added to test
3292 3303 the anchor PERF_SDD_3500_INT. In the procedure Prf_Int_Utils.Update_Refresh_Timer updates the passed-in timer's
3293 3304 record data. The passed in timer's refresh time shall be set to the difference between the current FM time
3294 3305 and the timer's reference start time, and the timer's reference start time set equal to the current FM time.
3295 3306 (PERF_SDD_3500_INT)
3296 3307 This test case is also written to cover the anchor PERF_SDD_3501_INT. A running average of the most recent refresh
3297 3308 time data points (up to five) shall be computed and stored in the passed-in timer's record data, along with the actual
3298 3309 refresh time data points (up to five) used to compute the average.
3299 3310 (PERF_SDD_3501_INT)
3300 3311
3301 3312
3302 3313 -- INPUTS:
3303 3314
3304 3315 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
3305 3316 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
3306 3317 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
3307 3318 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
3308 3319 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
3309 3320 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
3310 3321 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
3311 3322 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
3312 3323 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
3313 3324 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
3314 3325 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
3315 3326 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3316 3327 Perf_Background_Dpkg.Opt_Step_Data.Distodest := 25.0
3317 3328 Perf_Background_Dpkg.Opt_Step_Data.Timetogo := 5.0
3318 3329 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
3319 3330 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
3320 3331 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
3321 3332 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
3322 3333 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
3323 3334 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
3324 3335 Perf_Background_Dpkg.Pcoptalt.Valid := True
3325 3336 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
3326 3337 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
3327 3338 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
3328 3339 Perf_Background_Dpkg.Preds_Output(Active) := True
3329 3340 Perf_Background_Dpkg.Psfinalalt := 0.0
3330 3341 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
3331 3342 Perf_Background_Dpkg.Psfpolfnlful := 0.0
3332 3343 Perf_Background_Dpkg.Psfpolfnltme := 0.0
3333 3344 Perf_Background_Dpkg.Psfpolfnltg := 0.0
3334 3345 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
3335 3346 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
3336 3347 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
3337 3348 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
3338 3349 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
3339 3350 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
3340 3351 Perf_Background_Dpkg.Pcfpln := Actprimary
3341 3352 Perf_Background_Dpkg.Pcfltphase := Cruise
3342 3353 Perf_Background_Dpkg.Psfinaldes := True
3343 3354 Perf_Background_Dpkg.Vert_Auto_Mode := True
3344 3355 Perf_Background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
3345 3356 Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
3346 3357 Perf_Background_Dpkg.Maxalt.Gwt := 150000.0
3347 3358 Perf_Background_Dpkg.Maxalt.Num_Engout := 0
3348 3359 Perf_Background_Dpkg.Maxalt.Maximum_Alt.Valid := False
3349 3360 Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
3350 3361 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
3351 3362 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
3352 3363 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
3353 3364 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
3354 3365 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
3355 3366 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
3356 3367 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
3357 3368 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
3358 3369 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
3359 3370 Perf_Background_Dpkg.Psprddataseq := 3

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3360 3371 Perf_Background_Dpkg.Etp_Itin_Ran := False
3361 3372 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
3362 3373 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
3363 3374
3364 3375 CTP_PERF_BKGND_PUT_BK_DATA.Fpln := Active
3365 3376
3366 3377 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Number_Of_Points := 5
3367 3378 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(1) := 4.0
3368 3379 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(2) := 3.0
3369 3380 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(3) := 2.0
3370 3381 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Average_Refresh_Time := 0.0
3371 3382
3372 3383 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Start_Time := 0
3373 3384 Fmcs_Partition_Data_Pkg.Ops_Time.Gpc_Time := 20
3374 3385 Ops_Timer_Pkg:body.Ops_time.Gpc_Time := 30
3375 3386
3376 3387 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(4) := 1.0
3377 3388 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(5) := 0.5
3378 3389 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
3379 3390 #Change := False
3380 3391 #go
3381 3392 #end
3382 3393
3383 3394 # sba Prf_Int_Utils.Update_Refresh_Timer before_end
3384 3395 # go
3385 3396 Timer.Start_Time = 30
3386 3397 Timer.Refresh_Time = 1.00000E-03
3387 3398 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Average_Refresh_Time = 1.30020E+00
3388 3399
3389 3400 !run_test()
3390 3401
3391 3402 -- OUTPUTS
3392 3403
3393 3404
3394 3405 TESTID: 30
3395 3406
3396 3407 Itin is a maxalt and partition is in Dual_Slave mode.
3397 3408 Prf_Int_Utils.Dual_Status is a function that shall return the master/slave and dual
3398 3409 indication via a single data item based on IO/OPS status items.
3399 3410 (PERF_SDD_3523_INT)
3400 3411
3401 3412
3402 3413 -- INPUTS:
3403 3414

```



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3404 3415 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
3405 3416 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
3406 3417 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
3407 3418 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
3408 3419 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
3409 3420 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
3410 3421 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
3411 3422 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
3412 3423 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
3413 3424 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
3414 3425 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
3415 3426 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
3416 3427 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
3417 3428 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
3418 3429 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
3419 3430 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
3420 3431 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
3421 3432 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
3422 3433 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
3423 3434 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
3424 3435 Perf_Background_Dpkg.Pcoptalt.Valid := True
3425 3436 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
3426 3437 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Base_Domain_Services_Tpkg.Spare
3427 3438 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
3428 3439 Perf_Background_Dpkg.Preds_Output(Active) := True
3429 3440 Perf_Background_Dpkg.Psfinalalt := 0.0
3430 3441 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
3431 3442 Perf_Background_Dpkg.Psfpolfnlful := 0.0
3432 3443 Perf_Background_Dpkg.Psfpolfnltme := 0.0
3433 3444 Perf_Background_Dpkg.Psfpolfnltg := 0.0
3434 3445 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
3435 3446 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
3436 3447 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
3437 3448 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
3438 3449 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
3439 3450 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
3440 3451 Perf_Background_Dpkg.Pcfpln := Actprimary
3441 3452 Perf_Background_Dpkg.Pcfltphase := Cruise
3442 3453 Perf_Background_Dpkg.Psfinaldes := True
3443 3454 Perf_Background_Dpkg.Vert_Auto_Mode := True
3444 3455 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
3445 3456 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
3446 3457 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
3447 3458 Perf_background_Dpkg.Maxalt.Num_Engout := 0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3448 3459 Perf_Background_Dpkg.Etp_Itin_Ran := True
3449 3460 Perf_Background_Dpkg.Maxalt.Maximum_Alt.Valid := False
3450 3461 Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
3451 3462 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Dual
3452 3463
3453 3464 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
3454 3465 Perf_Background_Dpkg.Pcitin.Itinerary := Maxalt
3455 3466 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
3456 3467 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
3457 3468 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
3458 3469 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
3459 3470 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
3460 3471 Perf_Background_Dpkg.Psprddataseq := 3
3461 3472 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
3462 3473 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
3463 3474 CTP_PERF_BKGND_PUT_BK_DATA.Du_Status := Perf_Int_Base_Tpkg.Dual_Master
3464 3475 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
3465 3476 #Change := False
3466 3477 #go
3467 3478 #end
3468 3479 !run_test()
3469 3480
3470 3481 -- OUTPUTS
3471 3482
3472 3483 CTP_PERF_BKGND_PUT_BK_DATA.Du_Status = Perf_Int_Base_Tpkg.Dual_Slave
3473 3484
3474 3485
3475 3486 TESTID: 31
3476 3487
3477 3488 If the first legs match, then the Lateral Offset Data Point data shall be copied from Perf's working data to the appro
    » priate
3478 3489 Active or Secondary LGB header.Prf_Bkgnd_Pkg.Put_Bk_Data consist Store out the Lateral Offset Data Points.
3479 3490 In this test case Active Primary Flt Plan and CAPTURE_PATH_START Lateral Offset Data Points are considered
3480 3491 (PERF_SDD_3968_INT)
3481 3492
3482 3493
3483 3494 -- INPUTS:
3484 3495
3485 3496 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
3486 3497 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
3487 3498 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
3488 3499 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
3489 3500 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
3490 3501 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3491 3502 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
3492 3503 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
3493 3504 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
3494 3505 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
3495 3506 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
3496 3507 Perf_Etp_Dpkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
3497 3508 Perf_Background_Dpkg.Opt_Step_Data.Distodest := 25.0
3498 3509 Perf_Background_Dpkg.Opt_Step_Data.Timetogo := 5.0
3499 3510 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
3500 3511 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
3501 3512 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
3502 3513 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
3503 3514 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
3504 3515 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
3505 3516 Perf_Background_Dpkg.Pcoptalt.Valid := True
3506 3517 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
3507 3518 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
3508 3519 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Cold_Start
3509 3520 Perf_Background_Dpkg.Preds_Output(Active) := True
3510 3521 Perf_Background_Dpkg.Psfinalalt := 0.0
3511 3522 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
3512 3523 Perf_Background_Dpkg.Psfpolfnlful := 0.0
3513 3524 Perf_Background_Dpkg.Psfpolfnltme := 0.0
3514 3525 Perf_Background_Dpkg.Psfpolfnltg := 0.0
3515 3526 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
3516 3527 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
3517 3528 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Percent := 100.0
3518 3529 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Upper_Limit := 4.0
3519 3530 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Lower_Limit := 1.0
3520 3531 Options_And_Data_Pkg:body.All_Options.Ats_Enable := True
3521 3532 Options_And_Data_Pkg:body.All_Options.Alt_n_Trip_In_Rsv_Enb := True
3522 3533 Options_And_Data_Pkg:body.All_Options.Cmp_Rsv_In_Flt_Enb := True
3523 3534 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
3524 3535 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
3525 3536 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
3526 3537 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := True
3527 3538 Perf_Background_Dpkg.Pcfpln := Actprimary
3528 3539 Perf_Background_Dpkg.Pcfltphase := Preflight
3529 3540 Perf_Background_Dpkg.Psfinaldes := True
3530 3541 Perf_Background_Dpkg.Vert_Auto_Mode := True
3531 3542 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
3532 3543 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
3533 3544 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
3534 3545 Perf_background_Dpkg.Maxalt.Num_Engout := 0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3535 3546 Perf_Background_Dpkg.Etp_Itin_Ran := True
3536 3547 Perf_Background_Dpkg.Maxalt.Maximum_Alt.Valid := False
3537 3548 Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
3538 3549 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
3539 3550 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
3540 3551 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
3541 3552 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
3542 3553 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
3543 3554 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
3544 3555 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
3545 3556 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
3546 3557 Perf_Background_Dpkg.Psprddataseq := 3
3547 3558 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
3548 3559 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
3549 3560 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
3550 3561 Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest := "P"
3551 3562 CTP_PERF_BKGND_PUT_BK_DATA.Du_Status := Perf_Int_Base_Tpkg.Dual_Master
3552 3563 Perf_Background_Dpkg.Ats_Enable := False
3553 3564 Perf_Background_Dpkg.Psrsvaltn := False
3554 3565 Perf_Background_Dpkg.Psrsvinflt := False
3555 3566 Perf_Background_Dpkg.Psrtersvpctg := 0.0
3556 3567 Perf_Background_Dpkg.Psmaxrtersv := 0.0
3557 3568 Perf_Background_Dpkg.Psminrtersv := 0.0
3558 3569 Perf_Background_Dpkg.Ref_Flight_Plan := 1
3559 3570 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).PRDTAS := 65.0
3560 3571 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Prd_Wind_Mag := 66.0
3561 3572 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Prd_Wind_True_Brg := 68.0
3562 3573 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Prddataseq := 5
3563 3574 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Prdalt := 1000.0
3564 3575 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Prdgwttofix := 69.0
3565 3576 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Fixdistodest := 70.0
3566 3577 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Fixdtdbias := 80.0
3567 3578 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Fltphasefix := PREFLIGHT
3568 3579 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Prdterm := TRUE
3569 3580 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Firstpass := FALSE
3570 3581 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
3571 3582 #Change := False
3572 3583 #go
3573 3584 #end
3574 3585
3575 3586 !run_test()
3576 3587
3577 3588 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.PRDTAS = 0.0
3578 3589 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Prd_Wind_Mag = 0.0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3579 3590 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Prd_Wind_True_Brg =0.0
3580 3591 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Prddataseq = 0
3581 3592 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Prdalt = 0.0
3582 3593 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Prdgwttofix = 0.0
3583 3594 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Fixdistodest = 0.0
3584 3595 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Fixdtdbias = 0.0
3585 3596 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Fltphasefix = PREFLIGHT
3586 3597 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Prdterm = False
3587 3598 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Firstpass = FALSE
3588 3599
3589 3600
3590 3601 TESTID: 32
3591 3602
3592 3603 If the first legs match, then the Lateral Offset Data Point data shall be copied from Perf's working data to the appro
    » priate
3593 3604 Active or Secondary LGB header. This test case is written to cover the
3594 3605 sdd anchor PERF_SDD_3968_INT. Prf_Bkgnd_Pkg.Put_Bk_Data consist Store out the Lateral Offset Data Points,
3595 3606 In this test case Active Alternate Primary Flt Plan and CAPTURE_PATH_END Lateral Offset Data Points are considered
3596 3607 (PERF_SDD_3968_INT)
3597 3608
3598 3609
3599 3610 -- INPUTS:
3600 3611
3601 3612 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
3602 3613 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
3603 3614 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
3604 3615 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
3605 3616 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
3606 3617 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
3607 3618 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
3608 3619 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
3609 3620 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
3610 3621 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
3611 3622 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
3612 3623 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
3613 3624 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
3614 3625 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
3615 3626 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
3616 3627 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
3617 3628 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
3618 3629 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
3619 3630 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
3620 3631 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
3621 3632 Perf_Background_Dpkg.Pcoptalt.Valid := True

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3622 3633 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
3623 3634 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
3624 3635 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Cold_Start
3625 3636 Perf_Background_Dpkg.Preds_Output(Active) := True
3626 3637 Perf_Background_Dpkg.Psfinalalt := 0.0
3627 3638 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
3628 3639 Perf_Background_Dpkg.Psfpolfnlful := 0.0
3629 3640 Perf_Background_Dpkg.Psfpolfnltme := 0.0
3630 3641 Perf_Background_Dpkg.Psfpolfnltg := 0.0
3631 3642 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
3632 3643 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
3633 3644 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Percent := 100.0
3634 3645 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Upper_Limit := 4.0
3635 3646 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Lower_Limit := 1.0
3636 3647 Options_And_Data_Pkg:body.All_Options.Ats_Enable := True
3637 3648 Options_And_Data_Pkg:body.All_Options.Alt_Trip_In_Rsv_Enb := True
3638 3649 Options_And_Data_Pkg:body.All_Options.Cmp_Rsv_In_Flt_Enb := True
3639 3650 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
3640 3651 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
3641 3652 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
3642 3653 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := True
3643 3654 Perf_Background_Dpkg.Pcfpln := Actprimary
3644 3655 Perf_Background_Dpkg.Pcfltphase := Preflight
3645 3656 Perf_Background_Dpkg.Psfinaldes := True
3646 3657 Perf_Background_Dpkg.Vert_Auto_Mode := True
3647 3658 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
3648 3659 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
3649 3660 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
3650 3661 Perf_background_Dpkg.Maxalt.Num_Engout := 0
3651 3662 Perf_Background_Dpkg.Etp_Itin_Ran := True
3652 3663 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
3653 3664 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
3654 3665 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
3655 3666 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
3656 3667 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
3657 3668 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
3658 3669 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
3659 3670 Perf_Background_Dpkg.Pcgmttime.Gpc_Time := 2
3660 3671 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
3661 3672 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
3662 3673 Perf_Background_Dpkg.Psprddataseq := 3
3663 3674 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
3664 3675 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
3665 3676 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3666 3677 Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest := "P"
3667 3678 CTP_PERF_BKGND_PUT_BK_DATA.Du_Status := Perf_Int_Base_Tpkg.Dual_Master
3668 3679 Perf_Background_Dpkg.Ats_Enable := False
3669 3680 Perf_Background_Dpkg.Psrsvaltn := False
3670 3681 Perf_Background_Dpkg.Psrsvinflt := False
3671 3682 Perf_Background_Dpkg.Psrtersvpctg := 0.0
3672 3683 Perf_Background_Dpkg.Psmaxrtersv := 0.0
3673 3684 Perf_Background_Dpkg.Psminrtersv := 0.0
3674 3685 Perf_Background_Dpkg.Ref_Flight_Plan := 2
3675 3686 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).PRDTAS := 66.0
3676 3687 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Prd_Wind_Mag := 66.0
3677 3688 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Prd_Wind_True_Brg :=68.0
3678 3689 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Prddataseq := 5
3679 3690 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Prdalt := 1000.0
3680 3691 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Prdgwttofix := 69.0
3681 3692 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Fixdistodest := 70.0
3682 3693 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Fixdtdbias := 80.0
3683 3694 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Fltphasefix := PREFLIGHT
3684 3695 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Prdterm := TRUE
3685 3696 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Firstpass := FALSE
3686 3697 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
3687 3698 #Change := False
3688 3699 #go
3689 3700 #end
3690 3701
3691 3702 !run_test()
3692 3703
3693 3704 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.PRDTAS = 0.0
3694 3705 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Prd_Wind_Mag = 0.0
3695 3706 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Prd_Wind_True_Brg =0.0
3696 3707 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Prddataseq = 0
3697 3708 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Prdalt = 0.0
3698 3709 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Prdgwttofix = 0.0
3699 3710 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Fixdistodest = 0.0
3700 3711 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Fixdtdbias = 0.0
3701 3712 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Fltphasefix = PREFLIGHT
3702 3713 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Prdterm = False
3703 3714 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Firstpass = FALSE
3704 3715
3705 3716
3706 3717 TESTID: 33
3707 3718
3708 3719 If the first legs match, then the Lateral Offset Data Point data shall be copied from Perf's working data to the appro
» priate

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3709 3720 Active or Secondary LGB header. This test case is written to cover the
3710 3721 sdd anchor PERF_SDD_3968_INT. Prf_Bkgnd_Pkg.Put_Bk_Data consist Store out the Lateral Offset Data Points,
3711 3722 In this test case Secondary Primary Flt Plan and RETURN_PATH_START Lateral Offset Data Points are considered
3712 3723 (PERF_SDD_3968_INT)
3713 3724
3714 3725
3715 3726 -- INPUTS:
3716 3727
3717 3728 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
3718 3729 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
3719 3730 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
3720 3731 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
3721 3732 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
3722 3733 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
3723 3734 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
3724 3735 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
3725 3736 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
3726 3737 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
3727 3738 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
3728 3739 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
3729 3740 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
3730 3741 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
3731 3742 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
3732 3743 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
3733 3744 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
3734 3745 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
3735 3746 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
3736 3747 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
3737 3748 Perf_Background_Dpkg.Pcoptalt.Valid := True
3738 3749 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
3739 3750 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
3740 3751 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Cold_Start
3741 3752 Perf_Background_Dpkg.Preds_Output(Active) := True
3742 3753 Perf_Background_Dpkg.Psfinalalt := 0.0
3743 3754 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
3744 3755 Perf_Background_Dpkg.Psfpolfnlful := 0.0
3745 3756 Perf_Background_Dpkg.Psfpolfnltme := 0.0
3746 3757 Perf_Background_Dpkg.Psfpolfnltg := 0.0
3747 3758 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
3748 3759 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
3749 3760 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Percent := 100.0
3750 3761 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Upper_Limit := 4.0
3751 3762 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Lower_Limit := 1.0
3752 3763 Options_And_Data_Pkg:body.All_Options.Ats_Enable := True

```



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3753 3764 Options_And_Data_Pkg:body.All_Options.Alt_n_Trip_In_Rsv_Enb := True
3754 3765 Options_And_Data_Pkg:body.All_Options.Cmp_Rsv_In_Flt_Enb := True
3755 3766 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
3756 3767 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
3757 3768 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
3758 3769 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := True
3759 3770 Perf_Background_Dpkg.Pcflpln := Actprimary
3760 3771 Perf_Background_Dpkg.Pcfltpase := Preflight
3761 3772 Perf_Background_Dpkg.Psfinaldes := True
3762 3773 Perf_Background_Dpkg.Vert_Auto_Mode := True
3763 3774 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
3764 3775 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
3765 3776 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
3766 3777 Perf_background_Dpkg.Maxalt.Num_Engout := 0
3767 3778 Perf_Background_Dpkg.Etp_Itin_Ran := True
3768 3779 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
3769 3780 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
3770 3781 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
3771 3782 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
3772 3783 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
3773 3784 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
3774 3785 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
3775 3786 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
3776 3787 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
3777 3788 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
3778 3789 Perf_Background_Dpkg.Psprddataseq := 3
3779 3790 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
3780 3791 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
3781 3792 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
3782 3793 Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest := "P"
3783 3794 CTP_PERF_BKGND_PUT_BK_DATA.Du_Status := Perf_Int_Base_Tpkg.Dual_Master
3784 3795 Perf_Background_Dpkg.Ats_Enable := False
3785 3796 Perf_Background_Dpkg.Psrsvaltn := False
3786 3797 Perf_Background_Dpkg.Psrsvinflt := False
3787 3798 Perf_Background_Dpkg.Psrtersvpctg := 0.0
3788 3799 Perf_Background_Dpkg.Psmaxrtersv := 0.0
3789 3800 Perf_Background_Dpkg.Psminrtersv := 0.0
3790 3801 Perf_Background_Dpkg.Ref_Flight_Plan := 3
3791 3802 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).PRDTAS := 67.0
3792 3803 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Prd_Wind_Mag := 66.0
3793 3804 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Prd_Wind_True_Brg := 68.0
3794 3805 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Prddataseq := 5
3795 3806 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Prdalt := 1000.0
3796 3807 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Prdgwttofix := 69.0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3797 3808 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Fixdistodest := 70.0
3798 3809 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Fixdtdbias := 80.0
3799 3810 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Fltphasefix := PREFLIGHT
3800 3811 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Prdterm := TRUE
3801 3812 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Firstpass := FALSE
3802 3813 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
3803 3814 #Change := False
3804 3815 #go
3805 3816 #end
3806 3817
3807 3818 !run_test()
3808 3819
3809 3820 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.PRDTAS = 0.0
3810 3821 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Prd_Wind_Mag = 0.0
3811 3822 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Prd_Wind_True_Brg =0.0
3812 3823 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Prddataseq = 0
3813 3824 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Prdalt = 0.0
3814 3825 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Prdgwttofix = 0.0
3815 3826 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Fixdistodest = 0.0
3816 3827 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Fixdtdbias = 0.0
3817 3828 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Fltphasefix = PREFLIGHT
3818 3829 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Prdterm = False
3819 3830 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Firstpass = FALSE
3820 3831
3821 3832
3822 3833 TESTID: 34
3823 3834
3824 3835 If the first legs match, then the Lateral Offset Data Point data shall be copied from Perf's working data to the appro
    » priate
3825 3836 Active or Secondary LGB header. This test case is written to cover the
3826 3837 sdd anchor PERF_SDD_3968_INT. Prf_Bkgnd_Pkg.Put_Bk_Data consist Store out the Lateral Offset Data Points,
3827 3838 In this test case Secondary Alternate Primary Flt Plan and RETURN_PATH_END Lateral Offset Data Points are considere
    » d
3828 3839 (PERF_SDD_3968_INT)
3829 3840
3830 3841
3831 3842 -- INPUTS:
3832 3843
3833 3844 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
3834 3845 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
3835 3846 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
3836 3847 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
3837 3848 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
3838 3849 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3839 3850 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
3840 3851 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
3841 3852 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
3842 3853 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
3843 3854 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
3844 3855 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
3845 3856 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
3846 3857 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
3847 3858 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
3848 3859 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
3849 3860 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
3850 3861 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
3851 3862 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
3852 3863 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
3853 3864 Perf_Background_Dpkg.Pcoptalt.Valid := True
3854 3865 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
3855 3866 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
3856 3867 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Cold_Start
3857 3868 Perf_Background_Dpkg.Preds_Output(Active) := True
3858 3869 Perf_Background_Dpkg.Psfinalalt := 0.0
3859 3870 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
3860 3871 Perf_Background_Dpkg.Psfpolfnlful := 0.0
3861 3872 Perf_Background_Dpkg.Psfpolfnltme := 0.0
3862 3873 Perf_Background_Dpkg.Psfpolfnltg := 0.0
3863 3874 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
3864 3875 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
3865 3876 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Percent := 100.0
3866 3877 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Upper_Limit := 4.0
3867 3878 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Lower_Limit := 1.0
3868 3879 Options_And_Data_Pkg:body.All_Options.Ats_Enable := True
3869 3880 Options_And_Data_Pkg:body.All_Options.AltN_Trip_In_Rsv_Enb := True
3870 3881 Options_And_Data_Pkg:body.All_Options.Cmp_Rsv_In_Flt_Enb := True
3871 3882 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
3872 3883 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
3873 3884 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
3874 3885 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := True
3875 3886 Perf_Background_Dpkg.Pcfpln := Actprimary
3876 3887 Perf_Background_Dpkg.Pcfltphase := Preflight
3877 3888 Perf_Background_Dpkg.Psfinaldes := True
3878 3889 Perf_Background_Dpkg.Vert_Auto_Mode := True
3879 3890 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
3880 3891 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
3881 3892 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
3882 3893 Perf_background_Dpkg.Maxalt.Num_Engout := 0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3883 3894 Perf_Background_Dpkg.Etp_Itin_Ran := True
3884 3895 Perf_Background_Dpkg.Maxalt.Maximum_Alt.Valid := False
3885 3896 Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
3886 3897 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
3887 3898 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
3888 3899 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
3889 3900 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
3890 3901 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
3891 3902 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
3892 3903 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
3893 3904 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
3894 3905 Perf_Background_Dpkg.Psprddataseq := 3
3895 3906 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
3896 3907 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
3897 3908 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
3898 3909 Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest := "P"
3899 3910 CTP_PERF_BKGND_PUT_BK_DATA.Du_Status := Perf_Int_Base_Tpkg.Dual_Master
3900 3911 Perf_Background_Dpkg.Ats_Enable := False
3901 3912 Perf_Background_Dpkg.Psrsvaltn := False
3902 3913 Perf_Background_Dpkg.Psrsvinflt := False
3903 3914 Perf_Background_Dpkg.Psrtersvpctg := 0.0
3904 3915 Perf_Background_Dpkg.Psmaxrtersv := 0.0
3905 3916 Perf_Background_Dpkg.Psminrtersv := 0.0
3906 3917 Perf_Background_Dpkg.Ref_Flight_Plan := 4
3907 3918 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).PRDTAS := 68.0
3908 3919 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Prd_Wind_Mag := 66.0
3909 3920 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Prd_Wind_True_Brg := 68.0
3910 3921 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Prddataseq := 5
3911 3922 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Prdalt := 1000.0
3912 3923 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Prdgwttofix := 69.0
3913 3924 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Fixdistodest := 70.0
3914 3925 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Fixdtdbias := 80.0
3915 3926 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Fltphasefix := PREFLIGHT
3916 3927 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Prdterm := TRUE
3917 3928 Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Firstpass := FALSE
3918 3929 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
3919 3930 #Change := False
3920 3931 #go
3921 3932 #end
3922 3933
3923 3934 !run_test()
3924 3935
3925 3936 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.PRDTAS = 0.0
3926 3937 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Prd_Wind_Mag = 0.0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3927 3938 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Prd_Wind_True_Brg =0.0
3928 3939 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Prddataseq = 0
3929 3940 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Prdalt = 0.0
3930 3941 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Prdgwttofix = 0.0
3931 3942 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Fixdistodest = 0.0
3932 3943 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Fixdtdbias = 0.0
3933 3944 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Fltphasefix = PREFLIGHT
3934 3945 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Prdterm = False
3935 3946 CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Firstpass = FALSE
3936 3947
3937 3948
3938 3949 TESTID: 35
3939 3950
3940 3951 Itin is active primary but Src_Idx equals the Chk_Idx and the perf request flag is set true so information is not outp
» uted.
3941 3952 (PERF_SDD_2631_INT,PERF_SDD_4543_INT)
3942 3953 The ETP predictions-in-progress flag will hold TRUE Value initialised in Input Since
3943 3954     1)the current itinerary is the Active Primary Flight Plan Predictions
3944 3955     2)the ETP-itinerary-has-run flag is True
3945 The ETP itinerary has run flag is then reset to false.
3946 Here there is perf restart request hence the flags not reset.
3947 3956 (PERF_SDD_3155_INT)
3948 3957
3949 3958 If the current itinerary is Active Primary Flight Plan Predictions, then the last Cruise flight level
3950 3959 shall be sent to IO for output when the flight plan has been completely predicted.
3951 3960 (PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))
3952 3961
3953 3962
3954 3963 -- INPUTS:
3955 3964
3956 3965 Perf_Background_Dpkg.Pcactorsec := Fprequestrec_Types.Temporary
3957 3966 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
3958 3967 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
3959 3968 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
3960 3969 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
3961 3970 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
3962 3971 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
3963 3972 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
3964 3973 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
3965 3974 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 0
3966 3975 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
3967 3976 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
3968 3977 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
3969 3978 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

3970 3979 Perf_Background_Dpkg.Opt_Step_Data.Timetogo := 5.0
3971 3980 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
3972 3981 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
3973 3982 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
3974 3983 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
3975 3984 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
3976 3985 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
3977 3986 Perf_Background_Dpkg.Pcoptalt.Valid := True
3978 3987 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
3979 3988 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
3980 3989 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
3981 3990 Perf_Background_Dpkg.Preds_Output(Active) := True
3982 3991 Perf_Background_Dpkg.Psfinalalt := 0.0
3983 3992 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
3984 3993 Perf_Background_Dpkg.Psfpolfnlful := 0.0
3985 3994 Perf_Background_Dpkg.Psfpolfnltme := 0.0
3986 3995 Perf_Background_Dpkg.Psfpolfnltg := 0.0
3987 3996 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
3988 3997 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
3989 3998 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
3990 3999 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
3991 4000 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
3992 4001 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
3993 4002 Perf_Background_Dpkg.Pcfpln := ScratchFpln
3994 4003 Perf_Background_Dpkg.Pcfltphase := Cruise
3995 4004 Perf_Background_Dpkg.Psfinaldes := True
3996 4005 Perf_Background_Dpkg.Vert_Auto_Mode := True
3997 4006 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
3998 4007 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
3999 4008 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
4000 4009 Perf_background_Dpkg.Maxalt.Num_Engout := 0
4001 4010 Perf_Background_Dpkg.Etp_Itin_Ran := True
4002 4011 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
4003 4012 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
4004 4013 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
4005 4014 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
4006 4015 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
4007 4016 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
4008 4017 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
4009 4018 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
4010 4019 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
4011 4020 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
4012 4021 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
4013 4022 Perf_Background_Dpkg.Psprddataseq := 3

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

4014	4023	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
4015	4024	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
4016	4025	#sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
4017	4026	#Change := False
4018	4027	#go
4019	4028	#end
4020	4029	#delb/all
4021		<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #412</del>
	4030	#sba PRF_BKGND_PKG.PUT_BK_DATA #414
4022	4031	#go
4023	4032	#Chk_Idx := 0
4024		<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #434</del>
	4033	#sba PRF_BKGND_PKG.PUT_BK_DATA #436
4025	4034	#go
4026	4035	#Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfregst := True
4027	4036	#delb/all
4028	4037	
4029	4038	!run_test()
4030	4039	
4031	4040	-- OUTPUTS
4032	4041	
4033	4042	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr = 0
4034	4043	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = True
	4044	Perf_Background_Dpkg.Etp_Itin_Ran = True
4035	4045	Perf_Background_Dpkg.Psfinalalt = 0.0
4036	4046	Perf_Background_Dpkg.Psfpolfnlful = 0.0
4037	4047	Perf_Background_Dpkg.Psfpolfnltme = 0.0
4038	4048	Perf_Background_Dpkg.Psfpolfnltg = 0.0
4039	4049	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
4040	4050	
4041	4051	
4042	4052	TESTID: 36
4043	4053	
4044	4054	Itin is active primary and Src_Idx equals Chk_Idx and the perf request flag is set true so information is not outputed » .
4045	4055	LGB index of the dest leg of Scratch fpln is set equal Critical index destwpt.and Aircraft Level change Autocontrol
4046	4056	Flag is set False.
4047	4057	(PERF_SDD_2631_INT,PERF_SDD_4543_INT)
4048	4058	
4049	4059	The ETP predictions-in-progress flag shall hold True since all of the following conditions are not met
4050	4060	1)the current itinerary is the Active Primary Flight Plan Predictions
4051	4061	2)the ETP-itinerary-has-run flag is TRUE
4052	4062	The ETP-itinerary-has-run flag is then reset to false.
4053	4063	Here there is perf restart request hence the flags not reset.

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

4054 4064 (PERF_SDD_3155_INT)
4055 4065
4056 4066 If the current itinerary is Active Primary Flight Plan Predictions, then the last Cruise flight level
4057 4067 shall be sent to IO for output when the flight plan has been completely predicted.
4058 4068 (PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))
4059 4069
4060 4070
4061 4071 -- INPUTS:
4062 4072
4063 4073 Perf_Background_Dpkg.Pcactorsec := Fprequestrec_Types.Temporary
4064 4074 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
4065 4075 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
4066 4076 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
4067 4077 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
4068 4078 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
4069 4079 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
4070 4080 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
4071 4081 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
4072 4082 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 0
4073 4083 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Destwpt) := 0
4074 4084 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
4075 4085 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
4076 4086 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
4077 4087 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
4078 4088 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
4079 4089 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
4080 4090 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
4081 4091 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
4082 4092 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
4083 4093 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
4084 4094 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
4085 4095 Perf_Background_Dpkg.Pcoptalt.Valid := True
4086 4096 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
4087 4097 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
4088 4098 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
4089 4099 Perf_Background_Dpkg.Preds_Output(Active) := True
4090 4100 Perf_Background_Dpkg.Psfinalalt := 0.0
4091 4101 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
4092 4102 Perf_Background_Dpkg.Psfpolfnlful := 0.0
4093 4103 Perf_Background_Dpkg.Psfpolfnltme := 0.0
4094 4104 Perf_Background_Dpkg.Psfpolfnltg := 0.0
4095 4105 Perf_Background_Dpkg.Pslcautoctl := False
4096 4106 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
4097 4107 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50

```



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

4098	4108	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
4099	4109	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
4100	4110	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
4101	4111	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
4102	4112	Perf_Background_Dpkg.Pcfpln := ScratchFpln
4103	4113	Perf_Background_Dpkg.Pcfltphase := Cruise
4104	4114	Perf_Background_Dpkg.Psfinaldes := True
4105	4115	Perf_Background_Dpkg.Vert_Auto_Mode := True
4106	4116	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
4107	4117	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
4108	4118	Perf_background_Dpkg.Maxalt.Gwt := 150000.0
4109	4119	Perf_background_Dpkg.Maxalt.Num_Engout := 0
4110	4120	Perf_Background_Dpkg.Etp_Itin_Ran := False
4111	4121	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
4112	4122	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
4113	4123	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
4114	4124	Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
4115	4125	Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
4116	4126	Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
4117	4127	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
4118	4128	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
4119	4129	Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
4120	4130	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
4121	4131	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
4122	4132	Perf_Background_Dpkg.Psprddataseq := 3
4123	4133	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
4124	4134	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
4125	4135	#sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
4126	4136	#Change := False
4127	4137	#go
4128	4138	#end
4129	4139	#delb/all
4130		<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #412</del>
	4140	#sba PRF_BKGND_PKG.PUT_BK_DATA #414
4131	4141	#go
4132	4142	#Chk_Idx := 0
4133	4143	#delb/all
4134		<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #530</del>
	4144	#sba PRF_BKGND_PKG.PUT_BK_DATA #532
4135	4145	#go
4136	4146	#Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := True
4137	4147	!run_test()
4138	4148	
4139	4149	-- OUTPUTS

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

4140 4150
4141 4151 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr = 1
4142 4152 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = True
4143 4153 Perf_Background_Dpkg.Psfinalalt = 0.0
4144 4154 Perf_Background_Dpkg.Psfpolfnlful = 0.0
4145 4155 Perf_Background_Dpkg.Psfpolfnltme = 0.0
4146 4156 Perf_Background_Dpkg.Psfpolfnltg = 0.0
4147 4157 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = True
4148 4158
4149 4159
4150 4160 TESTID: 37
4151 4161
4152 4162 Itin is active primary and Src_Idx equals Chk_Idx and perf request flag is set true so information is not outputed.
4153 4163 (PERF_SDD_2631_INT)
4154 4164 The ETP predictions-in-progress flag shall hold True since all of the following conditions are not met
4155 4165     1)the current itinerary is the Active Primary Flight Plan Predictions
4156 4166     2)the ETP-itinerary-has-run flag is TRUE
4157 4167 The ETP-itinerary-has-run flag is then reset to false.
4158 4168 Here the ETP-itinerary-has-run flag is false hence the flags not reset.
4159 4169 (PERF_SDD_3155_INT)
4160 4170
4161 4171 If the current itinerary is Active Primary Flight Plan Predictions, then the last Cruise flight level
4162 4172 shall be sent to IO for output when the flight plan has been completely predicted.
4163 4173 (PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))
4164 4174 Active flight plan predictions refresh timer is updated by calling Prf_Int_Utills.Update_Refresh_Timer.
4165 4175 When Number of points are greater than Max refresh point
4166 4176 (PERF_SDD_3511_INT)
4167 4177
4168 4178
4169 4179 -- INPUTS:
4170 4180
4171 4181 Perf_Background_Dpkg.Pcactorsec := Fprequestrec_Types.Temporary
4172 4182 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
4173 4183 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
4174 4184 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
4175 4185 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
4176 4186 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
4177 4187 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
4178 4188 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
4179 4189 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
4180 4190 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 0
4181 4191 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Destwpt) := 0
4182 4192 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
4183 4193 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

4184 4194 Perf_Etp_Dpkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
4185 4195 Perf_Background_Dpkg.Opt_Step_Data.Distodest := 25.0
4186 4196 Perf_Background_Dpkg.Opt_Step_Data.Timetogo := 5.0
4187 4197 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
4188 4198 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
4189 4199 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
4190 4200 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
4191 4201 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
4192 4202 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
4193 4203 Perf_Background_Dpkg.Pcoptalt.Valid := True
4194 4204 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
4195 4205 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
4196 4206 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
4197 4207 Perf_Background_Dpkg.Preds_Output(Active) := True
4198 4208 Perf_Background_Dpkg.Psfinalalt := 0.0
4199 4209 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
4200 4210 Perf_Background_Dpkg.Psfpolfnlful := 0.0
4201 4211 Perf_Background_Dpkg.Psfpolfnltme := 0.0
4202 4212 Perf_Background_Dpkg.Psfpolfnltg := 0.0
4203 4213 Perf_Background_Dpkg.Pslcautoctl := True
4204 4214 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
4205 4215 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
4206 4216 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
4207 4217 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
4208 4218 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
4209 4219 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
4210 4220 Perf_Background_Dpkg.Pcfpln := ScratchFpln
4211 4221 Perf_Background_Dpkg.Pcfltphase := Cruise
4212 4222 Perf_Background_Dpkg.Psfinaldes := True
4213 4223 Perf_Background_Dpkg.Vert_Auto_Mode := True
4214 4224 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
4215 4225 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
4216 4226 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
4217 4227 Perf_background_Dpkg.Maxalt.Num_Engout := 0
4218 4228 Perf_Background_Dpkg.Etp_Itin_Ran := False
4219 4229 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
4220 4230 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
4221 4231 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
4222 4232 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
4223 4233 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
4224 4234 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
4225 4235 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
4226 4236 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
4227 4237 Perf_Background_Dpkg.Pcgmttime.Gpc_Time := 2

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

4228	4238	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
4229	4239	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
4230	4240	Perf_Background_Dpkg.Psprddataseq := 3
4231	4241	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
4232	4242	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
4233	4243	
4234	4244	#sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
4235	4245	#Change := False
4236	4246	#go
4237	4247	#end
4238	4248	#delb/all
4239	4249	
4240		<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #412</del>
	4250	<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #414</del>
4241	4251	#go
4242	4252	#Chk_Idx := 0
4243	4253	#delb/all
4244	4254	
4245		<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #530</del>
	4255	<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #532</del>
4246	4256	#go
4247	4257	#Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfregst := True
4248		<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #542</del>
	4258	<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #544</del>
4249	4259	#go
4250	4260	#Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfregst := False
4251	4261	#sba Prf_Int_Utils.Update_Refresh_Timer after_elab begin
4252	4262	#Timer.Number_Of_Points := 6
4253	4263	#Timer.Refresh_Time := 20.0
4254	4264	#go
4255	4265	#end
4256	4266	#delb/all
4257	4267	
4258	4268	#sba Prf_Int_Utils."Update_Refresh_Timer":BODY before_end begin
4259	4269	Timer.Start_Time = 0
4260	4270	Timer.Refresh_Time = 20.0
4261	4271	Timer.Average_Refresh_Time = 0.0
4262	4272	Timer.Number_Of_Points = 6
4263	4273	Timer.Avg_Refresh_Time_Data(5) = 20.0
4264	4274	#go
4265	4275	#end
4266	4276	#delb/all
4267	4277	
4268	4278	!run_test()

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```
4269 4279
4270 4280 -- OUTPUTS
4271 4281
4272 4282 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr = 1
4273 4283 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = True
4274 4284 Perf_Background_Dpkg.Psfinalalt = 0.0
4275 4285 Perf_Background_Dpkg.Psfpolfnlful = 0.0
4276 4286 Perf_Background_Dpkg.Psfpolfnltime = 0.0
4277 4287 Perf_Background_Dpkg.Psfpolfnltime = 0.0
4278 4288 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = True
4279 4289
4280 4290
4281 4291 TESTID: 38
4282 4292
4283 4293 Itin is active primary and Src_Idx equals Chk_Idx and perf request flag is set True so information do not exist.
4284 4294 (PERF_SDD_2631_INT)
4285 4295 The ETP predictions-in-progress flag shall hold True since all of the following conditions are not met
4286 4296     1)the current itinerary is the Active Primary Flight Plan Predictions
4287 4297     2)the ETP-itinerary-has-run flag is TRUE
4288 4298 Here the ETP-itinerary-has-run flag is false hence the flags not reset.
4289 4299 (PERF_SDD_3155_INT)
4290 4300
4291 4301 If the current itinerary is Active Primary Flight Plan Predictions, then the last Cruise flight level
4292 4302 shall be sent to IO for output when the flight plan has been completely predicted.
4293 4303 (PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))
4294 4304 ETT data have been transmitted from the slave FM to the Master when
4295 4305     - Current Fm is not the master FM in the dual Configuration
4296 4306     - A valid ETT has been computed on this pass of predictions.
4297 4307 (PERF_SDD_3518_INT).
4298 4308 ETT data output processing has been performed
4299 4309 (PERF_SDD_3515_INT).
4300 4310
4301 4311
4302 4312 -- INPUTS:
4303 4313
4304 4314 Perf_Background_Dpkg.Pcactorsec := Active
4305 4315 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr := 0
4306 4316 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
4307 4317 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
4308 4318 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
4309 4319 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
4310 4320 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
4311 4321 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
4312 4322 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

4313 4323 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 0
4314 4324 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Destwpt) := 0
4315 4325 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
4316 4326 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
4317 4327 Perf_Etp_Dpkg:body.Data_Storage.Ckequiddata.Data(1).Pack_Vals.Predinprog := True
4318 4328 Perf_Background_Dpkg.Opt_Step_Data.Distodest := 25.0
4319 4329 Perf_Background_Dpkg.Opt_Step_Data.Timetogo := 5.0
4320 4330 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
4321 4331 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
4322 4332 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
4323 4333 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
4324 4334 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
4325 4335 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
4326 4336 Perf_Background_Dpkg.Pcoptalt.Valid := True
4327 4337 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
4328 4338 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
4329 4339 CTP_PERF_BKGND_PUT_BK_DATA.Du_Status := Perf_Int_Base_Tpkg.Dual_Slave
4330 4340 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
4331 4341 Perf_Background_Dpkg.Preds_Output(Active) := True
4332 4342 Perf_Background_Dpkg.Psfinalalt := 0.0
4333 4343 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000
4334 4344 Perf_Background_Dpkg.Psfpolfnlful := 0.0
4335 4345 Perf_Background_Dpkg.Psfpolfnltme := 0.0
4336 4346 Perf_Background_Dpkg.Psfpolfnltg := 0.0
4337 4347 Perf_Background_Dpkg.Pctcstridx := 1
4338 4348 Perf_Background_Dpkg.Pslcautoctl := True
4339 4349 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
4340 4350 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
4341 4351 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
4342 4352 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
4343 4353 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
4344 4354 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
4345 4355 Perf_Background_Dpkg.Pcfpln := Secprimary
4346 4356 Perf_Background_Dpkg.Pcfltphase := Cruise
4347 4357 Perf_Background_Dpkg.Psfinaldes := True
4348 4358 Perf_Background_Dpkg.Pccompet(Active) := True
4349 4359 Perf_Background_Dpkg.Vert_Auto_Mode := True
4350 4360 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
4351 4361 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
4352 4362 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
4353 4363 Perf_background_Dpkg.Maxalt.Num_Engout := 0
4354 4364 Perf_Background_Dpkg.Etp_Itin_Ran := False
4355 4365 Perf_Background_Dpkg.Ett(Active).Data := 20.0
4356 4366 Perf_Background_Dpkg.Ett(Active).Status := Valid

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

4357 4367 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
4358 4368 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
4359 4369 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
4360 4370 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
4361 4371 Perf_Background_Dpkg.Pcitin.Flight_Plan := Secondary
4362 4372 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
4363 4373 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
4364 4374 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
4365 4375 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
4366 4376 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
4367 4377 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
4368 4378 Perf_Background_Dpkg.Psprddataseq := 3
4369 4379 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
4370 4380 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
4371 4381 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data := 5.0
4372 4382 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status := Invalid
4373 4383 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh := False
4374 4384
4375 4385 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
4376 4386 #Change := False
4377 4387 #go
4378 4388 #end
4379 4389 #delb/all
4380 4390
4381 4391 !run_test()
4382 4392
4383 4393 -- OUTPUTS
4384 4394
4385 4395 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr = 0
4386 4396 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog = True
4387 4397 Perf_Background_Dpkg.Psfinalalt = 0.0
4388 4398 Perf_Background_Dpkg.Psfpolfnlful = 0.0
4389 4399 Perf_Background_Dpkg.Psfpolfnltme = 0.0
4390 4400 Perf_Background_Dpkg.Psfpolfnltg = 0.0
4391 4401 CTP_PERF_BKGND_PUT_BK_DATA.Putperflag = False
4392 4402 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data = 20.0
4393 4403 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status = Valid
4394 4404 Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh = True
4395 4405
4396 4406
4397 4407 TESTID: 39
4398 4408
4399 4409 Time Constraint Processing :
4400 4410 Cost Index computation is for Active fpln TIME CSTR.

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

4401 4411 Performance Cost index cannot be released to the system, the RTA working and control data have been output
4402 4412 through the Perf RTA object manager.
4403 4413 (PERF_SDD_3520_INT).
4404 4414 Time Constraint Control data is stored out to the object manager after each pass of Predictions
4405 4415 (PERF_SDD_3106_INT).
4406 4416 This Test verifies for the output when the data is not transmitted to slave FM, Hence it stores the previous value.
4407 4417
4408 4418
4409 4419 -- INPUTS:
4410 4420
4411 4421 Perf_Background_Dpkg.Pcitin.Itinerary := Time_Constraint_Eval
4412 4422 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
4413 4423 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Adjcostidx := 10.0
4414 4424 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Lastphase := Descent
4415 4425 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Glidx := 100
4416 4426 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Fpln := Secondary
4417 4427 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Valid := False
4418 4428 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Eval_Done := False
4419 4429 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Env_Limit := False
4420 4430 Perf_Background_Dpkg.Pctcstrctrl(Active).Adjcostidx := 20.0
4421 4431 Perf_Background_Dpkg.Pctcstrctrl(Active).Lastphase := Cruise
4422 4432 Perf_Background_Dpkg.Pctcstrctrl(Active).Glidx := 2
4423 4433 Perf_Background_Dpkg.Pcactorsec := Active
4424 4434 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
4425 4435 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
4426 4436 Perf_Background_Dpkg.Pctcstrctrl(Active).Envelope_Limit := True
4427 4437 Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit := False
4428 4438
4429 4439 !run_test()
4430 4440
4431 4441 -- OUTPUTS
4432 4442
4433 4443 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Adjcostidx = 10.0
4434 4444 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Lastphase = Descent
4435 4445 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Glidx = 100
4436 4446 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Fpln = Secondary
4437 4447 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Valid = False
4438 4448 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Eval_Done = False
4439 4449 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Env_Limit = False
4440 4450 Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit = False
4441 4451 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid = True
4442 4452 CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg = False
4443 4453
4444 4454

```



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

4445 4455 TESTID: 40
4446 4456
4447 4457 In the procedure Prf_Int_Utils.Update_Refresh_Timer updates the passed-in timer's record data. The passed in timer's r
» efresh
4448 4458 time shall be set to the difference between the current FM time and the timer's reference start time, and the timer's
» reference
4449 4459 start time set equal to the current FM time.
4450 4460 (PERF_SDD_3500_INT)
4451 4461 A running average of the most recent refresh time data points (up to five) shall be computed and stored in the passed-
» in timer's
4452 4462 record data, along with the actual refresh time data points (up to five) used to compute the average.
4453 4463 This Test also verifies for the output when the number of points are equal to the maximum refresh points.
4454 4464 (PERF_SDD_3501_INT)
4455 4465
4456 4466
4457 4467 -- INPUTS:
4458 4468
4459 4469 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx := 2
4460 4470 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change := False
4461 4471 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := False
4462 4472 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := False
4463 4473 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := False
4464 4474 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := False
4465 4475 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := False
4466 4476 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec := False
4467 4477 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg) := 2
4468 4478 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest := 0.0
4469 4479 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo := 0.0
4470 4480 Perf_Etp_DPkg:body.Data.Storage.Ckequidata.Data(1).Pack_Vals.Predinprog := True
4471 4481 Perf_Background_DPkg.Opt_Step_Data.Distodest := 25.0
4472 4482 Perf_Background_DPkg.Opt_Step_Data.Timetogo := 5.0
4473 4483 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed := 0.0
4474 4484 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel := 0.0
4475 4485 Perf_Background_Dpkg.Pshmpreddata.Speed := 250.0
4476 4486 Perf_Background_Dpkg.Pshmpreddata.Fuel := 50.0
4477 4487 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid := False
4478 4488 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data := 0.0
4479 4489 Perf_Background_Dpkg.Pcoptalt.Valid := True
4480 4490 Perf_Background_Dpkg.Pcoptalt.Data := 19000.0
4481 4491 Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master
4482 4492 Ctp_Perf_bkgnd_put_bk_data.Boot_Status := Warm_Start
4483 4493 Perf_Background_Dpkg.Preds_Output(Active) := True
4484 4494 Perf_Background_Dpkg.Psfinalalt := 0.0
4485 4495 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt := 5000

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

4486 4496 Perf_Background_Dpkg.Psfpolfnlful := 0.0
4487 4497 Perf_Background_Dpkg.Psfpolfnltme := 0.0
4488 4498 Perf_Background_Dpkg.Psfpolfnltg := 0.0
4489 4499 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel := 40
4490 4500 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time := 50
4491 4501 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time := 60
4492 4502 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := True
4493 4503 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
4494 4504 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass := False
4495 4505 Perf_Background_Dpkg.Pcfpln := Actprimary
4496 4506 Perf_Background_Dpkg.Pcfltphase := Cruise
4497 4507 Perf_Background_Dpkg.Psfinaldes := True
4498 4508 Perf_Background_Dpkg.Vert_Auto_Mode := True
4499 4509 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data := 50000.0
4500 4510 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data := 55000.0
4501 4511 Perf_background_Dpkg.Maxalt.Gwt := 150000.0
4502 4512 Perf_background_Dpkg.Maxalt.Num_Engout := 0
4503 4513 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid := False
4504 4514 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid := False
4505 4515 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Single
4506 4516 Perf_Dpkg.Pstopofcrzfl(Active).Valid := False
4507 4517 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
4508 4518 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
4509 4519 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
4510 4520 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
4511 4521 Perf_Background_Dpkg.Pcgmttime.Gpc_Time := 2
4512 4522 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
4513 4523 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
4514 4524 Perf_Background_Dpkg.Psprddataseq := 3
4515 4525 Perf_Background_Dpkg.Etp_Itin_Ran := False
4516 4526 cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc := True
4517 4527 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
4518 4528
4519 4529 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Number_Of_Points := 5
4520 4530 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(1) := 4.0
4521 4531 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(2) := 3.0
4522 4532 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(3) := 2.0
4523 4533 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Average_Refresh_Time := 0.0
4524 4534
4525 4535 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Start_Time := 0
4526 4536 Fmcs_Partition_Data_Pkg.Ops_Time.Gpc_Time := 20
4527 4537 Ops_Timer_Pkg:body.Ops_time.Gpc_Time := 30
4528 4538 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
4529 4539 #Change := False

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

4530 4540 #go
4531 4541 #end
4532 4542 #delb/all
4533 4543
4534 4544 # sba Prf_Int_Utils.Update_Refresh_Timer before_end
4535 4545 # go
4536 4546 Timer.Start_Time = 30
4537 4547 Timer.Refresh_Time = 0.001
4538 4548 Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Average_Refresh_Time = 1.0002
4539 4549 #delb/all
4540 4550
4541 4551 !run_test()
4542 4552
4543 4553 -- OUTPUT
4544 4554
4545 4555
4546 4556 TESTID: 41
4547 4557
4548 4558 All the inflection points stored in Flight Planning Working Layer shall be moved to
4549 4559 Flight Planning Active Layer as follows:
4550 4560     Get write point access to the Flight Plan being modified by calling routine
4551 4561     Perf_Lgb_Interface_Mgr_Pkg.Requestlgb
4552 4562     if the current executing itinerary is Primary Fpln Prediction, then
4553 4563     Activate Strategic Working Point List for the Flight Plan being modified by calling routine
4554 4564     Fpp_Wrap_Point_Pkg.Activate_Strategic_Working_Point_List.
4555 4565     if the current executing itinerary is Current_Mode_Preds or Current_Mode_Hi_Pri, then
4556 4566     Activate Tactical Working Point List for the Flight Plan being modified by calling routine
4557 4567     Fpp_Wrap_Point_Pkg.Activate_Tactical_Working_Point_List.
4558 4568     Release write point access to the Flight Plan being modified by calling routine
4559 4569     Perf_Lgb_Interface_Mgr_Pkg.Releaselgb
4560 4570 PERF_SDD_7018, PERF_SDD_07154
4561 4571
4562 4572 If the current itinerary is Active Primary Flight Plan Predictions,
4563 4573 then utility procedure Prf_Int_Utils.Align_Segments_At_Leg with inputs of active leg index
4564 4574 shall be called within the same LGB access for activating the strategic inflection points.
4565 4575 PERF_SDD_07527
4566 4576
4567 4577
4568 4578 -- INPUTS:
4569 4579
4570 4580 Perf_Background_Dpkg.Pcitin.Itinerary := Perf_Int_Base_Tpkg.Prim_Fpln_Preds
4571 4581 Perf_Background_Dpkg.Psstepover := False
4572 4582 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
4573 4583 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```
4574 4584
4575 4585 #define Request_LGB_Called := FALSE
4576 4586 #define Activate_Strategic_Working_Point_List_Called := FALSE
4577 4587 #define Align_Segments_At_Leg_Exec := False
4578 4588 #define Activate_Tactical_Working_Point_List_Called := FALSE
4579 4589 #define Release_lgb_Called := FALSE
4580 4590
4581 4591 #sba Perf_Lgb_Interface_Mgr_Pkg.Request_lgb after_elab begin
4582 4592 #define Request_LGB_Called := TRUE
4583 4593 #go
4584 4594 #end
4585 4595
4586 4596 #sba Prf_Int_Utils.Align_Segments_At_Leg after_elab begin
4587 4597 #define Align_Segments_At_Leg_Exec := True
4588 4598 #go
4589 4599 #end
4590 4600
4591 4601 #sba Fpp_Wrap_Point_Pkg.Activate_Strategic_Working_Point_List after_elab begin
4592 4602 #define Activate_Strategic_Working_Point_List_Called := TRUE
4593 4603 #go
4594 4604 #end
4595 4605
4596 4606 #sba Fpp_Wrap_Point_Pkg.Activate_Tactical_Working_Point_List after_elab begin
4597 4607 #define Activate_Tactical_Working_Point_List_Called := TRUE
4598 4608 #go
4599 4609 #end
4600 4610
4601 4611 #sba Perf_Lgb_Interface_Mgr_Pkg.Release_lgb after_elab begin
4602 4612 #define Release_lgb_Called := TRUE
4603 4613 #go
4604 4614 #end
4605 4615
4606 4616 !run_test()
4607 4617
4608 4618 -- OUTPUTS
4609 4619
4610 4620 Request_LGB_Called = TRUE
4611 4621 Activate_Strategic_Working_Point_List_Called = TRUE
4612 4622 Activate_Tactical_Working_Point_List_Called = FALSE
4613 4623 Release_lgb_Called = TRUE
4614 4624 Align_Segments_At_Leg_Exec = True
4615 4625
4616 4626
4617 4627 TESTID: 42
```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```
4618 4628
4619 4629 All the inflection points stored in Flight Planning Working Layer shall be moved to
4620 4630 Flight Planning Active Layer as follows:
4621 4631     Get write point access to the Flight Plan being modified by calling routine
4622 4632     Perf_Lgb_Interface_Mgr_Pkg.Requestlgb
4623 4633     if the current executing itinerary is Primary Fpln Prediction, then
4624 4634     Activate Strategic Working Point List for the Flight Plan being modified by calling routine
4625 4635     Fpp_Wrap_Point_Pkg.Activate_Strategic_Working_Point_List.
4626 4636     if the current executing itinerary is Current_Mode_Preds or Current_Mode_Hi_Pri, then
4627 4637     Activate Tactical Working Point List for the Flight Plan being modified by calling routine
4628 4638     Fpp_Wrap_Point_Pkg.Activate_Tactical_Working_Point_List.
4629 4639     Release write point access to the Flight Plan being modified by calling routine
4630 4640     Perf_Lgb_Interface_Mgr_Pkg.Releaselgb
4631 4641 PERF_SDD_7018, PERF_SDD_07154
4632 4642
4633 4643 If the current itinerary is not Active Primary Flight Plan Predictions, then utility procedure
4634 4644 Prf_Int_Utills.Align_Segments_At_Leg shall not be called .
4635 4645 PERF_SDD_07527
4636 4646
4637 4647
4638 4648 -- INPUTS:
4639 4649
4640 4650 Perf_Background_Dpkg.Pcitin.Itinerary := Perf_Int_Base_Tpkg.Current_Mode_Hi_Pri
4641 4651 Perf_Background_Dpkg.Psstepover := False
4642 4652 Perf_Background_Dpkg.Pcitin.Flight_Plan := Secondary
4643 4653 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
4644 4654
4645 4655 #define Request_LGB_Called := FALSE
4646 4656 #define Activate_Strategic_Working_Point_List_Called := FALSE
4647 4657 #define Align_Segments_At_Leg_Exec := False
4648 4658 #define Activate_Tactical_Working_Point_List_Called := FALSE
4649 4659 #define Releaselgb_Called := FALSE
4650 4660
4651 4661 #sba Perf_Lgb_Interface_Mgr_Pkg.Requestlgb after_elab begin
4652 4662 #define Request_LGB_Called := TRUE
4653 4663 #go
4654 4664 #end
4655 4665
4656 4666 #sba Prf_Int_Utills.Align_Segments_At_Leg after_elab begin
4657 4667 #define Align_Segments_At_Leg_Exec := True
4658 4668 #go
4659 4669 #end
4660 4670
4661 4671 #sba Fpp_Wrap_Point_Pkg.Activate_Strategic_Working_Point_List after_elab begin
```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

4662 4672 #define Activate_Strategic_Working_Point_List_Called := TRUE
4663 4673 #go
4664 4674 #end
4665 4675
4666 4676 #sba Fpp_Wrap_Point_Pkg.Activate_Tactical_Working_Point_List after_elab begin
4667 4677 #define Activate_Tactical_Working_Point_List_Called := TRUE
4668 4678 #go
4669 4679 #end
4670 4680
4671 4681 #sba Perf_Lgb_Interface_Mgr_Pkg.Releaselgb after_elab begin
4672 4682 #define Releaselgb_Called := TRUE
4673 4683 #go
4674 4684 #end
4675 4685
4676 4686 !run_test()
4677 4687
4678 4688 -- OUTPUTS
4679 4689
4680 4690 Request_LGB_Called = TRUE
4681 4691 Align_Segments_At_Leg_Exec = False
4682 4692 Activate_Strategic_Working_Point_List_Called = FALSE
4683 4693 Activate_Tactical_Working_Point_List_Called= TRUE
4684 4694 Releaselgb_Called = TRUE
4685 4695
4686 4696
4687 4697 TESTID: 43
4688 4698
4689 4699 All the inflection points stored in Flight Planning Working Layer shall be moved to
4690 4700 Flight Planning Active Layer as follows:
4691 4701     Get write point access to the Flight Plan being modified by calling routine
4692 4702     Perf_Lgb_Interface_Mgr_Pkg.Requestlgb
4693 4703     if the current executing itinerary is Primary Fpln Prediction, then
4694 4704     Activate Strategic Working Point List for the Flight Plan being modified by calling routine
4695 4705     Fpp_Wrap_Point_Pkg.Activate_Strategic_Working_Point_List.
4696 4706     if the current executing itinerary is Current_Mode_Preds or Current_Mode_Hi_Pri, then
4697 4707     Activate Tactical Working Point List for the Flight Plan being modified by calling routine
4698 4708     Fpp_Wrap_Point_Pkg.Activate_Tactical_Working_Point_List.
4699 4709     Release write point access to the Flight Plan being modified by calling routine
4700 4710     Perf_Lgb_Interface_Mgr_Pkg.Releaselgb
4701 4711 PERF_SDD_7018, PERF_SDD_07154
4702 4712
4703 4713
4704 4714 -- INPUTS:
4705 4715

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```
4706 4716 Perf_Background_Dpkg.Pcitin.Itinerary := Perf_Int_Base_Tpkg.Current_Mode_Preds
4707 4717 Perf_Background_Dpkg.Psstepover := False
4708 4718
4709 4719 #define Request_LGB_Called := FALSE
4710 4720 #define Activate_Strategic_Working_Point_List_Called := FALSE
4711 4721 #define Activate_Tactical_Working_Point_List_Called := FALSE
4712 4722 #define Releaselgb_Called := FALSE
4713 4723
4714 4724 #sba Perf_Lgb_Interface_Mgr_Pkg.Requestlgb after_elab begin
4715 4725 #define Request_LGB_Called := TRUE
4716 4726 #go
4717 4727 #end
4718 4728
4719 4729 #sba Fpp_Wrap_Point_Pkg.Activate_Strategic_Working_Point_List after_elab begin
4720 4730 #define Activate_Strategic_Working_Point_List_Called := TRUE
4721 4731 #go
4722 4732 #end
4723 4733
4724 4734 #sba Fpp_Wrap_Point_Pkg.Activate_Tactical_Working_Point_List after_elab begin
4725 4735 #define Activate_Tactical_Working_Point_List_Called := TRUE
4726 4736 #go
4727 4737 #end
4728 4738
4729 4739 #sba Perf_Lgb_Interface_Mgr_Pkg.Releaselgb after_elab begin
4730 4740 #define Releaselgb_Called := TRUE
4731 4741 #go
4732 4742 #end
4733 4743
4734 4744 !run_test()
4735 4745
4736 4746 -- OUTPUTS
4737 4747
4738 4748 Request_LGB_Called = TRUE
4739 4749 Activate_Strategic_Working_Point_List_Called = FALSE
4740 4750 Activate_Tactical_Working_Point_List_Called= TRUE
4741 4751 Releaselgb_Called = TRUE
4742 4752
4743 4753
4744 4754 TESTID: 44
4745 4755
4746 4756 All the inflection points stored in Flight Planning Working Layer shall be moved to
4747 4757 Flight Planning Active Layer as follows:
4748 4758     Get write point access to the Flight Plan being modified by calling routine
4749 4759     Perf_Lgb_Interface_Mgr_Pkg.Requestlgb
```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

4750 4760         if the current executing itinerary is Primary Fpln Prediction, then
4751 4761         Activate Strategic Working Point List for the Flight Plan being modified by calling routine
4752 4762         Fpp_Wrap_Point_Pkg.Activate_Strategic_Working_Point_List.
4753 4763         if the current executing itinerary is Current_Mode_Preds or Current_Mode_Hi_Pri, then
4754 4764         Activate Tactical Working Point List for the Flight Plan being modified by calling routine
4755 4765         Fpp_Wrap_Point_Pkg.Activate_Tactical_Working_Point_List.
4756 4766         Release write point access to the Flight Plan being modified by calling routine
4757 4767         Perf_Lgb_Interface_Mgr_Pkg.Releaselgb
4758 4768 PERF_SDD_7018, PERF_SDD_07154
4759 4769
4760 4770
4761 4771 -- INPUTS:
4762 4772
4763 4773 Perf_Background_Dpkg.Pcitin.Itinerary := Perf_Int_Base_Tpkg.Fuel_Plan_Stage2
4764 4774 Perf_Background_Dpkg.Psstepover := TRUE
4765 4775
4766 4776 #define Request_LGB_Called := FALSE
4767 4777 #define Activate_Strategic_Working_Point_List_Called := FALSE
4768 4778 #define Activate_Tactical_Working_Point_List_Called := FALSE
4769 4779 #define Releaselgb_Called := FALSE
4770 4780
4771 4781 #sba Perf_Lgb_Interface_Mgr_Pkg.Requestlgb after_elab begin
4772 4782 #define Request_LGB_Called := TRUE
4773 4783 #go
4774 4784 #end
4775 4785
4776 4786 #sba Fpp_Wrap_Point_Pkg.Activate_Strategic_Working_Point_List after_elab begin
4777 4787 #define Activate_Strategic_Working_Point_List_Called := TRUE
4778 4788 #go
4779 4789 #end
4780 4790
4781 4791 #sba Fpp_Wrap_Point_Pkg.Activate_Tactical_Working_Point_List after_elab begin
4782 4792 #define Activate_Tactical_Working_Point_List_Called := TRUE
4783 4793 #go
4784 4794 #end
4785 4795
4786 4796 #sba Perf_Lgb_Interface_Mgr_Pkg.Releaselgb after_elab begin
4787 4797 #define Releaselgb_Called := TRUE
4788 4798 #go
4789 4799 #end
4790 4800
4791 4801 !run_test()
4792 4802
4793 4803 -- OUTPUTS

```



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

4794	4804	
4795	4805	Request_LGB_Called = FALSE
4796	4806	Activate_Strategic_Working_Point_List_Called = FALSE
4797	4807	Activate_Tactical_Working_Point_List_Called = FALSE
4798	4808	Releaselgb_Called = FALSE
4799	4809	
4800	4810	
4801	4811	TESTID: 45
4802	4812	
4803	4813	The Flight Plan indicator LOCFP is set to Active for a temporary flight plan.
4804	4814	(PERF_SDD_5617_INT)
4805	4815	when a data save is initiated from the MRO page and it shall set the Perf_Data_Save_Initiated flag to true
4806	4816	to prevent a subsequent Data Save from being initiated while a Data Save is already in progress.
4807	4817	PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT)
4808	4818	
4809	4819	
4810	4820	-- INPUTS:
4811	4821	
4812	4822	Perf_Background_Dpkg.Pcactorsec := Fprequestrec.Types.Temporary
4813	4823	CTP_PERF_BKGND_PUT_BK_DATA.Data := 6
4814	4824	Perf_Vdu_Dpkg.Data_Save := Perf_Vdu_Tpkg.None
4815	4825	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated := False
4816		<del>#sba PERF_BKGND_PKG.PUT_BK_DATA #572</del>
	4826	#sba PERF_BKGND_PKG.PUT_BK_DATA #574
4817	4827	#go
4818	4828	Locfp = Active
4819	4829	#delb/all
4820	4830	
4821	4831	!run_test()
4822	4832	Perf_Vdu_Dpkg.Data_Save = Perf_Vdu_Tpkg.Current_Mode
4823	4833	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated = True
4824	4834	
4825	4835	TESTID: 46
4826	4836	
4827	4837	The Flight Plan indicator LOCFP is set to Perf_Background_Dpkg.Pcactorsec for all flight plan other than temporary fli » ght plan.
4828	4838	(PERF_SDD_5617_INT)
4829	4839	when a data save is initiated from the MRO page and it shall set the Perf_Data_Save_Initiated flag to true
4830	4840	to prevent a subsequent Data Save from being initiated while a Data Save is already in progress.
4831	4841	PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT)
4832	4842	
4833	4843	
4834	4844	-- INPUTS:
4835	4845	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

4836	4846	Perf_Background_Dpkg.Pcactorsec := Active
4837	4847	CTP_PERF_BKGND_PUT_BK_DATA.Data := 5
4838	4848	Perf_Vdu_Dpkg.Data_Save := Perf_Vdu_Tpkg.None
4839	4849	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated := False
4840		<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #572</del>
	4850	#sba PRF_BKGND_PKG.PUT_BK_DATA #574
4841	4851	#go
4842	4852	Locfp = Active
4843	4853	#delb/all
4844	4854	
4845	4855	!run_test()
4846	4856	Perf_Vdu_Dpkg.Data_Save = Perf_Vdu_Tpkg.Secondary3
4847	4857	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated = True
4848	4858	
4849	4859	TESTID: 47
4850	4860	
4851	4861	ETT data has not been transmitted from the slave FM to the Master
4852	4862	(PERF_SDD_3518_INT).
4853	4863	when a data save is initiated from the MRO page and it shall set the Perf_Data_Save_Initiated flag to true
4854	4864	to prevent a subsequent Data Save from being initiated while a Data Save is already in progress.
4855	4865	PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT)
4856	4866	
4857	4867	If the data buffering process has not started based on the user request than following shall not be done
4858	4868	(PERF_SDD_07467_INT)
4859	4869	- Following procedure shall be called:
4860	4870	Prf_Vdu_Utils.Save_Leg_Data - To buffer flight plan data.
4861	4871	Prf_Vdu_Utils.Save_Pseudo_Data - To buffer psuedo waypoint data.
4862	4872	Prf_Vdu_Utils.Save_Vga_Data - To buffer vertical guidance array data.
4863	4873	Prf_Vdu_Utils.Save_Altitude_Data - To buffer important altitude values.
4864	4874	(PERF_SDD_07468_INT)
4865	4875	- After all the required data is buffered to VDU buffer the buffer validity shall be set to true
4866	4876	and buffer prediction data sequence counter is set to current guidance header sequence counter.
4867	4877	(PERF_SDD_07470_INT)
4868	4878	- Flag indicating VDU Buffer save has been initiated for this pass of preds and
4869	4879	the flag indicating the data buffering process has started shall be set to false
4870	4880	(PERF_SDD_07471_INT)
4871	4881	
4872	4882	
4873	4883	-- INPUTS:
4874	4884	
4875	4885	CTP_PERF_BKGND_PUT_BK_DATA.Data := 4
4876	4886	Perf_Vdu_Dpkg.Data_Save := Perf_Vdu_Tpkg.None
4877	4887	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst := False
4878	4888	Fmcs_Partition_Data_Pkg.Ops_Master_Status := Master

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

4879	4889	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode := Dual
4880	4890	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
4881	4891	Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk := False
4882	4892	Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
4883	4893	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
4884	4894	Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
4885	4895	Perf_Background_Dpkg.Pctcstridx := 1
4886	4896	Perf_Background_Dpkg.Pcdestglidx := 0
4887	4897	Perf_Background_Dpkg.Pctcstrctrl(Active).Timeonly := True
4888	4898	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done := False
4889	4899	Perf_Background_Dpkg.Pcfltphase := Cruise
4890	4900	Perf_Background_Dpkg.Rta.Missed := False
4891	4901	Perf_Background_Dpkg.Pcperflegs(18).Included := True
4892	4902	Perf_Background_Dpkg.Pcperflegs(18).Dist := 600.0
4893	4903	Perf_Background_Dpkg.Pcstartpt.Dist := 600.0
4894	4904	Perf_Background_Dpkg.Pccompett(Active) := True
4895	4905	Perf_Background_Dpkg.Rta.Eval_Done := True
4896	4906	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid := True
4897	4907	Perf_Background_Dpkg.Ett(Active).Data := 20.0
4898	4908	Perf_Background_Dpkg.Ett(Active).Status := Valid
4899	4909	Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit := True
4900	4910	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data := 5.0
4901	4911	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status := Invalid
4902	4912	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh := False
4903	4913	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status := Perf_Int_Base_Tpkg.Dual_Slave
4904	4914	
4905	4915	Perf_Vdu_Dpkg.Vdu_Buffer.Buffer_Valid := False
4906	4916	Perf_Vdu_Dpkg.Vdu_Buffer.Prddataseq := 0
4907	4917	Perf_Background_Dpkg.Psprddataseq := 1
4908	4918	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated := True
4909	4919	CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec := False
4910	4920	CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec := False
4911	4921	CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec := False
4912	4922	CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec := False
4913	4923	
4914	4924	#sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
4915	4925	#Change := False
4916	4926	#go
4917	4927	#end
4918		<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #797</del>
	4928	<del>#sba PRF_BKGND_PKG.PUT_BK_DATA #801</del>
4919	4929	#go
4920	4930	Ett_Sys.Data_Fresh = False
4921	4931	Send_Ett = False

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

4922 4932 #delb/all
4923 4933
4924 4934 #Sba Prf_Vdu_Utils.Complete_Buffer after_elab
4925 4935 #go
4926 4936 Prf_Vdu_Utils:body.Data_Save_In_Progress := False
4927 4937 #sba Prf_Vdu_Utils.Save_Leg_Data after_elab begin
4928 4938 CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec := True
4929 4939 #go
4930 4940 #end
4931 4941 #sba Prf_Vdu_Utils.Save_Pseudo_Data after_elab begin
4932 4942 CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec := True
4933 4943 #go
4934 4944 #end
4935 4945 #sba Prf_Vdu_Utils.Save_Vga_Data after_elab begin
4936 4946 CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec := True
4937 4947 #go
4938 4948 #end
4939 4949 #sba Prf_Vdu_Utils.Save_Altitude_Data after_elab begin
4940 4950 CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec := True
4941 4951 #go
4942 4952 #end
4943 4953 #delb/a
4944 4954 #Sba Prf_Vdu_Utils.Complete_Buffer before_end
4945 4955 #go
4946 4956 CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec = False
4947 4957 CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec = False
4948 4958 CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec = False
4949 4959 CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec = False
4950 4960 Perf_Vdu_Dpkg.Vdu_Buffer.Buffer_Valid = False
4951 4961 Perf_Vdu_Dpkg.Vdu_Buffer.Prddataseq = 0
4952 4962 Perf_Vdu_Dpkg.Perf_Data_Save_Initiated = True
4953 4963 Perf_Vdu_Dpkg.Perf_Data_Save_Initiated := False
4954 4964
4955 4965 !run_test()
4956 4966
4957 4967 Perf_Vdu_Dpkg.Data_Save = Perf_Vdu_Tpkg.Secondary2
4958 4968 Perf_Vdu_Dpkg.Perf_Data_Save_Initiated = True
4959 4969
4960 4970
4961 4971 TESTID: 48
4962 4972
4963 4973 when a data save is initiated from the MRO page and it shall set the Perf_Data_Save_Initiated flag to true
4964 4974 to prevent a subsequent Data Save from being initiated while a Data Save is already in progress.
4965 4975 PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT)

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

4966 4976 - The distance to destination of point data buffered as a part of trajectory data shall be unbaised for all the points
      » buffered.
4967 4977 PERF_SDD_07469_INT
4968 4978
4969 4979 REQUIREMENTS UNDER EVALUATION : PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT),
4970 4980 PERF_SDD_07469_INT
4971 4981
4972 4982 SUPPORTING REQUIREMENTS : N/A
4973 4983
4974 4984
4975 4985 -- INPUTS:
4976 4986
4977 4987 CTP_PERF_BKGND_PUT_BK_DATA.Data := 3
4978 4988 Perf_Vdu_Dpkg.Data_Save := Perf_Vdu_Tpkg.None
4979 4989 Perf_Vdu_Dpkg.Perf_Data_Save_Initiated := False
4980 4990 Perf_Background_Dpkg.Pcitin.Itinerary := Goaround
4981 4991 Perf_Background_Dpkg.Destination_Data.Efob.Data := 20.0
4982 4992 Perf_Background_Dpkg.Destination_Data.Efob.Valid := True
4983 4993 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Data := 0.0
4984 4994 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Valid := False
4985 4995 #sba Prf_Vdu_Utils.Unbias_Points after_elab
4986 4996 #go
4987 4997 Prf_Vdu_Utils:body.Data_Save_In_Progress := False
4988 4998 Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Number_Of_Points := 2
4989 4999 Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Point_Data(1).Aircraft_State.Distance_To_Destination := 500.0
4990 5000 Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Point_Data(2).Aircraft_State.Distance_To_Destination := 100.0
4991 5001 Perf_Dpkg.Psbias := 400.0
4992 5002
4993 5003 !run_test()
4994 5004
4995 5005 -- OUTPUTS
4996 5006
4997 5007 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Data = 20.0
4998 5008 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Valid = True
4999 5009 Perf_Vdu_Dpkg.Data_Save = Perf_Vdu_Tpkg.Secondary1
5000 5010 Perf_Vdu_Dpkg.Perf_Data_Save_Initiated = True
5001 5011 Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Point_Data(1).Aircraft_State.Distance_To_Destination = 500.0
5002 5012 Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Point_Data(2).Aircraft_State.Distance_To_Destination = 100.0
5003 5013
5004 5014
5005 5015 TESTID: 49
5006 5016
5007 5017 when a data save is initiated from the MRO page and it shall set the Perf_Data_Save_Initiated flag to true
5008 5018 to prevent a subsequent Data Save from being initiated while a Data Save is already in progress.

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

5009 5019 PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT)
5010 5020 - The distance to destination of point data buffered as a part of trajectory data shall be unbaised for all the points
      5021 » buffered.
5011 5021 PERF_SDD_07469_INT
5012 5022
5013 5023 REQUIREMENTS UNDER EVALUATION : PERF_SDD_07469_INT
5014 5024
5015 5025 SUPPORTING REQUIREMENTS : N/A
5016 5026
5017 5027
5018 5028 -- INPUTS:
5019 5029 CTP_PERF_BKGND_PUT_BK_DATA.Data := 0
5020 5030 Perf_Vdu_Dpkg.Data_Save := Perf_Vdu_Tpkg.Secondary1
5021 5031 Perf_Background_Dpkg.Pcitin.Itinerary := Goaround
5022 5032 Perf_Background_Dpkg.Destination_Data.Efob.Data := 20.0
5023 5033 Perf_Background_Dpkg.Destination_Data.Efob.Valid := True
5024 5034 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Data := 0.0
5025 5035 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Valid := False
5026 5036
5027 5037 #sba Prf_Vdu_Utils.Unbias_Points after_elab
5028 5038 #go
5029 5039 Prf_Vdu_Utils:body.Data_Save_In_Progress := True
5030 5040 Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Number_Of_Points:= 2
5031 5041 Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Point_Data(1).Aircraft_State.Distance_To_Destination := 500.0
5032 5042 Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Point_Data(2).Aircraft_State.Distance_To_Destination := 100.0
5033 5043 Perf_Dpkg.Psbias := 400.0
5034 5044
5035 5045 !run_test()
5036 5046
5037 5047 -- OUTPUTS
5038 5048
5039 5049 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Data = 20.0
5040 5050 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Valid = True
5041 5051 Perf_Vdu_Dpkg.Data_Save = Perf_Vdu_Tpkg.None
5042 5052 Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Point_Data(1).Aircraft_State.Distance_To_Destination = 100.0
5043 5053 Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Point_Data(2).Aircraft_State.Distance_To_Destination = -300.0
5044 5054
5045 5055
5046 5056 TESTID: 50
5047 5057 If lateral segments are valid for the Active flight plan, then the following shall be performed to
5048 5058 align the lateral segments such that the DTD of the last segment of the input leg matches the DTD of the input leg:
5049 5059 1.The leg's last active segment is retrieved via Fpp_Wrap_Pkg.Get_Legs_Last_Active_Segment.
5050 5060 2.The leg corresponding to the input leg index is retrieved via Common_Lgb_Getlgbleg.
5051 5061 3.The adjustment factor (bias) is set to the leg's last segment DTD minus(the leg's DTD minus the leg's DTD bias).
```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

5052 5062 4.All segments in the working layer are deleted by calling Fpp_Wrap_Segment_Pkg.Delete_All_Segments_From_Working_List.
5053 5063 5.The active layer segments are copied to the working layer by calling Fpp_Wrap_Segment_Pkg.Copy_Active_Segments_To_Wo
    » rking.
5054 5064 6.The adjustment factor is removed from the working segments by calling
5055 5065     Fpp_Wrap_Segment_Pkg.Un_Bias_DTD_For_All_Working_Segments with the input computed bias.
5056 5066 when a data save is initiated from the MRO page and it shall set the Perf_Data_Save_Initiated flag to true
5057 5067 to prevent a subsequent Data Save from being initiated while a Data Save is already in progress.
5058 5068 PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT)
5059 5069 If the data buffering process has not started based on the user request than following shall not be done
5060 5070 (PERF_SDD_07467_INT)
5061 5071 - Following procedure shall be called:
5062 5072 Prf_Vdu_Utils.Save_Leg_Data - To buffer flight plan data.
5063 5073 Prf_Vdu_Utils.Save_Pseudo_Data - To buffer psuedo waypoint data.
5064 5074 Prf_Vdu_Utils.Save_Vga_Data - To buffer vertical guidance array data.
5065 5075 Prf_Vdu_Utils.Save_Altitude_Data - To buffer important altitude values.
5066 5076 (PERF_SDD_07468_INT)
5067 5077 - After all the required data is buffered to VDU buffer the buffer validity shall be set to true
5068 5078     and buffer prediction data sequence counter is set to current guidance header sequence counter.
5069 5079 (PERF_SDD_07470_INT)
5070 5080 - Flag indicating VDU Buffer save has been initiated for this pass of preds and
5071 5081     the flag indicating the data buffering process has started shall be set to false
5072 5082 (PERF_SDD_07471_INT)
5073 5083 This function(Get_Data_Save_State) shall return the flag Perf_Data_Save_Initiated that is used to prevent a subsequent
    » Data Save
5074 5084 from being initiated while a Data Save is already in progress. While the flag is true, a new data save cannot be initi
    » ated.
5075 5085 PERF_SDD_07481(PERF_SRD_23173_INT)
5076 5086 This function(Int_To_Str) shall always return a string of two characters; the characters are always the digits.
5077 5087 The first digit of the string is the result of integer division of input number by 10.
5078 5088 The second digit of the string is the result of following equation: input number - first digit * 10.
5079 5089 PERF_SDD_07480_INT
5080 5090
5081 5091 REQUIREMENTS UNDER EVALUATION : PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT), PERF_SDD_07481(PERF_SRD_23173_
    » INT),
5082 5092                                     PERF_SDD_07467_INT, PERF_SDD_07468_INT, PERF_SDD_07470_INT, PERF_SDD_07471_INT, PERF_S
    » DD_07480_INT
5083 5093
5084 5094 SUPPORTING REQUIREMENTS : N/A
5085 5095
5086 5096
5087 5097 -- INPUTS:
5088 5098 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfregst := False
5089 5099 CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec      := False
5090 5100 CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec   := False

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

5091 5101 CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec      := False
5092 5102 CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec := False
5093 5103 Perf_Background_Dpkg.Psstepover := False
5094 5104 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
5095 5105 Perf_Background_Dpkg.Pcitin.Flight_Plan := Active
5096 5106 Perf_Vdu_Dpkg.Vdu_Buffer.Buffer_Valid := False
5097 5107 Perf_Vdu_Dpkg.Vdu_Buffer.Prddataseq := 0
5098 5108 Perf_Background_Dpkg.Psprddataseq := 1
5099 5109 Perf_Vdu_Dpkg.Perf_Data_Save_Initiated := True
5100 5110 Prf_Vdu_Utils:body.Data_Save_In_Progress := False
5101 5111 CTP_PERF_BKGND_PUT_BK_DATA.Data := 1
5102 5112 Perf_Vdu_Dpkg.Data_Save := Perf_Vdu_Tpkg.None
5103 5113 CTP_PERF_BKGND_PUT_BK_DATA.Num := 99
5104 5114 CTP_PERF_BKGND_PUT_BK_DATA.Int_To_Str_Exec := "01"
5105 5115 #sba Prf_Vdu_Utils.Save_Leg_Data after_elab begin
5106 5116 CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec := True
5107 5117 #go
5108 5118 #end
5109 5119 #sba Prf_Vdu_Utils.Save_Pseudo_Data after_elab begin
5110 5120 CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec := True
5111 5121 #go
5112 5122 #end
5113 5123 #sba Prf_Vdu_Utils.Save_Vga_Data after_elab begin
5114 5124 CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec := True
5115 5125 #go
5116 5126 #end
5117 5127 #sba Prf_Vdu_Utils.Save_Altitude_Data after_elab begin
5118 5128 CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec := True
5119 5129 #go
5120 5130 #end
5121 5131 #delb/a
5122 5132 #Sba Prf_Vdu_Utils.Complete_Buffer before_end
5123 5133 #go
5124 5134 Perf_Vdu_Dpkg.Vdu_Buffer.Buffer_Valid = False
5125 5135 Perf_Vdu_Dpkg.Perf_Data_Save_Initiated = True
5126 5136 Perf_Vdu_Dpkg.Vdu_Buffer.Prddataseq = 0
5127 5137 CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec      = False
5128 5138 CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec    = False
5129 5139 CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec      = False
5130 5140 CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec = False
5131 5141 #delb/a
5132 5142 #sba Prf_Vdu_Utils.Initiate_Data_Save after_elab
5133 5143 #go
5134 5144 Perf_Vdu_Dpkg.Perf_Data_Save_Initiated := False

```



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

5135 5145 #delb/a
5136 5146
5137 5147 !run_test()
5138 5148
5139 5149 -- OUTPUTS
5140 5150 CTP_PERF_BKGND_PUT_BK_DATA.Int_To_Str_Exec = "99"
5141 5151 CTP_PERF_BKGND_PUT_BK_DATA.Get_Data_Save_State_Exec = True
5142 5152 Perf_Vdu_Dpkg.Data_Save = Perf_Vdu_Tpkg.Active
5143 5153 Perf_Vdu_Dpkg.Perf_Data_Save_Initiated = True
5144 5154
5145 5155
5146 5156 TESTID: 51
5147 5157 when a data save is initiated from the MRO page and it shall set the Perf_Data_Save_Initiated flag to true
5148 5158 to prevent a subsequent Data Save from being initiated while a Data Save is already in progress.
5149 5159 PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT)
5150 5160
5151 5161 If the data buffering process has started based on the user request than following shall be done
5152 5162 (PERF_SDD_07467_INT)
5153 5163 - Following procedure shall be called:
5154 5164 Prf_Vdu_Utils.Save_Leg_Data - To buffer flight plan data.
5155 5165 Prf_Vdu_Utils.Save_Pseudo_Data - To buffer psuedo waypoint data.
5156 5166 Prf_Vdu_Utils.Save_Vga_Data - To buffer vertical guidance array data.
5157 5167 Prf_Vdu_Utils.Save_Altitude_Data - To buffer important altitude values.
5158 5168 (PERF_SDD_07468_INT)
5159 5169 - After all the required data is buffered to VDU buffer the buffer validity shall be set to true
5160 5170 and buffer prediction data sequence counter is set to current guidance header sequence counter.
5161 5171 (PERF_SDD_07470_INT)
5162 5172 - Flag indicating VDU Buffer save has been initiated for this pass of preds and
5163 5173 the flag indicating the data buffering process has started shall be set to false
5164 5174 (PERF_SDD_07471_INT)
5165 5175 This function(Get_Data_Save_State) shall return the flag Perf_Data_Save_Initiated that is used to prevent a subsequent
5166 5176 » Data Save
5167 5176 from being initiated while a Data Save is already in progress. While the flag is true, a new data save cannot be initi
5168 5176 » ated.
5167 5177 PERF_SDD_07481(PERF_SRD_23173_INT)
5168 5178 This function(Int_To_Str) shall always return a string of two characters; the characters are always the digits.
5169 5179 The first digit of the string is the result of integer division of input number by 10.
5170 5180 The second digit of the string is the result of following equation: input number - first digit * 10.
5171 5181 PERF_SDD_07480_INT
5172 5182 Access to LGB is requested using the utility Perf_Lgb_Interface_Mgr_Pkg.Requestlgb and
5173 5183 first leg data in the flight plan shall be obtained using the utility Common_Lgb.Getlgbleg.
5174 5184 PERF_SDD_07473_INT
5175 5185 Flight plan data required to draw the trajectory shall be buffered to VDU buffer for the all the legs in the flight pl
5176 5185 » an.

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

5176 5186 PERF_SDD_07474_INT
5177 5187 The distance to destination data for the first leg shall be set to the current aircraft distance to destination only
5178 5188 if the current flight phase is preflight.
5179 5189 PERF_SDD_07475_INT
5180 5190 On completion of buffering of the data leg data access obtained to LGB shall be released by calling the utility
5181 5191 Perf_Lgb_Interface_Mgr_Pkg.Releaselgb and number of flight plan legs buffered into VDU buffer is updated.
5182 5192 PERF_SDD_07476_INT
5183 5193 Pseudo waypoint data shall be buffered to VDU buffer (Perf_Vdu_Dpkg.Vdu_Buffer.Pseudos) from
5184 5194 Perf background (Perf_Background_Dpkg.Pcperflgls).
5185 5195 PERF_SDD_07477_INT
5186 5196 Descent path data shall be buffered to VDU buffer (Perf_Vdu_Dpkg.Vdu_Buffer.Despath) from
5187 5197 Perf background (Perf_Despath_Dpkg.Pcdespath).
5188 5198 PERF_SDD_07479_INT
5189 5199 Following altitude value and validity shall be copied from background variables to VDU buffer:
5190 5200 - Cruise altitude.
5191 5201 - Maximum Certified altitude.
5192 5202 - Recommended Maximum altitude.
5193 5203 - Computed Optimum altitude.
5194 5204 - Clearance altitude.
5195 5205 - Tropopause altitude.
5196 5206 PERF_SDD_07472_INT
5197 5207
5198 5208 REQUIREMENTS UNDER EVALUATION : PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT), PERF_SDD_07481(PERF_SRD_23173_
5199 5209 » INT),
5200 5210 PERF_SDD_07467_INT, PERF_SDD_07468_INT, PERF_SDD_07470_INT, PERF_SDD_07471_INT, PERF_S
5201 5211 » DD_07480_INT,
5202 5212 PERF_SDD_07473_INT, PERF_SDD_07474_INT, PERF_SDD_07475_INT, PERF_SDD_07476_INT, PERF_S
5203 5213 » DD_07477_INT,
5204 5214 PERF_SDD_07479_INT, PERF_SDD_07472_INT
5205 5215
5206 5216 SUPPORTING REQUIREMENTS : N/A
5207 5217
5208 5218 -- INPUTS:
5209 5219 CTP_PERF_BKGND_PUT_BK_DATA.Data := 2
5210 5220 CTP_PERF_BKGND_PUT_BK_DATA.Num := 10
5211 5221 Perf_Background_Dpkg.Pcitin.Itinerary := Perf_Int_Base_Tpkg.Current_Mode_Preds
5212 5222 Prf_Vdu_Utils:body.Data_Save_In_Progress := True
5213 5223 Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.FixIdent := "aB19 fg"
5214 5224 Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Altaacstr := 123.00
5215 5225 Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Altabcstr := 12345.6
5216 5226 Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Cnstraintspd := 12345.6
5217 5227 Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Targetalt := 12345.6
5218 5228 Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Cstraltlim := 12345.6

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

5217	5227	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Spcspd	:= 12345.6
5218	5228	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Fpa	:= 1234.56
5219	5229	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.FpaVal	:= True
5220	5230	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.PathTerm	:= FA
5221	5231	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Clbordescstr	:= Descentseg
5222	5232	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Altaacstrval	:= True
5223	5233	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Altabcstrval	:= True
5224	5234	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Spcspdval	:= True
5225	5235	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Toosteppath	:= Tsptop
5226	5236	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Fixdistodest	:= 123456.00
5227	5237	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Incourse	:= 12300.0
5228	5238	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.ISADev	:= 1000.00
5229	5239	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.LegDistance	:= 1000.00
5230	5240	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Outcourse	:= 1000.00
5231	5241	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Prdairstpd	:= (1234.5,CAS)
5232	5242	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Prdalt	:= 123.00
5233	5243	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Prdetatofix	:= 12
5234	5244	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Prdgndspd	:= 1000.2
5235	5245	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Nextfpn	:= 2
5236	5246	Perf_Background_Dpkg.Pcfltphase	:= Preflight
5237	5247	Perf_Background_Dpkg.Psldistodest	:= 20006.00
5238	5248	Perf_Background_Dpkg.Pcfirstlegidx	:= 1
5239	5249	Perf_Background_Dpkg.Pcdestglidx	:= 2
5240	5250	Perf_Background_Dpkg.Psprddataseq	:= 1
5241	5251		
5242	5252	Perf_Background_Dpkg.Pscrzalt.Data	:= 10000.00
5243	5253	Perf_Background_Dpkg.Pscrzalt.Valid	:= True
5244	5254	Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	:= 50000.00
5245	5255	Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	:= True
5246	5256	Perf_Background_Dpkg.Maxalt.Maximum_Alt.Data	:= 55000.00
5247	5257	Perf_Background_Dpkg.Maxalt.Maximum_Alt.Valid	:= True
5248	5258	Perf_Background_Dpkg.Pcoptalt.Data	:= 1000.00
5249	5259	Perf_Background_Dpkg.Pcoptalt.Valid	:= True
5250	5260	Perf_Background_Dpkg.Pstropoalt	:= 20000.00
5251	5261	CTP_PERF_BKGND_PUT_BK_DATA.Clr.Data	:= 5000.00
5252	5262	CTP_PERF_BKGND_PUT_BK_DATA.Clr.Valid	:= True
5253	5263		
5254	5264	Perf_Background_Dpkg.Pcperflegs(33).Included	:= True
5255	5265	Perf_Background_Dpkg.Pcperflegs(33).Dist	:= 10002.0
5256	5266		
5257	5267	Perf_Despath_Dpkg.Pcdespath.vga(74).PACK.DISCON	:= True
5258	5268	Perf_Despath_Dpkg.Pcdespath.VGAINDXLAST	:= 74
5259	5269	Perf_Despath_Dpkg.Pcdespath.VGAVALID	:= True
5260	5270		

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

5261 5271 --INITIALIZATION:
5262 5272 Perf_Vdu_Dpkg.Data_Save := Perf_Vdu_Tpkg.None
5263 5273 CTP_PERF_BKGND_PUT_BK_DATA.Int_To_Str_Exec := "99"
5264 5274 CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec := False
5265 5275 CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec := False
5266 5276 CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec := False
5267 5277 CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec := False
5268 5278 CTP_PERF_BKGND_PUT_BK_DATA.Requestlgb_Exec := False
5269 5279 CTP_PERF_BKGND_PUT_BK_DATA.Releaselgb_Exec := FALSE
5270 5280 CTP_PERF_BKGND_PUT_BK_DATA.Getlgbleg_Exec := False
5271 5281 Perf_Vdu_Dpkg.Vdu_Buffer.Buffer_Valid := False
5272 5282 Perf_Vdu_Dpkg.Vdu_Buffer.Prddataseq := 0
5273 5283 Perf_Vdu_Dpkg.Perf_Data_Save_Initiated := True
5274 5284 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).FixIdent      := "gfedcba"
5275 5285 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Altaacstr     := 321.00
5276 5286 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Altabcstr    := 32145.6
5277 5287 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Cstrspdlim   := 32145.6
5278 5288 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Targetalt     := 32145.6
5279 5289 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Cstraltilim  := 32145.6
5280 5290 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Spccspd      := 32145.6
5281 5291 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).SpCFpa       := 3214.56
5282 5292 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).FpaVal       := False
5283 5293 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).PathTerm      := AF
5284 5294 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Clbordescstr  := CLIMBSEG
5285 5295 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Altaacstrval  := False
5286 5296 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Altabcstrval  := False
5287 5297 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Spccspdval   := False
5288 5298 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Toosteppath   := TSPNULL
5289 5299 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Fixdistodest := 321456.00
5290 5300 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Incouse     := 32100.0
5291 5301 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).ISADev      := 3000.00
5292 5302 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).LegDistance  := 3000.00
5293 5303 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Outcourse    := 3000.00
5294 5304 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Prdairspd    := (3214.5,Mach)
5295 5305 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Prdalt       := 321.00
5296 5306 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Prdtime     := 21
5297 5307 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Prdgndspd    := 3000.2
5298 5308 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Num_GLlegs := 123
5299 5309
5300 5310 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Crz.Data := 1.0
5301 5311 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Crz.Valid := False
5302 5312 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Max.Data := 11000.0
5303 5313 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Max.Valid := False
5304 5314 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Rec.Data := 12345.6

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

5305 5315 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Rec.Valid := False
5306 5316 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Opt.Data := 12345.6
5307 5317 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Opt.Valid := False
5308 5318 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Clr.Data := 65432.1
5309 5319 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Clr.Valid := False
5310 5320 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Tropo.Data := 11.11
5311 5321 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Tropo.Valid := False
5312 5322
5313 5323 Perf_Vdu_Dpkg.Vdu_Buffer.Pseudos(33).Included := False
5314 5324 Perf_Vdu_Dpkg.Vdu_Buffer.Pseudos(33).Dist := 20001.0
5315 5325
5316 5326 Perf_Vdu_Dpkg.Vdu_Buffer.Despath.vga(74).PACK.DISCON := False
5317 5327 Perf_Vdu_Dpkg.Vdu_Buffer.Despath.VGAINDXLAST := 37
5318 5328 Perf_Vdu_Dpkg.Vdu_Buffer.Despath.VGAVALID := False
5319 5329
5320 5330 #sba Prf_Vdu_Utils.Save_Leg_Data after_elab
5321 5331 #go
5322 5332 CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec := True
5323 5333 #sba Prf_Vdu_Utils.Save_Leg_Data #96
5324 5334 #go
5325 5335 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Fixdistodest = 123456.00
5326 5336 #sba Prf_Vdu_Utils.Save_Leg_Data before_end
5327 5337 #go
5328 5338 CTP_PERF_BKGND_PUT_BK_DATA.Requestlgb_Exec = True
5329 5339 CTP_PERF_BKGND_PUT_BK_DATA.Getlgbleg_Exec = True
5330 5340 CTP_PERF_BKGND_PUT_BK_DATA.Releaselgb_Exec = True
5331 5341 #DELB/A
5332 5342
5333 5343 #sba Prf_Vdu_Utils.Save_Pseudo_Data after_elab
5334 5344 #go
5335 5345 CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec := True
5336 5346 #sba Prf_Vdu_Utils.Save_Vga_Data after_elab
5337 5347 #go
5338 5348 CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec := True
5339 5349 #sba Prf_Vdu_Utils.Save_Altitude_Data after_elab
5340 5350 #go
5341 5351 CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec := True
5342 5352 #DELB/A
5343 5353
5344 5354 #sba Prf_Vdu_Utils.Save_Altitude_Data #45
5345 5355 #go
5346 5356 Common_Lgb:BODY.Header_Control.Clralt.Data := 5000.00
5347 5357 Common_Lgb:BODY.Header_Control.Clralt.Valid := True
5348 5358

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

5349 5359 #sba Prf_Vdu_Utils.Save_Altitude_Data before_end
5350 5360 #go
5351 5361 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Crz.Data      = 10000.00
5352 5362 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Crz.Valid      = True
5353 5363 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Max.Data       = 50000.00
5354 5364 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Max.Valid      = True
5355 5365 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Rec.Data       = 55000.00
5356 5366 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Rec.Valid      = True
5357 5367 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Opt.Data       = 1000.00
5358 5368 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Opt.Valid      = True
5359 5369 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Clr.Data       = 5000.00
5360 5370 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Clr.Valid      = True
5361 5371 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Tropo.Data     = 20000.00
5362 5372 Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Tropo.Valid    = True
5363 5373
5364 5374 #sba Prf_Vdu_Utils.Complete_Buffer before_end
5365 5375 #go
5366 5376 Perf_Vdu_Dpkg.Perf_Data_Save_Initiated = False
5367 5377 Prf_Vdu_Utils:body.Data_Save_In_Progress = False
5368 5378 Perf_Vdu_Dpkg.Vdu_Buffer.Buffer_Valid = True
5369 5379 Perf_Vdu_Dpkg.Vdu_Buffer.Prddataseq = 1
5370 5380 CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec = True
5371 5381 CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec = True
5372 5382 CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec = True
5373 5383 CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec = True
5374 5384
5375 5385 Perf_Vdu_Dpkg.Vdu_Buffer.Pseudos(33).Included = True
5376 5386 Perf_Vdu_Dpkg.Vdu_Buffer.Pseudos(33).Dist = 10002.0
5377 5387 Perf_Vdu_Dpkg.Vdu_Buffer.Despath.vga(74).PACK.DISCON = True
5378 5388 Perf_Vdu_Dpkg.Vdu_Buffer.Despath.VGAINDXLAST = 74
5379 5389 Perf_Vdu_Dpkg.Vdu_Buffer.Despath.VGAVALID = True
5380 5390
5381 5391 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).FixIdent      = "aB19 fg"
5382 5392 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Altaacstr     = 123.00
5383 5393 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Altabcstr     = 12345.6
5384 5394 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Cstrspdlim    = 12345.6
5385 5395 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Targetalt     = 12345.6
5386 5396 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Cstraltilim  = 12345.6
5387 5397 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Spccspd      = 12345.6
5388 5398 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).SpCFpa        = 1234.56
5389 5399 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).FpaVal       = True
5390 5400 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).PathTerm       = FA
5391 5401 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Clbordescstr  = Descentseg
5392 5402 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Altaacstrval    = True

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

5393 5403 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Altabcstrval = True
5394 5404 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Spdspdval = True
5395 5405 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Toosteeppath = Tsptop
5396 5406 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Fixdistodest = 20006.00
5397 5407 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Incource = 12300.0
5398 5408 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).ISADev = 1000.00
5399 5409 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).LegDistance = 1000.00
5400 5410 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Outcourse = 1000.00
5401 5411 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Prdairspd.VALUE = 1234.5
5402 5412 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Prdairspd.SPEED_TYPE = CAS
5403 5413 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Prdalt = 123.00
5404 5414 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Prdtime = 12
5405 5415 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Prdgndspd = 1000.2
5406 5416 Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Num_GLlegs = 2
5407 5417 #DELB/ALL
5408 5418
5409 5419 !run_test()
5410 5420 -- OUTPUTS
5411 5421 CTP_PERF_BKGND_PUT_BK_DATA.Int_To_Str_Exec = "10"
5412 5422 CTP_PERF_BKGND_PUT_BK_DATA.Get_Data_Save_State_Exec = False
5413 5423 Perf_Vdu_Dpkg.Data_Save = Perf_Vdu_Tpkg.Temporary
5414 5424 Perf_Vdu_Dpkg.Perf_Data_Save_Initiated = True
5415 5425
5416 5426
5417 5427
5418 5428
5419 5429
5420 5430 TESTID: 52
5421 5431 If the scratch flight plan is not being used, the predictions-output indication shall be set
5422 5432 according to Table 11.14-4.
5423 5433
5424 5434 Scratchfpln Change_Occurred Psperfregst Predictions_Output
5425 5435 FALSE TRUE TRUE FALSE
5426 5436 FALSE TRUE FALSE FALSE
5427 5437 FALSE FALSE TRUE FALSE
5428 5438 FALSE FALSE FALSE TRUE
5429 5439
5430 5440 PERF_SDD_4544_INT
5431 5441
5432 5442 Perf copy of CDA Enabled shall be initialized to OPC option
5433 5443 Options_And_Data_Pkg.CDA_Enable
5434 5444
5435 5445 PERF_SDD_09025
5436 5446 REQUIREMENTS UNDER EVALUATION : PERF_SDD_4544_INT, PERF_SDD_09025

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

```

5437 5447 SUPPORTING REQUIREMENTS : N/A
5438 5448
5439 5449
5440 5450 -- INPUTS:
5441 5451
5442 5452 Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds
5443 5453 Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2
5444 5454 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0
5445 5455 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0
5446 5456 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False
5447 5457 Perf_Background_Dpkg.Destination_Data.Efob.Data := 20.0
5448 5458 Perf_Background_Dpkg.Destination_Data.Efob.Valid := True
5449 5459 Perf_Background_Dpkg.Destination_Data.Ete.Data := 50.0
5450 5460 Perf_Background_Dpkg.Destination_Data.Ete.Valid := True
5451 5461 Perf_Background_Dpkg.Destination_Data.Firstpass := True
5452 5462 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Data := 0.0
5453 5463 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Valid := False
5454 5464 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Ete.Data := 0.0
5455 5465 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Ete.Valid := False
5456 5466 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Firstpass := False
5457 5467 Perf_Background_Dpkg.Pcfpln := Actprimary
5458 5468 Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := False
5459 5469 Perf_Background_Dpkg.Preds_Output(Active) := False
5460 5470 Perf_Dpkg.CDA_Enabled := true
5461 5471 Options_And_Data_Pkg:body.All_Options.Cda_Enable := false
5462 5472 #sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin
5463 5473 #Change := False
5464 5474 #go
5465 5475 #end
5466 5476
5467 5477
5468 5478
5469 5479 !run_test()
5470 5480
5471 5481 -- OUTPUTS
5472 5482 Perf_Background_Dpkg.Preds_Output(Active) = True
5473 5483 Perf_Dpkg.CDA_Enabled = false
5474 5484
5475 5485
5476 5486 TESTID: 53
5477 5487 If the scratch flight plan is not being used, the predictions-output indication shall be set
5478 5488 according to Table 11.14-4.
5479 5489
5480 5490 Scratchfpln      Change_Occurred  Psperrfreqst      Predictions_Output

```



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

5481	5491	FALSE	TRUE	TRUE	FALSE
5482	5492	FALSE	TRUE	FALSE	FALSE
5483	5493	FALSE	FALSE	TRUE	FALSE
5484	5494	FALSE	FALSE	FALSE	TRUE
5485	5495				
5486	5496	PERF_SDD_4544_INT			
5487	5497				
5488	5498	Perf copy of CDA Enabled shall be initialized to OPC option			
5489	5499	Options_And_Data_Pkg.CDA_Enable			
5490	5500				
5491	5501	PERF_SDD_09025			
5492	5502	REQUIREMENTS UNDER EVALUATION : PERF_SDD_4544_INT,PERF_SDD_09025			
5493	5503	SUPPORTING REQUIREMENTS : N/A			
5494	5504				
5495	5505				
5496	5506	-- INPUTS:			
5497	5507				
5498	5508	Perf_Background_Dpkg.Pcitin.Itinerary := Prim_Fpln_Preds			
5499	5509	Perf_Background_Dpkg.Pcgmtime.Gpc_Time := 2			
5500	5510	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt := 0			
5501	5511	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq := 0			
5502	5512	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid := False			
5503	5513	Perf_Background_Dpkg.Destination_Data.Efob.Data := 20.0			
5504	5514	Perf_Background_Dpkg.Destination_Data.Efob.Valid := True			
5505	5515	Perf_Background_Dpkg.Destination_Data.Ete.Data := 50.0			
5506	5516	Perf_Background_Dpkg.Destination_Data.Ete.Valid := True			
5507	5517	Perf_Background_Dpkg.Destination_Data.Firstpass := True			
5508	5518	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Data := 0.0			
5509	5519	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Valid := False			
5510	5520	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Ete.Data := 0.0			
5511	5521	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Ete.Valid := False			
5512	5522	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Firstpass := False			
5513	5523	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress := TRUE			
5514	5524	Perf_Background_Dpkg.Preds_Output(Active) := TRUE			
5515	5525	Perf_Background_Dpkg.Pcfpln := Actprimary			
5516	5526	Perf_Dpkg.CDA_Enabled := false			
5517	5527	Options_And_Data_Pkg:body.All_Options.Cda_Enable := true			
5518	5528	#define Verify_SDD_07059_Invalid := False			
5519	5529	#sba Sys_Change_Flags_Pkg.Change_Occurred After_elab begin			
5520	5530	#Change := False			
5521	5531	#go			
5522	5532	#end			
5523	5533	#delb/all			
5524	5534				

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.TDF (continued)

5525	5535	
5526	5536	!run_test()
5527	5537	
5528	5538	-- OUTPUTS
5529	5539	Perf_Background_Dpkg.Preds_Output(Active) = False
5530	5540	Perf_Dpkg.CDA_Enabled = true

Mode: All Lines

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.BAT

1	1	ECHO OFF
2	2	REM
3	3	REM BAT File
4	4	REM
5	5	REM CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.BAT
6	6	REM
7	7	REM CTP_A340S1A_PERF_BKGND_PUT_BK_DATA Started Execution
8	8	ECHO ON
9	9	ECHO Building Library
10	10	%build_lib% A340 %test% fm2
11	11	ECHO Compiling Drv
12	12	%acomp% CTP_A340S1A_PERF_BKGND_PUT_BK_DATA_D.ADA
13	13	ECHO Compiling Stb
14	14	%acomp% CTP_A340S1A_PERF_BND_PUT_BK_DAT_CDK_FUEL.STB
15	15	ECHO Compiling Stb
16	16	%acomp% CTP_A340S1A_PERF_BND_PUT_BK_DAT_COM_LGB.STB
17	17	ECHO Compiling Stb
18	18	%acomp% CTP_A340S1A_PERF_BND_PUT_BK_DAT_GTLGBHDR.STB
19	19	ECHO Compiling Stb
20	20	%acomp% CTP_A340S1A_PERF_BND_PUT_BK_DAT_GTLGBLEG.STB
21	21	ECHO Compiling Stb
22	22	%acomp% CTP_A340S1A_PERF_BND_PUT_BK_DAT_INTR_DKG.STB
23	23	ECHO Compiling Stb
24	24	%acomp% CTP_A340S1A_PERF_BND_PUT_BK_DAT_LGB_INTR.STB
25	25	ECHO Compiling Stb
26	26	%acomp% CTP_A340S1A_PERF_BND_PUT_BK_DAT_OPS_DELTA_TIME.STB
27	27	ECHO Compiling Stb
28	28	%acomp% CTP_A340S1A_PERF_BND_PUT_BK_DAT_PERF_BFR.STB
29	29	ECHO Compiling Stb
30	30	%acomp% CTP_A340S1A_PERF_BND_PUT_BK_DAT_PF_TO_CK.STB
31	31	ECHO Compiling Stb
32	32	%acomp% CTP_A340S1A_PERF_BND_PUT_BK_DAT_PTLGBLEG.STB
33	33	ECHO Compiling Stb
34	34	%acomp% CTP_A340S1A_PERF_BND_PUT_BK_DAT_SYS_PERF.STB
35	35	ECHO Compiling Stb
36	36	%ccomp% CTP_A340S1A_PERF_COMMON_OBJECTS.c
37	37	ECHO Linking
38	38	%alink% CTP_A340S1A_PERF_BKGND_PUT_BK_DATA_D
39	39	ECHO Running
40	40	%runtps% CTP_A340S1A_PERF_BKGND_PUT_BK_DATA Y
41	41	ECHO CTP_A340S1A_PERF_BKGND_PUT_BK_DATA Completed Execution

Mode: All Lines

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.CUL

1	1	##
2	2	## CUL FILE
3	3	##
4	4	## CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.CUL
5	5	##
6	6	PRF_BKGND_PKG.PUT_BK_DATA.PUT_BK_DATA
7	7	PRF_BKGND_PKG.INITIALIZE
8	8	PRF_BKGND_PKG.PUT_BK_DATA.OUTPUT_PREDS
9	9	PRF_MAXALT_DPKG.TRANSMIT_DUAL_DATA
10	10	PRF_MAXALT_DPKG.PUT_MAXIMUM_ALTITUDE_DATA
11	11	PRF_INT_UTILS.UPDATE_REFRESH_TIMER
12	12	PRF_INT_UTILS.DUAL_STATUS
13	13	PRF_INT_UTILS.ALIGN_SEGMENTS_AT_LEG
14	14	PRF_VDU_UTILS.INT_TO_STR
15	15	PRF_VDU_UTILS.GET_DATA_SAVE_STATE
16	16	PRF_VDU_UTILS.INITIALIZE_DATA_SAVE
17	17	PRF_VDU_UTILS.COMPLETE_BUFFER
18	18	PRF_VDU_UTILS.SAVE_ALTITUDE_DATA
19	19	PRF_VDU_UTILS.SAVE_LEG_DATA
20	20	PRF_VDU_UTILS.SAVE_PSEUDO_DATA
21	21	PRF_VDU_UTILS.SAVE_VGA_DATA
22	22	PRF_VDU_UTILS.UNBIAS_POINTS
23	23	

Mode: All Lines

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA\_D.ADA

```

1      1      --
2      2      --      A340 COMPONENT TEST DRIVER
3      3      --
4      4      --      COMPONENT : CTP_A340S1A_PERF_BKGND_PUT_BK_DATA_D.ADA
5      5      --
6      6      --*****
7      7      with Portable_Types_Pkg;
8      8      with Io_Interface_Tpkg;
9      9      with Apex_Partition_Pkg;
10     10     with Flight_Pln_Hdr_Types;
11     11     with Flight_Pln_Leg_Types;
12     12     with Perf_Buffer_Types;
13     13     with Hm_Pred_Tpkg;
14     14     with Optstep_Tpkg;
15     15     with Cdk_Fuel_Weight_Tpkg;
16     16     with Perf_Int_Base_Tpkg;
17     17     with Common_Lgb;
18     18     with Actnavrec_Types;
19     19     with Unchecked_Conversion;
20     20     with System;
21     21     use Actnavrec_Types;
22     22     with Perf_Ext_Tpkg;
23     23     with Destdata_Tpkg;
24     24
25     25     package CTP_PERF_BKGND_PUT_BK_DATA is
26     26
27     27     -- Global test variables go here
28     28     --
29     29
30     30     Get_Gb_Data_Exec : Boolean;
31     31     Get_Ky_Data_Exec : Boolean;
32     32     Get_Pb_Data_Exec : Boolean;
33     33     Boot_Status : Apex_Partition_Pkg.Operating_Mode_Type;
34     34     Fpln_Hdr_Arr : Common_Lgb.Flight_Plan_Headers_Arr;
35     35     Guidhdr : Flight_Pln_Hdr_Types.Flight_Pln_Hdr_Rec;
36     36     Guidhdrarray : Common_Lgb.Flight_Pln_Hdr_Access;
37     37     Gleg : Flight_Pln_Leg_Types.Leg_Rec;
38     38     Out_Gleg : Flight_Pln_Leg_Types.Leg_Rec;
39     39     Perfleg : Perf_Buffer_Types.Perflegrec;
40     40     Pshmpreddata : Hm_Pred_Tpkg.Hmpredtyp;
41     41     Pcoptalt : Io_Interface_Tpkg.Float_32_Valid.Normal;
42     42     Opt_Step_Data : Optstep_Tpkg.Optsteprec;

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA\_D.ADA (continued)

```

43      43      Pcaltnpreds_Exec : Boolean;
44      44      Pctriptime_Exec : Boolean;
45      45      Put_Final_Fuel_Exec : Boolean;
46      46      Put_Block_Fuel_Exec : Boolean;
47      47      Put_Hm_Preds_Exec : Boolean;
48      48      Put_Route_Reserve_Exec : Boolean;
49      49      Route_Reserve : Cdk_Fuel_Weight_Tpkg.Reserve_Record_T;
50      50      Chk_Idx : Flight_Pln_Leg_Types.Leg_Index_Type;
51      51      Leg_Ctr : Portable_Types_Pkg.Integer_32;
52      52      Du_Status : Perf_Int_Base_Tpkg.Dual_Status_Enum;
53      53      Putperflg : Boolean;
54      54      Fpln : Perf_Ext_Tpkg.Pred_Major_Fp_Type;
55      55
56      56      Save_Leg_Data_Exec : Boolean;
57      57      Save_Pseudo_Data_Exec : Boolean;
58      58      Save_Vga_Data_Exec : Boolean;
59      59      Save_Altitude_Data_Exec : Boolean;
60      60      Requestlgb_Exec : Boolean;
61      61      Releaselgb_Exec : Boolean;
62      62      Getlgbleg_Exec : Boolean;
63      63      Clr : Io_Interface_Tpkg.Float_32_Valid.Normal;
64      64      Get_Data_Save_State_Exec : Boolean;
65      65      Int_To_Str_Exec : String ( 1..2 );
66      66      Data: Portable_Types_Pkg.Unsigned_32;
67      67      Num : Portable_Types_Pkg.Integer_32;
68      68
69      69      Function To_Flight_Pln_Hdr_Access_Type is new Unchecked_Conversion(System.Address, Common_Lgb.Flight_Pln_Hdr_Access);
70      70
71      71 end   CTP_PERF_BKGND_PUT_BK_DATA;
72      72
73      73 with Flx_Semaphore_Pkg;
74      74 use Flx_Semaphore_Pkg;
75      75 with Prf_Bkgnd_Pkg;
76      76 with Prf_Int_Utils;
77      77 with Perf_Interface_Dpkg;
78      78 with Prf_Vdu_Utils;
79      79
80      80 with CTP_PERF_BKGND_PUT_BK_DATA;
81      81 use CTP_PERF_BKGND_PUT_BK_DATA;
82      82
83      83 procedure CTP_A340S1A_PERF_BKGND_PUT_BK_DATA_D is
84      84
85      85     begin
86      86

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA\_D.ADA (continued)

87	87	-- execute SUT
88	88	
89	89	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray := CTP_PERF_BKGND_PUT_BK_DATA.To_Flight_Pln_Hdr_Access_Type(Fpln_Hdr_Arr'addr » ess);
90	90	
91	91	prf_bkgnd_pkg.initialize(boot_status);
92	92	
93	93	prf_bkgnd_pkg.put_bk_data;
94	94	Du_Status := Prf_Int_Utils.Dual_Status;
95	95	Get_Data_Save_State_Exec := Prf_Vdu_Utils.Get_Data_Save_State;
96	96	Int_To_Str_Exec := Prf_Vdu_Utils.Int_To_Str(Num);
97	97	Prf_Vdu_Utils.Initiate_Data_Save(Data);
98	98	Prf_Vdu_Utils.Unbias_Points;
99	99	<<testend>> NULL;
100	100	end CTP_A340S1A_PERF_BKGND_PUT_BK_DATA_D ;

Mode: All Lines

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rpt

1	1	#####
2	2	#
3	3	#
4	4	#
5	5	#
6	6	#####
7	7	
8		<del>Tue Aug 26 11:16:44 China Standard Time 2014</del>
	8	Tue Oct 21 09:15:09 China Standard Time 2014
9	9	
10		<del>Test Coverage Analyzer (TCA) V6.16 CLASS A ps4082880-124</del>
11		<del>Win32 Host: WinNT 6.1 Build 7601 UserID: e821569 Node: CH71DT76F653X (Intel PentPro Model 23 Step 10)</del>
12		<del>Current Dir: C:\workspace\A340\ST2050\CTP_A340S1A_PERF_BKGND_PUT_BK_DATA\new</del>
	10	Test Coverage Analyzer (TCA) V6.15 CLASS A ps4082880-123
	11	Win32 Host: WinNT 6.1 Build 7601 UserID: E872867 Node: CH71DT15J7P02 (Intel PentPro Model 58 Step 9)
	12	Current Dir: C:\Workspace\A340\ST2050\CTP_A340S1A_PERF_BKGND_PUT_BK_DATA\new
13	13	
14	14	-----
15		<del>TCA invoked Tue Aug 26 11:16:39 China Standard Time 2014 with command line:</del>
	15	TCA invoked Tue Oct 21 09:15:02 China Standard Time 2014 with command line:
16	16	tca.exe -TABS -r CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.rpt -type 4 -p ...
17	17	CTP_A340S1A_PERF_BKGND_PUT_BK_DATA_d.pth -x ...
18	18	CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.xin -c ...
19	19	CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.cul
20	20	-----
21	21	Expanded command line:
22	22	tca.exe -TABS -r CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.rpt -type 4 -p ...
23	23	CTP_A340S1A_PERF_BKGND_PUT_BK_DATA_d.pth -x ...
24	24	CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.xin -c ...
25	25	CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.cul
26	26	-----
27	27	
28	28	
29	29	Test Coverage Type: 4
30	30	
31	31	Report File Name : CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.rpt
32	32	
33	33	Paths file(s) :
34	34	
35		<del>(P01) CTP_A340S1A_PERF_BKGND_PUT_BK_DATA_d.pth Tue Aug 26 11:11:32 2014</del>
	35	(P01) CTP_A340S1A_PERF_BKGND_PUT_BK_DATA_d.pth Tue Oct 21 09:10:32 2014
36	36	HADS-290x0 (PC/Windows NT) Ada Compiler, Version 2.9, PS4078711-104



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rpt (continued)

37	37	HADS-290x0 (PC/Windows NT) Ada Compiler, Version 2.9.61, PS4082845-107
38		<del>Post Object Paths Processor (POPP), v1.4, ps4082858-105</del>
	38	Post Object Paths Processor (POPP), v1.6, ps4082858-107
39	39	Honeywell 29K Assembler, v3.9, ps4082836-115
40	40	Honeywell 29K Assembler, V2.4, PS4072677-105
41	41	Post Object Paths Processor (POPP), v1.3, ps4082858-104
	42	Post Object Paths Processor (POPP), v1.4, ps4082858-105
42	43	HADS-290x0 (PC/Windows NT) Ada Linker, Version 2.9.61, PS4082846-109
43	44	
44	45	XInfo file(s) Test Date Test Platform:
45	46	
46	47	(P01) CTP_A340S1A_PERF_BKGND_PUT_BK_DATA_d.pth
47		<del>(X01) CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.xin Tue Aug 26 11:12:33 2014 ISS TCA Xinfo, Platform V7.02.04</del>
	48	(X01) CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.xin Tue Oct 21 09:12:03 2014 ISS TCA Xinfo, Platform V7.02.04
48	49	
49	50	-----
50	51	Compilation Test Coverage Statistics Warnings
51	52	Unit Name Total Decision Cond Statemnt Block Mixed Bool
52	53	-----
53	54	PRF_VDU_UTILS.UNBIAS_POINTS 100.0 n/a n/a 100.0 100.0 0 0
54	55	n/a n/a 2/2 8/8
55	56	
56	57	PRF_VDU_UTILS.INITIALIZE_DATA_SAVE 100.0 n/a n/a 100.0 100.0 0 0
57	58	n/a n/a 21/21 36/36
58	59	
59	60	PRF_VDU_UTILS.GET_DATA_SAVE_S -
60	61	TATE 100.0 n/a n/a 100.0 100.0 0 0
61	62	n/a n/a 1/1 3/3
62	63	
63	64	PRF_VDU_UTILS.INT_TO_STR 100.0 n/a n/a 100.0 100.0 0 0
64	65	n/a n/a 1/1 3/3
65	66	
66	67	PRF_VDU_UTILS.SAVE_ALTITUDE_DATA 100.0 n/a n/a 100.0 100.0 0 0
67	68	n/a n/a 15/15 8/8
68	69	
69	70	PRF_VDU_UTILS.SAVE_LEG_DATA 100.0 n/a n/a 100.0 100.0 0 0
70	71	n/a n/a 11/11 23/23
71	72	
72	73	PRF_VDU_UTILS.SAVE_PSEUDO_DATA 100.0 n/a n/a 100.0 100.0 0 0
73	74	n/a n/a 1/1 4/4
74	75	
75	76	PRF_VDU_UTILS.SAVE_VGA_DATA 100.0 n/a n/a 100.0 100.0 0 0
76	77	n/a n/a 1/1 5/5
77	78	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rpt (continued)

78	79	PRF_VDU_UTILS.COMPLETE_BUFFER	100.0	n/a	n/a	100.0	100.0	0	0
79	80			n/a	n/a	9/9	12/12		
80	81								
	82	PRF_MAXALT_DPKG.TRANSMIT_DUAL -							
	83	_DATA	100.0	n/a	n/a	100.0	100.0	0	0
	84			n/a	n/a	9/9	5/5		
	85								
	86	PRF_MAXALT_DPKG.PUT_MAXIMUM_A -							
	87	LTITUDE_DATA	100.0	n/a	n/a	100.0	100.0	0	0
	88			n/a	n/a	4/4	3/3		
	89								
81	90	PRF_INT_UTILS.UPDATE_REFRESH_ -							
82	91	TIMER	100.0	n/a	n/a	100.0	100.0	0	0
83	92			n/a	n/a	7/7	21/21		
84	93								
85	94	PRF_INT_UTILS.DUAL_STATUS	100.0	n/a	n/a	100.0	100.0	0	0
86	95			n/a	n/a	6/6	11/11		
87	96								
88	97	PRF_INT_UTILS.ALIGN_SEGMENTS_ -							
89	98	AT_LEG	100.0	n/a	n/a	100.0	100.0	0	0
90	99			n/a	n/a	1/1	2/2		
91	100								
92		<del>PRF_MAXALT_DPKG.TRANSMIT_DUAL</del>							
93		<del>_DATA</del>	<del>100.0</del>	<del>n/a</del>	<del>n/a</del>	<del>100.0</del>	<del>100.0</del>	<del>0</del>	<del>0</del>
94				<del>n/a</del>	<del>n/a</del>	<del>9/9</del>	<del>5/5</del>		
95									
96		<del>PRF_MAXALT_DPKG.PUT_MAXIMUM_A</del>							
97		<del>LTITUDE_DATA</del>	<del>100.0</del>	<del>n/a</del>	<del>n/a</del>	<del>100.0</del>	<del>100.0</del>	<del>0</del>	<del>0</del>
98				<del>n/a</del>	<del>n/a</del>	<del>4/4</del>	<del>3/3</del>		
99									
100	101	PRF_BKGND_PKG.INITIALIZE	100.0	n/a	n/a	100.0	100.0	0	0
101	102			n/a	n/a	28/28	29/29		
102	103								
103	104	PRF_BKGND_PKG.PUT_BK_DATA -							
104	105	.OUTPUT_PREDS	100.0	n/a	n/a	100.0	100.0	0	4
105	106			n/a	n/a	40/40	110/110		
106	107								
107	108	PRF_BKGND_PKG.PUT_BK_DATA -							
108	109	.PUT_BK_DATA	100.0	n/a	n/a	100.0	100.0	2	0
109				<del>n/a</del>	<del>n/a</del>	<del>114/114</del>	<del>278/278</del>		
	110			n/a	n/a	115/115	279/279		
110	111								
111	112	-----							
112	113	Total Percentages		n/a	n/a	100.0	100.0		

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rpt (continued)

113		<del>Totals</del>	<del>n/a</del>	<del>n/a</del>	<del>271/271</del>	<del>561/561</del>
	114	Totals	n/a	n/a	272/272	562/562
114	115	Total Coverage	100.0			
115	116	-----				
116	117	□				
117	118	*****				
118	119					
119		<del>Test Coverage Analyzer (TCA) Version 6.16 CLASS A</del>				
	120	Test Coverage Analyzer (TCA) Version 6.15 CLASS A				
120	121	*****				
121	122					
122	123					
123	124	Coverage Type: 4				
124	125					
125	126	Date of report / Report name :				
126	127					
127		<del>Tue Aug 26 11:16:44 2014 CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.rpt</del>				
	128	Tue Oct 21 09:15:09 2014 CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.rpt				
128	129					
129	130	Current Directory:				
130	131					
131		<del>C:\workspace\A340\ST2050\CTP_A340S1A_PERF_BKGND_PUT_BK_DATA\new</del>				
	132	C:\Workspace\A340\ST2050\CTP_A340S1A_PERF_BKGND_PUT_BK_DATA\new				
132	133					
133	134	Paths file(s) :				
134	135					
135		<del>(P01) CTP_A340S1A_PERF_BKGND_PUT_BK_DATA_d.pth Tue Aug 26 11:11:32 2014</del>				
	136	(P01) CTP_A340S1A_PERF_BKGND_PUT_BK_DATA_d.pth Tue Oct 21 09:10:32 2014				
136	137	HADS-290x0 (PC/Windows NT) Ada Compiler, Version 2.9, PS4078711-104				
137	138	HADS-290x0 (PC/Windows NT) Ada Compiler, Version 2.9.61, PS4082845-107				
138		<del>Post Object Paths Processor (POPP), v1.4, ps4082858-105</del>				
	139	Post Object Paths Processor (POPP), v1.6, ps4082858-107				
139	140	Honeywell 29K Assembler, v3.9, ps4082836-115				
140	141	Honeywell 29K Assembler, V2.4, PS4072677-105				
141	142	Post Object Paths Processor (POPP), v1.3, ps4082858-104				
	143	Post Object Paths Processor (POPP), v1.4, ps4082858-105				
142	144	HADS-290x0 (PC/Windows NT) Ada Linker, Version 2.9.61, PS4082846-109				
143	145					
144	146	XInfo file(s) Test Date Test Platform:				
145	147					
146	148	(P01) CTP_A340S1A_PERF_BKGND_PUT_BK_DATA_d.pth				
147		<del>(X01) CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.xin Tue Aug 26 11:12:33 2014 ISS TCA Xinfo, Platform V7.02.04</del>				
	149	(X01) CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.xin Tue Oct 21 09:12:03 2014 ISS TCA Xinfo, Platform V7.02.04				
148	150					

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rpt (continued)

149	151	Source file(s) :
150	152	
151		<del>A:\a340_Builds\st2050\src_st2050\FM\PRF_INT_UTILS_ALIGN_SEG_AT_LEG.ADA</del>
152		<del>A:\a340_Builds\st2050\src_st2050\FM\PRF_VDU_UTILS.ADA</del>
153		<del>A:\a340_Builds\st2050\src_st2050\FM\PRF_VDU_UTILS_COMPLETE_BUFFER.ADA</del>
154		<del>A:\a340_Builds\st2050\src_st2050\FM\PRF_VDU_UTILS_SAVE_ALTITUDE_DATA</del>
	153	A:\a340_Builds\st2099\src_st2099\FM\PRF_INT_UTILS_ALIGN_SEG_AT_LEG.ADA
	154	A:\a340_Builds\st2099\src_st2099\FM\PRF_VDU_UTILS.ADA
	155	A:\a340_Builds\st2099\src_st2099\FM\PRF_VDU_UTILS_COMPLETE_BUFFER.ADA
	156	A:\a340_Builds\st2099\src_st2099\FM\PRF_VDU_UTILS_SAVE_ALTITUDE_DATA -
155	157	.ADA
156		<del>A:\a340_Builds\st2050\src_st2050\FM\PRF_VDU_UTILS_SAVE_LEG_DATA.ADA</del>
157		<del>A:\a340_Builds\st2050\src_st2050\FM\PRF_VDU_UTILS_SAVE_PSEUDO_DATA.ADA</del>
158		<del>A:\a340_Builds\st2050\src_st2050\FM\PRF_VDU_UTILS_SAVE_VGA_DATA.ADA</del>
159		<del>A:\a340_Builds\st2050\src_st2050\FM\PRF_BKGND_PKG_INITIALIZE.ADA</del>
160		<del>A:\a340_Builds\st2050\src_st2050\FM\PRF_BKGND_PKG_PUT_BK_DATA.ADA</del>
161		<del>A:\a340_Builds\st2050\src_st2050\FM\PRF_INT_UTILS.ADA</del>
162		<del>A:\a340_Builds\st2050\src_st2050\FM\PRF_MAXALT_DPKG.ADA</del>
	158	A:\a340_Builds\st2099\src_st2099\FM\PRF_VDU_UTILS_SAVE_LEG_DATA.ADA
	159	A:\a340_Builds\st2099\src_st2099\FM\PRF_VDU_UTILS_SAVE_PSEUDO_DATA.ADA
	160	A:\a340_Builds\st2099\src_st2099\FM\PRF_VDU_UTILS_SAVE_VGA_DATA.ADA
	161	A:\a340_Builds\st2099\src_st2099\FM\PRF_BKGND_PKG_INITIALIZE.ADA
	162	A:\a340_Builds\st2099\src_st2099\FM\PRF_BKGND_PKG_PUT_BK_DATA.ADA
	163	A:\a340_Builds\st2099\src_st2099\FM\PRF_INT_UTILS.ADA
	164	A:\a340_Builds\st2099\src_st2099\FM\PRF_MAXALT_DPKG.ADA
163	165	Total Coverage statistics :
164	166	
165	167	
166	168	TYPE 4, 100.0%
167	169	
168	170	
169	171	*****
170	172	Source Report Legend Key
171	173	(Legend Key may be suppressed by -k option)
172	174	
173	175	Coverage messages preceding source code lines are annotated with
174	176	object code block tags of the form [x-y BLOCKTYPE]. For example,
175	177	[263-17 JMPT] is a block tag for the 17th block of the 263rd unit
176	178	in the pathfile and is a jump true block.
177	179	This block tag annotation is intended to be used as a reference to
178	180	the object code level block report (.tcb) generated with the -B option.
179	181	Each object code block is labeled with a unique block tag.
180	182	
181	183	Each line of source code may be prefixed by one of the following

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rpt (continued)

182	184	indicators:
183	185	. = source line completely or partially executed
184	186	* = source line shown ONLY to clarify previous source lines and
185	187	is NOT actually part of the uncovered source TCA is reporting on
186	188	Note that no prefix indicates source line was not executed
187	189	
188	190	
189	191	*****
190	192	
191	193	Compilation Unit / Source file :
192	194	
193	195	PRF_VDU_UTILS.UNBIAS_POINTS
194		<del>C:\A340\Builds\ST2050\SRC_ST2050\FM\PRF_VDU_UTILS.ADA</del>
	196	C:\A340\Builds\ST2099\SRC_ST2099\FM\PRF_VDU_UTILS.ADA
195	197	
196	198	Coverage statistics :
197	199	
198	200	TYPE 4, 100.0%
199	201	
200	202	Executed Total
201	203	Statements 2 2
202	204	Blocks 8 8
203	205	
204	206	
205	207	
206	208	*****
207	209	
208	210	Compilation Unit / Source file :
209	211	
210	212	PRF_VDU_UTILS.INITIALIZE_DATA_SAVE
211		<del>C:\A340\Builds\ST2050\SRC_ST2050\FM\PRF_VDU_UTILS.ADA</del>
	213	C:\A340\Builds\ST2099\SRC_ST2099\FM\PRF_VDU_UTILS.ADA
212	214	
213	215	Coverage statistics :
214	216	
215	217	TYPE 4, 100.0%
216	218	
217	219	Executed Total
218	220	Statements 21 21
219	221	Blocks 36 36
220	222	
221	223	
222	224	
223	225	*****

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rpt (continued)

224	226	
225	227	Compilation Unit / Source file :
226	228	
227	229	PRF_VDU_UTILS.GET_DATA_SAVE_STATE
228		<del>C:\A340\Builds\ST2050\SRC_ST2050\FM\PRF_VDU_UTILS.ADA</del>
	230	C:\A340\Builds\ST2099\SRC_ST2099\FM\PRF_VDU_UTILS.ADA
229	231	
230	232	Coverage statistics :
231	233	
232	234	TYPE 4, 100.0%
233	235	
234	236	Executed Total
235	237	Statements 1 1
236	238	Blocks 3 3
237	239	
238	240	
239	241	
240	242	*****
241	243	
242	244	Compilation Unit / Source file :
243	245	
244	246	PRF_VDU_UTILS.INT_TO_STR
245		<del>C:\A340\Builds\ST2050\SRC_ST2050\FM\PRF_VDU_UTILS.ADA</del>
	247	C:\A340\Builds\ST2099\SRC_ST2099\FM\PRF_VDU_UTILS.ADA
246	248	
247	249	Coverage statistics :
248	250	
249	251	TYPE 4, 100.0%
250	252	
251	253	Executed Total
252	254	Statements 1 1
253	255	Blocks 3 3
254	256	
255	257	
256	258	
257	259	*****
258	260	
259	261	Compilation Unit / Source file :
260	262	
261	263	PRF_VDU_UTILS.SAVE_ALTITUDE_DATA
262		<del>C:\A340\Builds\ST2050\SRC_ST2050\FM\PRF_VDU_UTILS_SAVE_ALTITUDE_DATA</del>
	264	C:\A340\Builds\ST2099\SRC_ST2099\FM\PRF_VDU_UTILS_SAVE_ALTITUDE_DATA -
263	265	.ADA
264	266	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rpt (continued)

265	267	Coverage statistics :
266	268	
267	269	TYPE 4, 100.0%
268	270	
269	271	Executed Total
270	272	Statements 15 15
271	273	Blocks 8 8
272	274	
273	275	
274	276	
275	277	*****
276	278	
277	279	Compilation Unit / Source file :
278	280	
279	281	PRF_VDU_UTILS.SAVE_LEG_DATA
280		<del>C:\A340\Builds\ST2050\SRC_ST2050\FM\PRF_VDU_UTILS_SAVE_LEG_DATA.ADA</del>
	282	C:\A340\Builds\ST2099\SRC_ST2099\FM\PRF_VDU_UTILS_SAVE_LEG_DATA.ADA
281	283	
282	284	Coverage statistics :
283	285	
284	286	TYPE 4, 100.0%
285	287	
286	288	Executed Total
287	289	Statements 11 11
288	290	Blocks 23 23
289	291	
290	292	
291	293	
292	294	*****
293	295	
294	296	Compilation Unit / Source file :
295	297	
296	298	PRF_VDU_UTILS.SAVE_PSEUDO_DATA
297		<del>C:\A340\Builds\ST2050\SRC_ST2050\FM\PRF_VDU_UTILS_SAVE_PSEUDO_DATA.ADA</del>
	299	C:\A340\Builds\ST2099\SRC_ST2099\FM\PRF_VDU_UTILS_SAVE_PSEUDO_DATA.ADA
298	300	
299	301	Coverage statistics :
300	302	
301	303	TYPE 4, 100.0%
302	304	
303	305	Executed Total
304	306	Statements 1 1
305	307	Blocks 4 4
306	308	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rpt (continued)

307	309	
308	310	
309	311	*****
310	312	
311	313	Compilation Unit / Source file :
312	314	
313	315	PRF_VDU_UTILS.SAVE_VGA_DATA
314		<del>C:\A340\Builds\ST2050\SRC_ST2050\FM\PRF_VDU_UTILS_SAVE_VGA_DATA.ADA</del>
	316	C:\A340\Builds\ST2099\SRC_ST2099\FM\PRF_VDU_UTILS_SAVE_VGA_DATA.ADA
315	317	
316	318	Coverage statistics :
317	319	
318	320	TYPE 4, 100.0%
319	321	
320	322	Executed Total
321	323	Statements 1 1
322	324	Blocks 5 5
323	325	
324	326	
325	327	
326	328	*****
327	329	
328	330	Compilation Unit / Source file :
329	331	
330	332	PRF_VDU_UTILS.COMPLETE_BUFFER
331		<del>C:\A340\Builds\ST2050\SRC_ST2050\FM\PRF_VDU_UTILS_COMPLETE_BUFFER.ADA</del>
	333	C:\A340\Builds\ST2099\SRC_ST2099\FM\PRF_VDU_UTILS_COMPLETE_BUFFER.ADA
332	334	
333	335	Coverage statistics :
334	336	
335	337	TYPE 4, 100.0%
336	338	
337	339	Executed Total
338	340	Statements 9 9
339	341	Blocks 12 12
340	342	
341	343	
342	344	
343	345	*****
344	346	
345	347	Compilation Unit / Source file :
346	348	
347		<del>PRF_INT_UTILS.UPDATE_REFRESH_TIMER</del>
348		<del>C:\A340\Builds\ST2050\SRC_ST2050\FM\PRF_INT_UTILS.ADA</del>



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rpt (continued)

349	
350	Coverage statistics :
351	
352	----- TYPE 4, 100.0%
353	
354	----- Executed ----- Total
355	----- Statements ----- 7 ----- 7
356	----- Blocks ----- 21 ----- 21
357	
358	
359	
360	*****
361	
362	Compilation Unit / Source file :
363	
364	----- PRF_INT_UTILS.DUAL_STATUS
365	----- C:\A340\Builds\ST2050\SRC_ST2050\fm\PRF_INT_UTILS.ADA
366	
367	Coverage statistics :
368	
369	----- TYPE 4, 100.0%
370	
371	----- Executed ----- Total
372	----- Statements ----- 6 ----- 6
373	----- Blocks ----- 11 ----- 11
374	
375	
376	
377	*****
378	
379	Compilation Unit / Source file :
380	
381	----- PRF_INT_UTILS.ALIGN_SEGMENTS_AT_LEG
382	----- C:\A340\Builds\ST2050\SRC_ST2050\FM\PRF_INT_UTILS_ALIGN_SEG_AT_LEG.ADA
383	
384	Coverage statistics :
385	
386	----- TYPE 4, 100.0%
387	
388	----- Executed ----- Total
389	----- Statements ----- 1 ----- 1
390	----- Blocks ----- 2 ----- 2
391	
392	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rpt (continued)

393		
394		*****
395		
396		Compilation Unit / Source file :
397		
398	349	PRF_MAXALT_DPKG.TRANSMIT_DUAL_DATA
399		<del>C:\A340\Builds\ST2050\SRC_ST2050\fm\PRF_MAXALT_DPKG.ADA</del>
	350	C:\A340\Builds\ST2099\SRC_ST2099\fm\PRF_MAXALT_DPKG.ADA
400	351	
401	352	Coverage statistics :
402	353	
403	354	TYPE 4, 100.0%
404	355	
405	356	Executed Total
406	357	Statements 9 9
407	358	Blocks 5 5
408	359	
409	360	
410	361	
411	362	*****
412	363	
413	364	Compilation Unit / Source file :
414	365	
415	366	PRF_MAXALT_DPKG.PUT_MAXIMUM_ALTITUDE_DATA
416		<del>C:\A340\Builds\ST2050\SRC_ST2050\fm\PRF_MAXALT_DPKG.ADA</del>
	367	C:\A340\Builds\ST2099\SRC_ST2099\fm\PRF_MAXALT_DPKG.ADA
417	368	
418	369	Coverage statistics :
419	370	
420	371	TYPE 4, 100.0%
421	372	
422	373	Executed Total
423	374	Statements 4 4
424	375	Blocks 3 3
425	376	
426	377	
427	378	
428	379	*****
429	380	
430	381	Compilation Unit / Source file :
431	382	
	383	PRF_INT_UTILS.UPDATE_REFRESH_TIMER
	384	C:\A340\Builds\ST2099\SRC_ST2099\fm\PRF_INT_UTILS.ADA
	385	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rpt (continued)

```
386 Coverage statistics :
387
388     TYPE 4, 100.0%
389
390           Executed      Total
391   Statements          7          7
392   Blocks              21          21
393
394
395
396 *****
397
398 Compilation Unit / Source file :
399
400     PRF_INT_UTILS.DUAL_STATUS
401     C:\A340\Builds\ST2099\SRC_ST2099\fm\PRF_INT_UTILS.ADA
402
403 Coverage statistics :
404
405     TYPE 4, 100.0%
406
407           Executed      Total
408   Statements          6          6
409   Blocks              11          11
410
411
412
413 *****
414
415 Compilation Unit / Source file :
416
417     PRF_INT_UTILS.ALIGN_SEGMENTS_AT_LEG
418     C:\A340\Builds\ST2099\SRC_ST2099\FM\PRF_INT_UTILS_ALIGN_SEG_AT_LEG.ADA
419
420 Coverage statistics :
421
422     TYPE 4, 100.0%
423
424           Executed      Total
425   Statements          1          1
426   Blocks              2          2
427
428
429
```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rpt (continued)

	430	*****
	431	
	432	Compilation Unit / Source file :
	433	
432	434	PRF_BKGND_PKG.INITIALIZE
433		<del>C:\A340\Builds\ST2050\SRC_ST2050\fm\PRF_BKGND_PKG_INITIALIZE.ADA</del>
	435	C:\A340\Builds\ST2099\SRC_ST2099\fm\PRF_BKGND_PKG_INITIALIZE.ADA
434	436	
435	437	Coverage statistics :
436	438	
437	439	TYPE 4, 100.0%
438	440	
439	441	Executed Total
440	442	Statements 28 28
441	443	Blocks 29 29
442	444	
443	445	
444	446	
445	447	*****
446	448	
447	449	Compilation Unit / Source file :
448	450	
449	451	PRF_BKGND_PKG.PUT_BK_DATA.OUTPUT_PREDS
450		<del>C:\A340\Builds\ST2050\SRC_ST2050\fm\PRF_BKGND_PKG_PUT_BK_DATA.ADA</del>
	452	C:\A340\Builds\ST2099\SRC_ST2099\fm\PRF_BKGND_PKG_PUT_BK_DATA.ADA
451	453	
452	454	Coverage statistics :
453	455	
454	456	TYPE 4, 100.0%
455	457	
456	458	Executed Total
457	459	Statements 40 40
458	460	Blocks 110 110
459	461	
460	462	
461	463	
462	464	*****
463	465	
464	466	Compilation Unit / Source file :
465	467	
466	468	PRF_BKGND_PKG.PUT_BK_DATA.PUT_BK_DATA
467		<del>C:\A340\Builds\ST2050\SRC_ST2050\fm\PRF_BKGND_PKG_PUT_BK_DATA.ADA</del>
	469	C:\A340\Builds\ST2099\SRC_ST2099\fm\PRF_BKGND_PKG_PUT_BK_DATA.ADA
468	470	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rpt (continued)

469	471	Coverage statistics :		
470	472			
471	473	TYPE 4, 100.0%		
472	474			
473	475		Executed	Total
474		<del>Statements</del>	<del>114</del>	<del>114</del>
475		<del>Blocks</del>	<del>278</del>	<del>278</del>
	476	Statements	115	115
	477	Blocks	279	279
476	478			
477	479			
478	480			
479	481	***** End of Report *****		

Mode: All Lines

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst

1	1				
2	2				
3	3	RESULTS FILE			
4	4				
5	5	*****			
6	6	Test Results Summary			
7	7				
8		<del>Percentage of Comparisons Passed : 99.2920%</del>			
	8	Percentage of Comparisons Passed : 100.0000%			
9	9				
10		<del>Total Number of Comparisons Failed : 4</del>			
	10	Total Number of Comparisons Failed : 0			
11	11	Total Number of Unknown Comparisons : 0			
12		<del>Total Number of Comparisons Passed : 561</del>			
13		<del>Total Number of Comparisons : 565</del>			
	12	Total Number of Comparisons Passed : 566			
	13	Total Number of Comparisons : 566			
14	14	Total Number of Test Cases Included : 53			
15	15				
16	16	Test Complete			
17	17				
18	18				
19	19				
20	20	*****			
21	21				
22	22				
23		<del>Test Start Time: Aug 26 11:12:43 2014</del>			
	23	Test Start Time: Oct 21 09:12:09 2014			
24	24				
25	25	FILE : CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.TDF			
26	26				
27	27	SOURCE CONFIGURATION : ISS (Instruction Set Simulator)			
28	28				
29	29	DESCRIPTION : CTP_A340S1A_PERF_BKGND_PUT_BK_DATA Test			
30	30				
31	31	MODIFICATION HISTORY :			
32	32				
33	33	DATE	SCR #	AUTHOR	DESCRIPTION
34	34	=====	=====	=====	=====
35	35				
36	36	Aug 18, 2010	52527.78	Zhihong Zhai	Initial Development for A340 S1A S1 plan.
37	37				1. Rollover from A320 S1A

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

38	38	» ZIP;21).				CTP_A320_PERF_BKGND_PUT_BK_DATA(TDF;20,
39	39					2. Updated following SRD/SDD generations:
40	40	» 58				11_3_3.SRD ; 47 -->
41	41	» 19				11_13.SRD ; 18 -->
42	42	» 28				11_7.SRD ; 27 -->
43	43	» 16				11_14_3.SRD ; 14 -->
44	44	» 25				11_14_4.SRD ; 24 -->
45	45	» 84				11_2_1_1.SRD ; 75 -->
46	46	» 17				11_2_9.SRD ; 14 -->
47	47	» 71				11_2_1_1_7.SRD ; 64 -->
48	48	» 20				11_2_1_12.SRD ; 18 -->
49	49	» 53				11_5_2.SRD ; 41 -->
50	50	» 176				11_1.SRD ; 157 -->
51	51	» 326				PERF_BACKGROUND_EXEC.SDD ; 280 -->
52	52	» 116				PERF_OBJECT_MAN.SDD ; 107 -->
53	53	» 104				PERF_UTILITIES.SDD ; 86 -->
54	54	» 28				PERF_MAXIMUM_ALT.SDD ; 27 -->
55	55	» XX)				3. Updated as per SCR 49154.01(FMS2000, A3
56	56					a). Updated TC 12-14, 17, 35-38 for SDD
57	57					PERF_SDD_3155_INT.
58	58					
59	59	» 1A120 for A340	Jul 9,2013	55836.04	Chen Jixing	Update as per A340_55677_04.DRAT on build S
60	60					Peg 2
61	61					1. Update SRd/SDD generations:
62	62	» 31				PERF_BACKGROUND_EXEC.SDD ; 326 --> 3

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

63	63				11_5_2.SRD;	53 --> 6
64	64	» 4			2. Updated as per SCR 55677.06(FMS2000, A3	
65	65	» XX)			a). Update TC 1, 28, 29 as remove ancho	
66	66	» r			PERF_SDD_07059(PERF_SRD_12280, PERF_	
67	67	» SRD_12372_INT)			b). Update TC 21, 48, 49, 53, Del TC 52	
68	68	» as remove of			anchor PERF_SDD_07063(PERF_SRD_12280	
69	69	» ),update TC 54			as delete of vars, update subsequent	
70	70	» TC id			after TC 52(here TC id refer to orig	
71	71	» inal ID)			c). Delete vars to remove test discrepan	
72	72	» cy			d). Update breakpoint line number to rem	
73	73	» ove test discrepancy				
74	74		Aug 26,2014	57231.93	Dun Qing	Update for A340 step2 CR1 on build ST2050.
75	75					1. Update SRd/SDD generations:
76	76					PERF_BACKGROUND_EXEC.SDD ; 331 --> 3
77	77	» 50				
78	78				PERF_OBJECT_MAN.SDD; 116 -> 128	
79	79				PERF_UTILITIES.SDD; 104 -> 117	
80	80				11_3_3.SRD; 58 -> 68	
81	81				11_7.SRD; 28 -> 32	
82	82				11_2_1_1.SRD; 84 -> 88	
83	83				11_2_1_1_7.SRD; 71 -> 78	
84	84				11_2_1_12.SRD; 20 -> 21	
85	85				11_1.SRD; 176 -> 183	
86	86				Deleted SRd/SDD files:	
87	87				11_5_2.SRD;	
88	88				11_2_9.SRD;	
89	89				2. Updated the breakpoints.	
90	90	» 025 as per SCR 55961.36(FMS2000, A3XX)			3. Updated TCs 52,53 to verify PERF_SDD_09	
91	91	» d of "Perf_Buffer_Types.Perf_Leg_Type"			4. Updated TCs 2,10,13,14,17 as the change	
	92		Oct 11,2014	58370.01	Gawain Jin	Updated for A340 STEP2 CR2 on build ST2099
		» .				



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

93	93	1.Updated the SDD/SRD generation as follow	
	94	» ing:	
	95		11_3_3.SRD ; 68-->73
	96		11_2_1_1_7.SRD ; 78-->85
	97		PERF_BACKGROUND_EXEC.SDD ; 350-->371
	98		PERF_OBJECT_MAN.SDD ; 128-->133
	99		PERF_UTILITIES.SDD ; 117-->126
	100		2.Updated breakpoints as build changed.
	101	» X).	3.Updated as per SCR 49180.00(FMS2000, A3X
	102	» INT completely.	a.Updated TC 35 to verify PERF_SDD_3155_
92	102		
93		<del>SRD/SDD DETAILS : 11_3_3.SRD ; 68</del>	
	103		
	104	SRD/SDD DETAILS : 11_3_3.SRD ; 73	
94	105	11_13.SRD ; 19	
95	106	11_7.SRD ; 32	
96	107	11_14_3.SRD ; 16	
97	108	11_14_4.SRD ; 25	
98	109	11_2_1_1.SRD ; 88	
99		<del>11_2_1_1_7.SRD ; 78</del>	
	110	11_2_1_1_7.SRD ; 85	
100	111	11_2_1_12.SRD ; 21	
101	112	11_1.SRD ; 183	
102		<del>PERF_BACKGROUND_EXEC.SDD ; 350</del>	
103		<del>PERF_OBJECT_MAN.SDD ; 128</del>	
104		<del>PERF_UTILITIES.SDD ; 117</del>	
	113	PERF_BACKGROUND_EXEC.SDD ; 371	
	114	PERF_OBJECT_MAN.SDD ; 133	
	115	PERF_UTILITIES.SDD ; 126	
105	116	PERF_MAXIMUM_ALT.SDD ; 28	
106	117	PERF_VDU_UTILS.SDD ; 4	
107	118		
108	119	TRACE DETAILS :	
109	120	ANCHOR : A340_PERF_TEST_2443	
110	121	SOURCE : SDD; PERF_SDD_0421, PERF_SDD_07154,	
111	122	PERF_SDD_07394_INT, PERF_SDD_07467_INT, PERF_SDD_07468_INT, PERF_SDD_07469_INT,	
112	123	PERF_SDD_07470_INT, PERF_SDD_07471_INT, PERF_SDD_07472_INT, PERF_SDD_07473_INT,	
113	124	PERF_SDD_07474_INT, PERF_SDD_07475_INT, PERF_SDD_07476_INT, PERF_SDD_07477_INT,	
114	125	PERF_SDD_07479_INT, PERF_SDD_07480_INT, PERF_SDD_07481, PERF_SDD_07482,	
115	126	PERF_SDD_07527, PERF_SDD_1826, PERF_SDD_1831, PERF_SDD_2094_INT,	
116	127	PERF_SDD_2095_INT, PERF_SDD_2096, PERF_SDD_2109_INT, PERF_SDD_2113_INT,	
117	128	PERF_SDD_2158_INT, PERF_SDD_2159_INT, PERF_SDD_2289, PERF_SDD_2407_INT,	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

118 129 PERF_SDD_2414_INT, PERF_SDD_2417_INT, PERF_SDD_2436, PERF_SDD_2631_INT,
119 130 PERF_SDD_2632_INT, PERF_SDD_3027, PERF_SDD_3052_INT, PERF_SDD_3106_INT,
120 131 PERF_SDD_3107_INT, PERF_SDD_3155_INT, PERF_SDD_3392_INT, PERF_SDD_3393_INT,
121 132 PERF_SDD_3500_INT, PERF_SDD_3501_INT, PERF_SDD_3511_INT, PERF_SDD_3515_INT,
122 133 PERF_SDD_3516_INT, PERF_SDD_3517_INT, PERF_SDD_3518_INT, PERF_SDD_3519_INT,
123 134 PERF_SDD_3520_INT, PERF_SDD_3523_INT, PERF_SDD_3680_INT, PERF_SDD_3739_INT,
124 135 PERF_SDD_3752_INT, PERF_SDD_3968_INT, PERF_SDD_4220_INT, PERF_SDD_4543_INT,
125 136 PERF_SDD_4544_INT, PERF_SDD_5587_INT, PERF_SDD_5614_DR, PERF_SDD_5617_INT,
126 137 PERF_SDD_7018, PERF_SDD_09025
127 138
128 139 SRD; PERF_SRD_10167_INT, PERF_SRD_10253, PERF_SRD_10333_INT, PERF_SRD_10869,
129 140 PERF_SRD_12092, PERF_SRD_12093, PERF_SRD_12094, PERF_SRD_12095,
130 141 PERF_SRD_1544_A3XX, PERF_SRD_2020,
131 142 PERF_SRD_2045, PERF_SRD_2051, PERF_SRD_2071, PERF_SRD_2087_INT,
132 143 PERF_SRD_23172_INT, PERF_SRD_23173_INT, PERF_SRD_7463, PERF_SRD_9993,
133 144 PERF_SRD_9994
134 145
135 146 -- -- -- BEGIN PROCESSING INCLUDE FILE C:\Program Files\honeywell_eng\TGS_v4_5_2\bin\debug_cmds.inc
136 147 -- -- -- END PROCESSING INCLUDE FILE C:\Program Files\honeywell_eng\TGS_v4_5_2\bin\debug_cmds.inc
137 148 -- -- -- *****
138 149 -- -- -- INITIALIZATION SECTION
139 150 -- -- -- *****
140 151
141 152
142 153 CONSTANT VALUE
143 154 -----
144 155 » -----
145 156 FP_DEF_TOL
146 157 » 0.001
147 158
148 159 define symbol True := Standard.True
149 160 define symbol False := Standard.False
150 161 define symbol Active := Fprequestrec_Types.Active
151 162 define symbol Actprimary := Airbus_Lgbm.Actprimary
152 163 define symbol Secprimary := Airbus_Lgbm.Secprimary
153 164 define symbol Scratchfpln := Airbus_Lgbm.Scratchfpln
154 165 define symbol Secondary := Fprequestrec_Types.Secondary
155 166 define symbol Cold_Start := Apex_Partition_Pkg.Cold_Start
156 167 define symbol Warm_Start := Apex_Partition_Pkg.Warm_Start
157 168 define symbol Prim_Fpln_Preds := Perf_Int_Base_Tpkg.Prim_Fpln_Preds
158 169 define symbol Optalt := Perf_Int_Base_Tpkg.Optimum_altitude
159 170 define symbol Maxalt := Perf_Int_Base_Tpkg.Maximum_Altitude
170 171 define symbol Holdactv := Perf_Int_Base_Tpkg.Manual_Hold_Preds

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

160	171	define symbol Fuelpredact	:= Perf_Int_Base_Tpkg.Fuel_Preds	
161	172	define symbol Fuelplanact2	:= Perf_Int_Base_Tpkg.Fuel_Plan_Stage2	
162	173	define symbol Optimum_step	:= Perf_Int_Base_Tpkg.Optimum_step	
163	174	define symbol Goaround	:= Perf_Int_Base_Tpkg.Go_Around_Preds	
164	175	define symbol Time_Constraint_Eval	:= Perf_Int_Base_Tpkg.Time_Constraint_Eval	
165	176	define symbol Climb	:= Base_Domain_Services_Tpkg.Climb	
166	177	define symbol Cruise	:= Base_Domain_Services_Tpkg.Cruise	
167	178	define symbol Preflight	:= Base_Domain_Services_Tpkg.Preflight	
168	179	define symbol Descent	:= Base_Domain_Services_Tpkg.Descent	
169	180	define symbol Single	:= Base_Domain_Services_Tpkg.Single	
170	181	define symbol Dual	:= Base_Domain_Services_Tpkg.Dual	
171	182	define symbol Firstleg	:= Flight_Pln_Hdr_Types.Firstleg	
172	183	define symbol Destwpt	:= Flight_Pln_Hdr_Types.Destwpt	
173	184	define symbol Invalid	:= Io_Interface_Tpkg.Entry_Stat_Type'(Io_Interface_Tpkg.Invalid)	
174	185	define symbol Valid	:= Io_Interface_Tpkg.Entry_Stat_Type'(Io_Interface_Tpkg.Valid)	
175	186	define symbol Master	:= Base_Domain_Services_Tpkg.Master	
176	187	define symbol ALTERNATE	:= Perf_Ext_Tpkg.Alternate	
177	188	define symbol AF	:= Lateral_Path_Type_Tpkg.AF	
178	189	define symbol FA	:= Lateral_Path_Type_Tpkg.FA	
179	190	define symbol CLIMBSEG	:= Fmcs_Fp_Guid_Btypes.CLIMBSEG	
180	191	define symbol Descentseg	:= Fmcs_Fp_Guid_Btypes.Descentseg	
181	192	define symbol Tspnull	:= Flight_Pln_Leg_Types.Tspnull	
182	193	define symbol Tsptop	:= Flight_Pln_Leg_Types.Tsptop	
183	194	define symbol CAS	:= Fmcs_Base_Types.CAS	
184	195	define symbol Mach	:= Fmcs_Base_Types.Mach	
185	196			
186	197			
187	198	DEFAULTS		VALUE
188	199	-----		-----
		» -----		
189	200	Perf_Background_Dpkg.Timeconmiss_Updated		
		» False		
190	201	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg		
		» False		
191	202			
192	203			
193	204	CONSTANT		VALUE
194	205	-----		-----
		» -----		
195	206	DBG_TIMEOUT		
		» 300		
196	207			
197	208			
198	209	TESTID: 1		

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

199 210
200 211 Current itinerary is Active Primary Flight plan and preds are output Active flight plan predictions refresh timer is
201 212 updated by calling Prf_Int_Utils.Update_Refresh_Timer.
202 213 (PERF_SDD_3511_INT)
203 214 Aircraft options and data shall be read in from the OPC and AMI databases upon system power-up (Cold Start).
204 215 The following data needs to be obtained:
205 216 Options_And_Data_Pkg.Final_Alt
206 217 Options_And_Data_Pkg.Final_Fuel
207 218 Options_And_Data_Pkg.Fuel_Pred_Final_Time
208 219 Options_And_Data_Pkg.Fuel_Pred_Final_Dest
209 220 Options_And_Data_Pkg.Fuel_Plng_Final_Time
210 221 Options_And_Data_Pkg.Altn_Trip_In_Rsv_Enb
211 222 Options_And_Data_Pkg.Ats_Enable
212 223 Options_And_Data_Pkg.Cmp_Rsv_In_Flt_Enb
213 224 Options_And_Data_Pkg.Route_Reserve_Percent
214 225 Options_And_Data_Pkg.Route_Reserve_Upper_Limit
215 226 Options_And_Data_Pkg.Route_Reserve_Lower_Limit
216 227 (PERF_SDD_2094_INT)
217 228 Itin is a maxalt and partition is in Dual_Slave mode.
218 229 Prf_Int_Utils.Dual_Status returns the master/slave and dual indication via a single data item based
219 230 on IO/OPS status items.
220 231 (PERF_SDD_3523_INT)
221 232 If the current itinerary is Active Primary Flight Plan Predictions, then the last flight level shall be
222 233 sent to IO for output when the flight plan has been completely predicted.
223 234 (PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))
224 235 If the predictions-output (Preds_Output) indication is true for the working flight plan, then indication
225 236 shall be stored to notify EFIS about the finish of predictions (Preds_Complete) for the working flight plan
226 237 by calling the procedure Perf_Interface_Dpkg.Put_Preds_Complete.
227 238 (PERF_SDD_5587_INT)
228 239 Options_And_Data_Pkg.Fuel_Pred_Final_Dest is equal to "P" indicating the final destination is the primary destination.
    » Then
229 240 Perf_Background_Dpkg.Pcfinaldest is set to Primary.
230 241 (PERF_SDD_5614_DR(PERF_SRD_1544_A3XX, PERF_SRD_7463))
231 242
232 243
233 244 INPUT
234 245 -----
    » -----
235 246 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr
    » 0
236 247 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx
    » 2
237 248 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change
    » False

```

VALUE

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

238	249	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec
		» False
239	250	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec
		» False
240	251	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec
		» False
241	252	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec
		» False
242	253	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec
		» False
243	254	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
		» False
244	255	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
		» 2
245	256	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
		» 0.0
246	257	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
		» 0.0
247	258	Perf_Background_Dpkg.Opt_Step_Data.Distodest
		» 25.0
248	259	Perf_Background_Dpkg.Opt_Step_Data.Timetogo
		» 5.0
249	260	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
		» 0.0
250	261	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
		» 0.0
251	262	Perf_Background_Dpkg.Pshmpreddata.Speed
		» 250.0
252	263	Perf_Background_Dpkg.Pshmpreddata.Fuel
		» 50.0
253	264	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
		» False
254	265	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
		» 0.0
255	266	Perf_Background_Dpkg.Pcoptalt.Valid
		» True
256	267	Perf_Background_Dpkg.Pcoptalt.Data
		» 19000.0
257	268	Fmcs_Partition_Data_Pkg.Ops_Master_Status
		» Master
258	269	Ctp_Perf_bkgnd_put_bk_data.Boot_Status
		» ld_Start
259	270	Perf_Background_Dpkg.Preds_Output(Active)
		» True

Co

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

260	271	Perf_Background_Dpkg.Psfinalalt	
		» 0.0	
261	272	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt	
		» 5000	
262	273	Perf_Background_Dpkg.Psfpolfnlful	
		» 0.0	
263	274	Perf_Background_Dpkg.Psfpolfnltme	
		» 0.0	
264	275	Perf_Background_Dpkg.Psfpolfnltg	
		» 0.0	
265	276	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel	
		» 40	
266	277	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	
		» 50	
267	278	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Percent	
		» 100.0	
268	279	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Upper_Limit	
		» 4.0	
269	280	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Lower_Limit	
		» 1.0	
270	281	Options_And_Data_Pkg:body.All_Options.Ats_Enable	
		» True	
271	282	Options_And_Data_Pkg:body.All_Options.Alt_n_Trip_In_Rsv_Enb	
		» True	
272	283	Options_And_Data_Pkg:body.All_Options.Cmp_Rsv_In_Flt_Enb	
		» True	
273	284	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	
		» 60	
274	285	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
275	286	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
276	287	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» True	
277	288	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
278	289	Perf_Background_Dpkg.Pcfltphase	P
		» reflight	
279	290	Perf_Background_Dpkg.Psfinaldes	
		» True	
280	291	Perf_Background_Dpkg.Vert_Auto_Mode	
		» True	
281	292	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

282	293	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 55000.0	
283	294	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	
284	295	Perf_background_Dpkg.Maxalt.Num_Engout	
		» 0	
285	296	Perf_Background_Dpkg.Etp_Itin_Ran	
		» True	
286	297	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» False	
287	298	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» False	
288	299	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
289	300	Perf_Background_Dpkg.Pcitin.Flight_Plan	
		» Active	
290	301	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
291	302	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst	
		» False	
292	303	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress	
		» False	
293	304	Perf_Background_Dpkg.Pcgmtime.Gpc_Time	
		» 2	
294	305	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	
		» 0	
295	306	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq	
		» 0	
296	307	Perf_Background_Dpkg.Psprddataseq	
		» 3	
297	308	Perf_Dpkg.Pstopofcrzfl(Active).Data	
		» 10.0	
298	309	Perf_Dpkg.Pstopofcrzfl(Active).Valid	
		» True	
299	310	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc	
		» True	
300	311	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	
		» False	
301	312	Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest	
		» "P"	
302	313	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status	Perf_Int_Base_Tpkg.Dua
		» 1_Master	
303	314	Perf_Background_Dpkg.Ats_Enable	
		» False	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

304	315	Perf_Background_Dpkg.Psrsvaltn			
		» False			
305	316	Perf_Background_Dpkg.Psrsvinflt			
		» False			
306	317	Perf_Background_Dpkg.Psrtersvpctg			
		» 0.0			
307	318	Perf_Background_Dpkg.Psmaxrtersv			
		» 0.0			
308	319	Perf_Background_Dpkg.Psminrtersv			
		» 0.0			
309	320	Perf_Background_Dpkg.Pcfinaldest			Perf_Ext_Tpkg.A
		» lternate			
310	321	CTP_PERF_BKGND_PUT_BK_DATA.Fpln			
		» Active			
311	322	Change			
		» True			
312	323	Change			
		» True			
313	324				
314	325				
315	326	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
316	327	-----	-----	-----	-----
		» -----			
317	328	Timer.Start_Time		0 (N/A)	
		» 0 P			
318	329	Timer.Refresh_Time		0.0 0.001	0.0
		» 0000E+00 P			
319	330	Timer.Average_Refresh_Time		0.0 0.001	0.0
		» 0000E+00 P			
320	331	Timer.Number_Of_Points		1 (N/A)	
		» 1 P			
321	332	Timer.Avg_Refresh_Time_Data(1)		0.0 0.001	0.0
		» 0000E+00 P			
322	333				
323	334				
324	335	INPUT			VALUE
325	336	-----	-----	-----	-----
		» -----			
326	337	Change			
		» True			
327	338	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog			
		» True			
328	339				



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

			EXPECTED	TOLERANCE	ACTUAL
329	340				
330	341	OUTPUT			
		» P/F			
331	342	-----	-----	-----	-----
		» -----			
332	343	Data_Storage.Preds_Complete(Fpln)	True	(N/A)	
		» TRUE P			
333	344	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr	0	(N/A)	
		» 0 P			
334	345	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	True	(N/A)	
		» TRUE P			
335	346	Perf_Background_Dpkg.Psfinalalt	5000.0	0.001	5.0
		» 0000E+03 P			
336	347	Perf_Background_Dpkg.Psfpolfnlful	40.0	0.001	4.0
		» 0000E+01 P			
337	348	Perf_Background_Dpkg.Psfpolfnltme	50.0	0.001	5.0
		» 0000E+01 P			
338	349	Perf_Background_Dpkg.Psfpolfnltg	60.0	0.001	6.0
		» 0000E+01 P			
339	350	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	False	(N/A)	
		» FALSE P			
340	351	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	False	(N/A)	
		» FALSE P			
341	352	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	False	(N/A)	
		» FALSE P			
342	353	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	False	(N/A)	
		» FALSE P			
343	354	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	False	(N/A)	
		» FALSE P			
344	355	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	False	(N/A)	
		» FALSE P			
345	356	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)	
		» FALSE P			
346	357	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status	Perf_Int_Base_Tpkg.Single	(N/A)	
		» SINGLE P			
347	358	Perf_Background_Dpkg.Ats_Enable	True	(N/A)	
		» TRUE P			
348	359	Perf_Background_Dpkg.Psrsvalt	True	(N/A)	
		» TRUE P			
349	360	Perf_Background_Dpkg.Psrsvinflt	True	(N/A)	
		» TRUE P			
350	361	Perf_Background_Dpkg.Psrtersvpctg	1.0	0.001	1.0
		» 0000E+00 P			
351	362	Perf_Background_Dpkg.Psmxrtersv	4.0	0.001	4.0

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

352	363	» 0000E+00 P			
		Perf_Background_Dpkg.Psminrtersv	1.0	0.001	1.0
		» 0000E+00 P			
353	364	Perf_Background_Dpkg.Preds_Output(Active)	True	(N/A)	
		» TRUE P			
354	365	Perf_Background_Dpkg.Pcfinaldest	Perf_Ext_Tpkg.Primary	(N/A)	
		» PRIMARY P			
355	366				
356	367				
357	368	====> All 28 Comparisons Passed <====			
358	369				
359	370				
360	371	TESTID: 2			
361	372				
362	373	Initialization occurs for a warm start. Also, itin is active preds and a change occurs that causes interruption of pr			
		» eds			
363	374	so no output is made.			
364	375	(PERF_SDD_2631_INT,PERF_SDD_2159_INT,PERF_SDD_4543_INT,PERF_SDD_2158_INT,			
365	376	PERF_SDD_2289(PERF_SRD_10253,PERF_SRD_10333_INT,PERF_SRD_12092,PERF_SRD_12093,			
366	377	PERF_SRD_12094,PERF_SRD_12095,PERF_SRD_9993,PERF_SRD_9994), PERF_SDD_2094_INT)			
367	378	Itin is a maxalt and partition is in Dual_Slave mode.			
368	379	Prf_Int_Utils.Dual_Status is a function that shall return the master/slave and dual indication via a single data item			
		» based			
369	380	on IO/OPS status items.			
370	381	(PERF_SDD_3523_INT)			
371	382	The last flight level shall be sent to IO for output when the flight plan has been completely predicted.			
372	383	(PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))			
373	384	If the scratch flight plan is not being used, the predictions-output indication shall be set			
374	385	according to Table 11.14-4.			
375	386	In this case Predictions_Output is set to TRUE			
376	387	(PERF_SDD_4544_INT)			
377	388				
378	389				
379	390	INPUT			VALUE
380	391	-----			
		» -----			
381	392	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr			
		» 0			
382	393	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change			
		» False			
383	394	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec			
		» False			
384	395	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec			
		» False			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

385 396 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec
      » False
386 397 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec
      » False
387 398 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec
      » False
388 399 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
      » False
389 400 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
      » 2
390 401 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
      » 0.0
391 402 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
      » 0.0
392 403 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog
      » True
393 404 Perf_Background_DPkg.Opt_Step_Data.Distodest
      » 25.0
394 405 Perf_Background_DPkg.Opt_Step_Data.Timetogo
      » 5.0
395 406 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
      » 0.0
396 407 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
      » 0.0
397 408 Perf_Background_Dpkg.Pshmpreddata.Speed
      » 250.0
398 409 Perf_Background_Dpkg.Pshmpreddata.Fuel
      » 50.0
399 410 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
      » False
400 411 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
      » 0.0
401 412 Perf_Background_Dpkg.Pcoptalt.Valid
      » True
402 413 Perf_Background_Dpkg.Pcoptalt.Data
      » 19000.0
403 414 Fmcs_Partition_Data_Pkg.Ops_Master_Status
      » Master
404 415 Ctp_Perf_bkgnd_put_bk_data.Boot_Status
      » rm_Start
405 416 Perf_Background_Dpkg.Preds_Output(Active)
      » True
406 417 Perf_Background_Dpkg.Psfinalalt
      » 0.0

```

Wa

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

407 418 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt
      » 5000
408 419 Perf_Background_Dpkg.Psfpolfnlful
      » 0.0
409 420 Perf_Background_Dpkg.Psfpolfnltme
      » 0.0
410 421 Perf_Background_Dpkg.Psfpolfnltg
      » 0.0
411 422 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel
      » 40
412 423 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time
      » 50
413 424 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time
      » 60
414 425 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Percent
      » 100.0
415 426 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Upper_Limit
      » 4.0
416 427 Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Lower_Limit
      » 1.0
417 428 Options_And_Data_Pkg:body.All_Options.Ats_Enable
      » True
418 429 Options_And_Data_Pkg:body.All_Options.Altn_Trip_In_Rsv_Enb
      » True
419 430 Options_And_Data_Pkg:body.All_Options.Cmp_Rsv_In_Flt_Enb
      » True
420 431 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done
      » True
421 432 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid
      » True
422 433 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass
      » False
423 434 Perf_Background_Dpkg.Pcfpln
      » atchfpln
424 435 Perf_Background_Dpkg.Pcfltphase
      » Cruise
425 436 Perf_Background_Dpkg.Psfinaldes
      » True
426 437 Perf_Background_Dpkg.Vert_Auto_Mode
      » True
427 438 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data
      » 50000.0
428 439 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data
      » 55000.0

```

Scr

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

429	440	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	
430	441	Perf_background_Dpkg.Maxalt.Num_Engout	
		» 0	
431	442	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» False	
432	443	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» False	
433	444	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
434	445	Perf_Dpkg.Pstopofcrzfl(Active).Valid	
		» True	
435	446	Perf_Dpkg.Pstopofcrzfl(Active).Data	
		» 10.0	
436	447	Perf_Background_Dpkg.Pcitin.Flight_Plan	
		» Active	
437	448	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
438	449	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst	
		» False	
439	450	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress	
		» False	
440	451	Perf_Background_Dpkg.Pcgmtime.Gpc_Time	
		» 2	
441	452	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	
		» 0	
442	453	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq	
		» 0	
443	454	Perf_Background_Dpkg.Psprddataseq	
		» 3	
444	455	Perf_Background_Dpkg.Etp_Itin_Ran	
		» False	
445	456	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc	
		» True	
446	457	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	
		» False	
447	458	Ctp_Perf_Bkgnd_Put_Bk_Data.Out_Gleg.Spalt1	
		» 0.0	
448	459	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Spalt1	
		» 2.0	
449	460	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status	Perf_Int_Base_Tpkg.Dua
		» 1_Master	
450	461	Perf_Background_Dpkg.Ats_Enable	
		» False	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

451	462	Perf_Background_Dpkg.Psrsvaltn			
		» False			
452	463	Perf_Background_Dpkg.Psrsvinflt			
		» False			
453	464	Perf_Background_Dpkg.Psrtersvpctg			
		» 0.0			
454	465	Perf_Background_Dpkg.Psmaxrtersv			
		» 0.0			
455	466	Perf_Background_Dpkg.Psminrtersv			
		» 0.0			
456	467	Change			
		» False			
457	468	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx			
		» 2			
458	469	Chk_Idx			Ctp_Perf_Bkgnd_Put_Bk_Data
		» .Chk_Idx			
459	470				
460	471				
461	472	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
462	473	-----	-----	-----	-----
		» -----			
463	474	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr	36	(N/A)	
		» 36 P			
464	475	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	True	(N/A)	
		» TRUE P			
465	476	Perf_Background_Dpkg.Psfinalalt	0.0	0.001	0.0
		» 0000E+00 P			
466	477	Perf_Background_Dpkg.Psfpolfnlful	0.0	0.001	0.0
		» 0000E+00 P			
467	478	Perf_Background_Dpkg.Psfpolfnltme	0.0	0.001	0.0
		» 0000E+00 P			
468	479	Perf_Background_Dpkg.Psfpolfnltg	0.0	0.001	0.0
		» 0000E+00 P			
469	480	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	False	(N/A)	
		» FALSE P			
470	481	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	False	(N/A)	
		» FALSE P			
471	482	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	False	(N/A)	
		» FALSE P			
472	483	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	False	(N/A)	
		» FALSE P			
473	484	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	False	(N/A)	
		» FALSE P			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

474	485	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	False	(N/A)	
		» FALSE P			
475	486	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	2	(N/A)	
		» 2 P			
476	487	Ctp_Perf_Bkgnd_Put_Bk_Data.Out_Gleg.Spalt1	2.0	0.001	2.0
		» 0000E+00 P			
477	488	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	True	(N/A)	
		» TRUE P			
478	489	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status	Perf_Int_Base_Tpkg.Single	(N/A)	
		» SINGLE P			
479	490	Perf_Background_Dpkg.Ats_Enable	False	(N/A)	
		» FALSE P			
480	491	Perf_Background_Dpkg.Psrsvaltn	False	(N/A)	
		» FALSE P			
481	492	Perf_Background_Dpkg.Psrsvinflt	False	(N/A)	
		» FALSE P			
482	493	Perf_Background_Dpkg.Psrtersvpctg	0.0	0.001	0.0
		» 0000E+00 P			
483	494	Perf_Background_Dpkg.Psmaxrtersv	0.0	0.001	0.0
		» 0000E+00 P			
484	495	Perf_Background_Dpkg.Psminrtersv	0.0	0.001	0.0
		» 0000E+00 P			
485	496	Perf_Background_Dpkg.Preds_Output(Active)	True	(N/A)	
		» TRUE P			
486	497				
487	498				
488	499	====> All 23 Comparisons Passed <====			
489	500				
490	501				
491	502	TESTID: 3			
492	503				
493	504	Verification when FM in in dual mode and when Itin is a maxalt then maximum altitude data from Master FM is imposed			
494	505	to Slave FM which keeps the Max Alt data synchronised between two FMs.			
495	506	(PERF_SDD_2096 (PERF_SRD_2020))			
496	507				
497	508				
498	509	INPUT			VALUE
499	510	-----			
		» -----			
500	511	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr			
		» 0			
501	512	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx			
		» 2			
502	513	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

	»	False
503	514	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec
	»	False
504	515	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec
	»	False
505	516	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec
	»	False
506	517	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec
	»	False
507	518	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec
	»	False
508	519	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
	»	False
509	520	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
	»	2
510	521	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
	»	0.0
511	522	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
	»	0.0
512	523	Perf_Etp_Dpkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog
	»	True
513	524	Perf_Background_Dpkg.Opt_Step_Data.Distodest
	»	25.0
514	525	Perf_Background_Dpkg.Opt_Step_Data.Timetogo
	»	5.0
515	526	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
	»	0.0
516	527	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
	»	0.0
517	528	Perf_Background_Dpkg.Pshmpreddata.Speed
	»	250.0
518	529	Perf_Background_Dpkg.Pshmpreddata.Fuel
	»	50.0
519	530	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
	»	False
520	531	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
	»	0.0
521	532	Perf_Background_Dpkg.Pcoptalt.Valid
	»	True
522	533	Perf_Background_Dpkg.Pcoptalt.Data
	»	19000.0
523	534	Fmcs_Partition_Data_Pkg.Ops_Master_Status
	»	Master
524	535	Ctp_Perf_bkgnd_put_bk_data.Boot_Status



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

	» rm_Start
525	536 Perf_Background_Dpkg.Preds_Output(Active)
	» True
526	537 Perf_Background_Dpkg.Psfinalalt
	» 0.0
527	538 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt
	» 5000
528	539 Perf_Background_Dpkg.Psfpolfnlful
	» 0.0
529	540 Perf_Background_Dpkg.Psfpolfnltme
	» 0.0
530	541 Perf_Background_Dpkg.Psfpolfnltg
	» 0.0
531	542 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel
	» 40
532	543 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time
	» 50
533	544 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time
	» 60
534	545 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done
	» True
535	546 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid
	» True
536	547 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass
	» False
537	548 Perf_Background_Dpkg.Pcfpln
	» tprimary
538	549 Perf_Background_Dpkg.Pcfltphase
	» Cruise
539	550 Perf_Background_Dpkg.Psfinaldes
	» True
540	551 Perf_Background_Dpkg.Vert_Auto_Mode
	» True
541	552 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data
	» 50000.0
542	553 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data
	» 55000.0
543	554 Perf_background_Dpkg.Maxalt.Gwt
	» 150000.0
544	555 Perf_background_Dpkg.Maxalt.Num_Engout
	» 0
545	556 Perf_Background_Dpkg.Etp_Itin_Ran
	» True
546	557 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid

Ac

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

547	558	» False			
		Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid			
		» False			
548	559	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode			
		» Single			
549	560	Perf_Dpkg.Pstopofcrzfl(Active).Valid			
		» False			
550	561	Perf_Background_Dpkg.Pcitin.Itinerary			
		» Maxalt			
551	562	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst			
		» False			
552	563	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress			
		» False			
553	564	Perf_Background_Dpkg.Pcgmtime.Gpc_Time			
		» 2			
554	565	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt			
		» 0			
555	566	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq			
		» 0			
556	567	Perf_Background_Dpkg.Psprddataseq			
		» 3			
557	568	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc			
		» True			
558	569	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid			
		» False			
559	570	Change			
		» False			
560	571				
561	572				
562	573	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
563	574	-----	-----	-----	-----
		» -----			
564	575	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	True	(N/A)	
		» TRUE P			
565	576	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	False	(N/A)	
		» FALSE P			
566	577	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	False	(N/A)	
		» FALSE P			
567	578	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	False	(N/A)	
		» FALSE P			
568	579	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	False	(N/A)	
		» FALSE P			
569	580	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	False	(N/A)	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

570	581	» FALSE P		
		Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	False	(N/A)
		» FALSE P		
571	582	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)
		» FALSE P		
572	583			
573	584			
574	585	====> All 8 Comparisons Passed <====		
575	586			
576	587			
577	588	TESTID: 4		
578	589			
579	590	Itin is a maxalt and partition is in dual mode so information needs to be passed from Master to Slave.		
580	591	Verify Maximum Alt data is transmitted from master to slave.		
581	592	(PERF_SDD_2096 (PERF_SRD_2020))		
582	593	This transmission of data is done by the procedure Prf_Maxalt_Dpkg.Transmit_Dual_Data.		
583	594	(PERF_SDD_2417_INT)		
584	595	The validity flags for Max Max Alt, Rec Max Alt and Eng Out Max Alt shall be output on the Flight Test Bus		
585	596	in re-packed format whenever Prf_Maxalt_Dpkg.Put_Maximum_Altitude_Data is called.		
586	597	(PERF_SDD_3680_INT,PERF_SDD_2414_INT,PERF_SDD_2407_INT)		
587	598	Prf_Int_Utls.Dual_Status is a function that shall return the master/slave and dual indication via a single data item		
		» based		
588	599	on IO/OPS status items.		
589	600	(PERF_SDD_3523_INT)		
590	601			
591	602			
592	603	INPUT		VALUE
593	604	-----		
		» -----		
594	605	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr		
		» 0		
595	606	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx		
		» 2		
596	607	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change		
		» False		
597	608	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec		
		» False		
598	609	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec		
		» False		
599	610	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec		
		» False		
600	611	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec		
		» False		
601	612	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec		

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

602   613 »      False
        Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
        »      False
603   614 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
        »      2
604   615 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
        »      0.0
605   616 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
        »      0.0
606   617 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog
        »      True
607   618 Perf_Background_DPkg.Opt_Step_Data.Distodest
        »      25.0
608   619 Perf_Background_DPkg.Opt_Step_Data.Timetogo
        »      5.0
609   620 Ctp_Perf_bkgnd_put_bk_data.Pshmpredata.Speed
        »      0.0
610   621 Ctp_Perf_bkgnd_put_bk_data.Pshmpredata.Fuel
        »      0.0
611   622 Perf_Background_Dpkg.Pshmpredata.Speed
        »      250.0
612   623 Perf_Background_Dpkg.Pshmpredata.Fuel
        »      50.0
613   624 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
        »      False
614   625 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
        »      0.0
615   626 Perf_Background_Dpkg.Pcoptalt.Valid
        »      True
616   627 Perf_Background_Dpkg.Pcoptalt.Data
        »      19000.0
617   628 Fmcs_Partition_Data_Pkg.Ops_Master_Status
        »      Master
618   629 Ctp_Perf_bkgnd_put_bk_data.Boot_Status
        »      rm_Start
619   630 Perf_Background_Dpkg.Preds_Output(Active)
        »      True
620   631 Perf_Background_Dpkg.Psfinalalt
        »      0.0
621   632 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt
        »      5000
622   633 Perf_Background_Dpkg.Psfpolfnlful
        »      0.0
623   634 Perf_Background_Dpkg.Psfpolfnltme

```

Wa

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

624 635 » 0.0
625 636 Perf_Background_Dpkg.Psfpolfnltg
626 637 » 0.0
627 638 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel
628 639 » 40
629 640 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time
630 641 » 50
631 642 Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time
632 643 » 60
633 644 Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done
634 645 » True
635 646 Perf_Background_Dpkg.Pctcstrctrl(Active).Valid
636 647 » True
637 648 Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass
638 649 » False
639 650 Perf_Background_Dpkg.Pcfpln
640 651 » tprimary
641 652 Perf_Background_Dpkg.Pcfltphase
642 653 » Cruise
643 654 Perf_Background_Dpkg.Psfinaldes
644 655 » True
645 656 Perf_Background_Dpkg.Vert_Auto_Mode
646 657 » True
647 658 Perf_background_Dpkg.Maxalt.Maximum_Alt.Data
648 659 » 50000.0
649 660 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data
650 661 » 55000.0
651 662 Perf_background_Dpkg.Maxalt.Gwt
652 663 » 150000.0
653 664 Perf_background_Dpkg.Maxalt.Num_Engout
654 665 » 1
655 666 Perf_Background_Dpkg.Etp_Itin_Ran
656 667 » True
657 668 Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid
658 669 » True
659 670 Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid
660 671 » True
661 672 Fmcs_Partition_Data_Pkg.Ops_Dual_Mode
662 673 » Dual
663 674 Perf_Dpkg.Pstopofcrzfl(Active).Valid
664 675 » False
665 676 Perf_Background_Dpkg.Pcitin.Itinerary
666 677 » Maxalt
667 678 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst

```

Ac

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» False	
646	657	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress	
		» False	
647	658	Perf_Background_Dpkg.Pcgmtime.Gpc_Time	
		» 2	
648	659	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	
		» 0	
649	660	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq	
		» 0	
650	661	Perf_Background_Dpkg.Psprddataseq	
		» 3	
651	662	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc	
		» True	
652	663	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	
		» False	
653	664	Perf_Background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» True	
654	665	Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» True	
655	666	Perf_Background_Dpkg.Maxalt.Eo_Maximum_Alt.Valid	
		» True	
656	667	Perf_Flight_Test_Dpkg.Perf_Repack_Data.Maxalt_Valid	
		» False	
657	668	Perf_Flight_Test_Dpkg.Perf_Repack_Data.Max_Maxalt_Valid	
		» False	
658	669	Perf_Flight_Test_Dpkg.Perf_Repack_Data.Engine_Out_Maxalt_Valid	
		» False	
659	670	Perf_Dual_Dpkg.Maxalt.Maximum_Alt	
		» 0.0	
660	671	Perf_Dual_Dpkg.Maxalt.Maximum_Maximum_Alt	
		» 0.0	
661	672	Perf_Dual_Dpkg.Maxalt.Gwt	
		» 0.0	
662	673	Perf_Dual_Dpkg.Maxalt.Engines_Out	
		» 0	
663	674	Perf_Dual_Dpkg.Maxalt.Valid	
		» False	
664	675	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status	Perf_Int_Base_Tpk
		» g.Single	
665	676	Change	
		» False	
666	677		
667	678		
668	679	OUTPUT	

EXPECTED

TOLERANCE

ACTUAL  
Beyond Compare 2.1.1

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» P/F			
669	680	-----			
		» -----			
670	681	Perf_Etp_Dpkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	True	(N/A)	
		» TRUE P			
671	682	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	False	(N/A)	
		» FALSE P			
672	683	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	False	(N/A)	
		» FALSE P			
673	684	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	False	(N/A)	
		» FALSE P			
674	685	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	False	(N/A)	
		» FALSE P			
675	686	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	False	(N/A)	
		» FALSE P			
676	687	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	False	(N/A)	
		» FALSE P			
677	688	Perf_Dual_Dpkg.Maxalt.Maximum_Alt	50000.0	0.001	5.0
		» 0000E+04 P			
678	689	Perf_Dual_Dpkg.Maxalt.Maximum_Maximum_Alt	55000.0	0.001	5.5
		» 0000E+04 P			
679	690	Perf_Dual_Dpkg.Maxalt.Gwt	150000.0	0.001	1.5
		» 0000E+05 P			
680	691	Perf_Dual_Dpkg.Maxalt.Engines_Out	1	(N/A)	
		» 1 P			
681	692	Perf_Dual_Dpkg.Maxalt.Valid	True	(N/A)	
		» TRUE P			
682	693	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)	
		» FALSE P			
683	694	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status	Perf_Int_Base_Tpkg.Dual_Master	(N/A)	DUA
		» L_MASTER P			
684	695				
685	696				
686	697	====> All 14 Comparisons Passed <====			
687	698				
688	699				
689	700	TESTID: 5			
690	701				
691	702	Itin is Fuelpredact.Block fuel has not become pilot entered then following routines Put_Pcaltnpreds, Put_Pctriptime,			
692	703	Put_Final_Fuel, and Put_Route_Reserve are called to output data for display.			
693	704	PERF_SDD_1826(PERF_SRD_10167_INT), PERF_SDD_1831(PERF_SRD_10167_INT)			
694	705				
695	706				
696	707	INPUT			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

697 708 -----
        » -----
698 709 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr
        »      0
699 710 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx
        »      2
700 711 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec
        »      False
701 712 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec
        »      False
702 713 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec
        »      False
703 714 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec
        »      False
704 715 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec
        »      False
705 716 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
        »      False
706 717 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
        »      2
707 718 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
        »      0.0
708 719 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
        »      0.0
709 720 Perf_Etp_Dpkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog
        »      True
710 721 Perf_Background_Dpkg.Opt_Step_Data.Distodest
        »      25.0
711 722 Perf_Background_Dpkg.Opt_Step_Data.Timetogo
        »      5.0
712 723 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
        »      0.0
713 724 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
        »      0.0
714 725 Perf_Background_Dpkg.Pshmpreddata.Speed
        »      250.0
715 726 Perf_Background_Dpkg.Pshmpreddata.Fuel
        »      50.0
716 727 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
        »      False
717 728 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
        »      0.0
718 729 Perf_Background_Dpkg.Pcoptalt.Valid
        »      True

```



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

719	730	Perf_Background_Dpkg.Pcoptalt.Data	
		» 19000.0	
720	731	Fmcs_Partition_Data_Pkg.Ops_Master_Status	
		» Master	
721	732	Ctp_Perf_bkgnd_put_bk_data.Boot_Status	Wa
		» rm_Start	
722	733	Perf_Background_Dpkg.Preds_Output(Active)	
		» True	
723	734	Perf_Background_Dpkg.Psfinalalt	
		» 0.0	
724	735	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt	
		» 5000	
725	736	Perf_Background_Dpkg.Psfpolfnlful	
		» 0.0	
726	737	Perf_Background_Dpkg.Psfpolfnltme	
		» 0.0	
727	738	Perf_Background_Dpkg.Psfpolfnltg	
		» 0.0	
728	739	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel	
		» 40	
729	740	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	
		» 50	
730	741	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	
		» 60	
731	742	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
732	743	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
733	744	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» False	
734	745	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
735	746	Perf_Background_Dpkg.Pcfltphase	
		» Cruise	
736	747	Perf_Background_Dpkg.Psfinaldes	
		» True	
737	748	Perf_Background_Dpkg.Vert_Auto_Mode	
		» True	
738	749	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	
739	750	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 55000.0	
740	751	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

741	752	Perf_background_Dpkg.Maxalt.Num_Engout			
		» 0			
742	753	Perf_Background_Dpkg.Etp_Itin_Ran			
		» True			
743	754	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid			
		» False			
744	755	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid			
		» False			
745	756	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode			
		» Single			
746	757	Perf_Dpkg.Pstopofcrzfl(Active).Valid			
		» False			
747	758	Perf_Background_Dpkg.Pcitin.Itinerary			Fue
		» lpredact			
748	759	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst			
		» False			
749	760	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress			
		» False			
750	761	Perf_Background_Dpkg.Pcgmtime.Gpc_Time			
		» 2			
751	762	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt			
		» 0			
752	763	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq			
		» 0			
753	764	Perf_Background_Dpkg.Psprddataseq			
		» 3			
754	765	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc			
		» True			
755	766	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid			
		» False			
756	767	Change			
		» False			
757	768				
758	769				
759	770	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
760	771	-----	-----	-----	-----
		» -----			
761	772	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	True	(N/A)	
		» TRUE P			
762	773	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	True	(N/A)	
		» TRUE P			
763	774	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	True	(N/A)	
		» TRUE P			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

764	775	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	False	(N/A)
		» FALSE P		
765	776	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	True	(N/A)
		» TRUE P		
766	777	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	True	(N/A)
		» TRUE P		
767	778	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	False	(N/A)
		» FALSE P		
768	779	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)
		» FALSE P		
769	780			
770	781			
771	782	====> All 8 Comparisons Passed <====		
772	783			
773	784			
774	785	TESTID: 6		
775	786			
776	787	Itin is Fuelplanact2. Block fuel has not become pilot entered then following routines Put_Pcaltnpreds, Put_Pctriptime,		
777	788	Put_Final_Fuel, and Put_Route_Reserve are called to output data for display. Block Fuel is also outputed for display		
778	789	via Put_Block_Fuel given not pilot entered.		
779	790	PERF_SDD_1826(PERF_SRD_10167_INT), PERF_SDD_1831(PERF_SRD_10167_INT)		
780	791			
781	792			
782	793	INPUT		VALUE
783	794	-----		-----
		» -----		
784	795	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr		
		» 0		
785	796	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx		
		» 2		
786	797	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec		
		» False		
787	798	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec		
		» False		
788	799	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec		
		» False		
789	800	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec		
		» False		
790	801	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec		
		» False		
791	802	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec		
		» False		
792	803	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)		
		» 2		

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

793 804 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
      » 0.0
794 805 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
      » 0.0
795 806 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog
      » True
796 807 Perf_Background_DPkg.Opt_Step_Data.Distodest
      » 25.0
797 808 Perf_Background_DPkg.Opt_Step_Data.Timetogo
      » 5.0
798 809 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
      » 0.0
799 810 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
      » 0.0
800 811 Perf_Background_Dpkg.Pshmpreddata.Speed
      » 250.0
801 812 Perf_Background_Dpkg.Pshmpreddata.Fuel
      » 50.0
802 813 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
      » False
803 814 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
      » 0.0
804 815 Perf_Background_Dpkg.Pcoptalt.Valid
      » True
805 816 Perf_Background_Dpkg.Pcoptalt.Data
      » 19000.0
806 817 Fmcs_Partition_Data_Pkg.Ops_Master_Status
      » Master
807 818 Ctp_Perf_bkgnd_put_bk_data.Boot_Status
      » rm_Start
808 819 Perf_Background_Dpkg.Preds_Output(Active)
      » True
809 820 Perf_Background_Dpkg.Psfinalalt
      » 0.0
810 821 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt
      » 5000
811 822 Perf_Background_Dpkg.Psfpolfnlful
      » 0.0
812 823 Perf_Background_Dpkg.Psfpolfnltme
      » 0.0
813 824 Perf_Background_Dpkg.Psfpolfnltg
      » 0.0
814 825 Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel
      » 40

```

Wa

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

815	826	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	
		» 50	
816	827	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	
		» 60	
817	828	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
818	829	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
819	830	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» False	
820	831	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
821	832	Perf_Background_Dpkg.Pcfltphase	
		» Cruise	
822	833	Perf_Background_Dpkg.Psfinaldes	
		» True	
823	834	Perf_Background_Dpkg.Vert_Auto_Mode	
		» True	
824	835	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	
825	836	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 55000.0	
826	837	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	
827	838	Perf_background_Dpkg.Maxalt.Num_Engout	
		» 0	
828	839	Perf_Background_Dpkg.Etp_Itin_Ran	
		» True	
829	840	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» False	
830	841	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» False	
831	842	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
832	843	Perf_Dpkg.Pstopofcrzfl(Active).Valid	
		» False	
833	844	Perf_Background_Dpkg.Pcitin.Itinerary	Fuel
		» planact2	
834	845	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst	
		» False	
835	846	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress	
		» False	
836	847	Perf_Background_Dpkg.Pcgmtime.Gpc_Time	
		» 2	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

837	848	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt			
		» 0			
838	849	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq			
		» 0			
839	850	Perf_Background_Dpkg.Psprddataseq			
		» 3			
840	851	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc			
		» True			
841	852	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid			
		» False			
842	853	Change			
		» False			
843	854				
844	855				
845	856	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
846	857	-----	-----	-----	-----
		» -----			
847	858	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	True	(N/A)	
		» TRUE P			
848	859	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	True	(N/A)	
		» TRUE P			
849	860	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	True	(N/A)	
		» TRUE P			
850	861	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	True	(N/A)	
		» TRUE P			
851	862	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	True	(N/A)	
		» TRUE P			
852	863	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	True	(N/A)	
		» TRUE P			
853	864	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	False	(N/A)	
		» FALSE P			
854	865	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)	
		» FALSE P			
855	866				
856	867				
857	868	====> All 8 Comparisons Passed <====			
858	869				
859	870				
860	871	TESTID: 7			
861	872				
862	873	Itin is Optalt so opt crz alt is outputed for display via Put_Cdoptalt.			
863	874	(PERF_SDD_2109_INT)			
864	875	The current itinerary is Optimum Altitude Predictions, optimum cruise altitude is copied to Perf interface Optimum alt			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» itude.	
865	876	(PERF_SDD_4220_INT)	
866	877		
867	878		
868	879	INPUT	VALUE
869	880	-----	-----
		» -----	
870	881	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr	
		» 0	
871	882	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx	
		» 2	
872	883	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change	
		» False	
873	884	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	
		» False	
874	885	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	
		» False	
875	886	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	
		» False	
876	887	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	
		» False	
877	888	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	
		» False	
878	889	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	
		» False	
879	890	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)	
		» 2	
880	891	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest	
		» 0.0	
881	892	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo	
		» 0.0	
882	893	Perf_Etp_Dpkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	
		» True	
883	894	Perf_Background_Dpkg.Opt_Step_Data.Distodest	
		» 25.0	
884	895	Perf_Background_Dpkg.Opt_Step_Data.Timetogo	
		» 5.0	
885	896	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed	
		» 0.0	
886	897	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel	
		» 0.0	
887	898	Perf_Background_Dpkg.Pshmpreddata.Speed	
		» 250.0	
888	899	Perf_Background_Dpkg.Pshmpreddata.Fuel	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» 50.0	
889	900	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid	
		» False	
890	901	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data	
		» 0.0	
891	902	Perf_Background_Dpkg.Pcoptalt.Valid	
		» True	
892	903	Perf_Background_Dpkg.Pcoptalt.Data	
		» 19000.0	
893	904	Fmcs_Partition_Data_Pkg.Ops_Master_Status	
		» Master	
894	905	Ctp_Perf_bkgnd_put_bk_data.Boot_Status	Wa
		» rm_Start	
895	906	Perf_Background_Dpkg.Preds_Output(Active)	
		» True	
896	907	Perf_Background_Dpkg.Psfinalalt	
		» 0.0	
897	908	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt	
		» 5000	
898	909	Perf_Background_Dpkg.Psfpolfnlful	
		» 0.0	
899	910	Perf_Background_Dpkg.Psfpolfnltme	
		» 0.0	
900	911	Perf_Background_Dpkg.Psfpolfnltg	
		» 0.0	
901	912	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel	
		» 40	
902	913	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	
		» 50	
903	914	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	
		» 60	
904	915	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
905	916	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
906	917	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» False	
907	918	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
908	919	Perf_Background_Dpkg.Pcfltphase	
		» Cruise	
909	920	Perf_Background_Dpkg.Psfinaldes	
		» True	
910	921	Perf_Background_Dpkg.Vert_Auto_Mode	



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

911	922	» True
		Perf_background_Dpkg.Maxalt.Maximum_Alt.Data
		» 50000.0
912	923	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data
		» 55000.0
913	924	Perf_background_Dpkg.Maxalt.Gwt
		» 150000.0
914	925	Perf_background_Dpkg.Maxalt.Num_Engout
		» 0
915	926	Perf_Background_Dpkg.Etp_Itin_Ran
		» True
916	927	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid
		» False
917	928	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid
		» False
918	929	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode
		» Single
919	930	Perf_Dpkg.Pstopofcrzfl(Active).Valid
		» False
920	931	Perf_Background_Dpkg.Pcitin.Itinerary
		» Optalt
921	932	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst
		» False
922	933	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress
		» False
923	934	Perf_Background_Dpkg.Pcgmtime.Gpc_Time
		» 2
924	935	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt
		» 0
925	936	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq
		» 0
926	937	Perf_Background_Dpkg.Psprddataseq
		» 3
927	938	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc
		» True
928	939	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid
		» False
929	940	Perf_Dpkg.Optimum_Altitude.Data
		» 0.0
930	941	Change
		» False
931	942	
932	943	
933	944	OUTPUT

EXPECTED

TOLERANCE

 ACTUAL  
 Beyond Compare 2.1.1

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» P/F			
934	945	-----			
		» -----			
935	946	Perf_Etp_Dpkg:body.Data.Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	True	(N/A)	
		» TRUE P			
936	947	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	False	(N/A)	
		» FALSE P			
937	948	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	False	(N/A)	
		» FALSE P			
938	949	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	False	(N/A)	
		» FALSE P			
939	950	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	False	(N/A)	
		» FALSE P			
940	951	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	False	(N/A)	
		» FALSE P			
941	952	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest	0.0	0.001	0.0
		» 0000E+00 P			
942	953	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo	0.0	0.001	0.0
		» 0000E+00 P			
943	954	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	False	(N/A)	
		» FALSE P			
944	955	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed	0.0	0.001	0.0
		» 0000E+00 P			
945	956	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel	0.0	0.001	0.0
		» 0000E+00 P			
946	957	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid	True	(N/A)	
		» TRUE P			
947	958	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data	19000.0	0.001	1.9
		» 0000E+04 P			
948	959	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)	
		» FALSE P			
949	960	Perf_Dpkg.Optimum_Altitude.Data	19000.0	0.001	1.9
		» 0000E+04 P			
950	961				
951	962				
952	963	====> All 15 Comparisons Passed <====			
953	964				
954	965				
955	966	TESTID: 8			
956	967				
957	968	Itin is optimum step so optimum step data is outputed for display via Put_Optimum_Step			
958	969	(PERF_SDD_2113_INT)			
959	970				
960	971				

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		VALUE
961	972	INPUT
962	973	-----
		» -----
963	974	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr
		» 0
964	975	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx
		» 2
965	976	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change
		» False
966	977	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec
		» False
967	978	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec
		» False
968	979	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec
		» False
969	980	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec
		» False
970	981	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec
		» False
971	982	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
		» False
972	983	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
		» 2
973	984	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
		» 0.0
974	985	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
		» 0.0
975	986	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog
		» True
976	987	Perf_Background_DPkg.Opt_Step_Data.Distodest
		» 25.0
977	988	Perf_Background_DPkg.Opt_Step_Data.Timetogo
		» 5.0
978	989	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
		» 0.0
979	990	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
		» 0.0
980	991	Perf_Background_Dpkg.Pshmpreddata.Speed
		» 250.0
981	992	Perf_Background_Dpkg.Pshmpreddata.Fuel
		» 50.0
982	993	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
		» False
983	994	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» 0.0	
984	995	Perf_Background_Dpkg.Pcoptalt.Valid	
		» True	
985	996	Perf_Background_Dpkg.Pcoptalt.Data	
		» 19000.0	
986	997	Fmcs_Partition_Data_Pkg.Ops_Master_Status	
		» Master	
987	998	Ctp_Perf_bkgnd_put_bk_data.Boot_Status	Wa
		» rm_Start	
988	999	Perf_Background_Dpkg.Preds_Output(Active)	
		» True	
989	1000	Perf_Background_Dpkg.Psfinalalt	
		» 0.0	
990	1001	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt	
		» 5000	
991	1002	Perf_Background_Dpkg.Psfpolfnlful	
		» 0.0	
992	1003	Perf_Background_Dpkg.Psfpolfnltme	
		» 0.0	
993	1004	Perf_Background_Dpkg.Psfpolfnltg	
		» 0.0	
994	1005	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel	
		» 40	
995	1006	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	
		» 50	
996	1007	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	
		» 60	
997	1008	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
998	1009	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
999	1010	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» False	
1000	1011	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
1001	1012	Perf_Background_Dpkg.Pcfltphase	
		» Cruise	
1002	1013	Perf_Background_Dpkg.Psfinaldes	
		» True	
1003	1014	Perf_Background_Dpkg.Vert_Auto_Mode	
		» True	
1004	1015	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	
1005	1016	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1006	1017	» 55000.0			
		Perf_background_Dpkg.Maxalt.Gwt			
		» 150000.0			
1007	1018	Perf_background_Dpkg.Maxalt.Num_Engout			
		» 0			
1008	1019	Perf_Background_Dpkg.Etp_Itin_Ran			
		» True			
1009	1020	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid			
		» False			
1010	1021	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid			
		» False			
1011	1022	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode			
		» Single			
1012	1023	Perf_Dpkg.Pstopofcrzfl(Active).Valid			
		» False			
1013	1024	Perf_Background_Dpkg.Pcitin.Itinerary			Opti
		» mum_step			
1014	1025	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst			
		» False			
1015	1026	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress			
		» False			
1016	1027	Perf_Background_Dpkg.Pcgmtime.Gpc_Time			
		» 2			
1017	1028	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt			
		» 0			
1018	1029	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq			
		» 0			
1019	1030	Perf_Background_Dpkg.Psprddataseq			
		» 3			
1020	1031	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc			
		» True			
1021	1032	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid			
		» False			
1022	1033	Change			
		» False			
1023	1034				
1024	1035				
1025	1036	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
1026	1037	-----	-----	-----	-----
		» -----			
1027	1038	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	True	(N/A)	
		» TRUE P			
1028	1039	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	False	(N/A)	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1029	1040	» FALSE P			
		Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	False	(N/A)	
1030	1041	» FALSE P			
		Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	False	(N/A)	
1031	1042	» FALSE P			
		Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	False	(N/A)	
1032	1043	» FALSE P			
		Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	False	(N/A)	
1033	1044	» 0000E+01 P	25.0	0.001	2.5
1034	1045	» 0000E+00 P	5.0	0.001	5.0
1035	1046	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	False	(N/A)	
1036	1047	» FALSE P			
		Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed	0.0	0.001	0.0
1037	1048	» 0000E+00 P			
		Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel	0.0	0.001	0.0
1038	1049	» 0000E+00 P			
		Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid	False	(N/A)	
1039	1050	» FALSE P			
		Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data	0.0	0.001	0.0
1040	1051	» 0000E+00 P			
		CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)	
1041	1052	» FALSE P			
1042	1053				
1043	1054	====> All 14 Comparisons Passed <====			
1044	1055				
1045	1056				
1046	1057	TESTID: 9			
1047	1058				
1048	1059	Itin is Manual Hold Predictions so hold predictions shall be output to CDU and LG via Put_Hm_Preds.			
1049	1060	(PERF_SDD_2436 (PERF_SRD_2071,PERF_SRD_2087_INT))			
1050	1061				
1051	1062				
1052	1063	INPUT			VALUE
1053	1064	-----			
		» -----			
1054	1065	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr			
		» 0			
1055	1066	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx			
		» 2			
1056	1067	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

	»	False
1057	1068	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec
	»	False
1058	1069	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec
	»	False
1059	1070	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec
	»	False
1060	1071	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec
	»	False
1061	1072	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec
	»	False
1062	1073	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
	»	False
1063	1074	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
	»	2
1064	1075	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
	»	0.0
1065	1076	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
	»	0.0
1066	1077	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog
	»	True
1067	1078	Perf_Background_DPkg.Opt_Step_Data.Distodest
	»	25.0
1068	1079	Perf_Background_DPkg.Opt_Step_Data.Timetogo
	»	5.0
1069	1080	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
	»	0.0
1070	1081	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
	»	0.0
1071	1082	Perf_Background_Dpkg.Pshmpreddata.Speed
	»	250.0
1072	1083	Perf_Background_Dpkg.Pshmpreddata.Fuel
	»	50.0
1073	1084	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
	»	False
1074	1085	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
	»	0.0
1075	1086	Perf_Background_Dpkg.Pcoptalt.Valid
	»	True
1076	1087	Perf_Background_Dpkg.Pcoptalt.Data
	»	19000.0
1077	1088	Fmcs_Partition_Data_Pkg.Ops_Master_Status
	»	Master
1078	1089	Ctp_Perf_bkgnd_put_bk_data.Boot_Status

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» rm_Start
1079	1090	Perf_Background_Dpkg.Preds_Output(Active)
		» True
1080	1091	Perf_Background_Dpkg.Psfinalalt
		» 0.0
1081	1092	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt
		» 5000
1082	1093	Perf_Background_Dpkg.Psfpolfnlful
		» 0.0
1083	1094	Perf_Background_Dpkg.Psfpolfnltme
		» 0.0
1084	1095	Perf_Background_Dpkg.Psfpolfnltg
		» 0.0
1085	1096	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel
		» 40
1086	1097	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time
		» 50
1087	1098	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time
		» 60
1088	1099	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done
		» True
1089	1100	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid
		» True
1090	1101	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass
		» False
1091	1102	Perf_Background_Dpkg.Pcfpln
		» tprimary
1092	1103	Perf_Background_Dpkg.Pcfltphase
		» Cruise
1093	1104	Perf_Background_Dpkg.Psfinaldes
		» True
1094	1105	Perf_Background_Dpkg.Vert_Auto_Mode
		» True
1095	1106	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data
		» 50000.0
1096	1107	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data
		» 55000.0
1097	1108	Perf_background_Dpkg.Maxalt.Gwt
		» 150000.0
1098	1109	Perf_background_Dpkg.Maxalt.Num_Engout
		» 0
1099	1110	Perf_Background_Dpkg.Etp_Itin_Ran
		» True
1100	1111	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid

Ac



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1101	1112	» False			
		Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid			
		» False			
1102	1113	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode			
		» Single			
1103	1114	Perf_Dpkg.Pstopofcrzfl(Active).Valid			
		» False			
1104	1115	Perf_Background_Dpkg.Pcitin.Itinerary			
		» Holdactv			
1105	1116	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst			
		» False			
1106	1117	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress			
		» False			
1107	1118	Perf_Background_Dpkg.Pcgmtime.Gpc_Time			
		» 2			
1108	1119	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt			
		» 0			
1109	1120	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq			
		» 0			
1110	1121	Perf_Background_Dpkg.Psprddataseq			
		» 3			
1111	1122	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc			
		» True			
1112	1123	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid			
		» False			
1113	1124	Change			
		» False			
1114	1125				
1115	1126				
1116	1127	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
1117	1128	-----	-----	-----	-----
		» -----			
1118	1129	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	True	(N/A)	
		» TRUE P			
1119	1130	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	False	(N/A)	
		» FALSE P			
1120	1131	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	False	(N/A)	
		» FALSE P			
1121	1132	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	False	(N/A)	
		» FALSE P			
1122	1133	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	False	(N/A)	
		» FALSE P			
1123	1134	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	False	(N/A)	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1124	1135	» FALSE P			
		Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest	0.0	0.001	0.0
		» 0000E+00 P			
1125	1136	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo	0.0	0.001	0.0
		» 0000E+00 P			
1126	1137	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	True	(N/A)	
		» TRUE P			
1127	1138	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed	250.0	0.001	2.5
		» 0000E+02 P			
1128	1139	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel	50.0	0.001	5.0
		» 0000E+01 P			
1129	1140	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid	False	(N/A)	
		» FALSE P			
1130	1141	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data	0.0	0.001	0.0
		» 0000E+00 P			
1131	1142	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)	
		» FALSE P			
1132	1143				
1133	1144				
1134	1145	====> All 14 Comparisons Passed <====			
1135	1146				
1136	1147				
1137	1148	TESTID: 10			
1138	1149				
1139	1150	Itin is Secprim so legs are outputed to buffers.			
1140	1151	(PERF_SDD_2631_INT,PERF_SDD_2159_INT,PERF_SDD_4543_INT,PERF_SDD_2158_INT)			
1141	1152	If the scratch flight plan is not being used, the predictions-output indication shall be set			
1142	1153	according to Table 11.14-4.			
1143	1154	In this case predictions-output is set to true			
1144	1155	(PERF_SDD_4544_INT)			
1145	1156				
1146	1157				
1147	1158	INPUT			VALUE
1148	1159	-----			
		» -----			
1149	1160	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr			
		» 0			
1150	1161	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change			
		» False			
1151	1162	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec			
		» False			
1152	1163	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec			
		» False			
1153	1164	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

	»	False
1154	1165	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec
	»	False
1155	1166	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec
	»	False
1156	1167	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
	»	False
1157	1168	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
	»	2
1158	1169	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
	»	0.0
1159	1170	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
	»	0.0
1160	1171	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog
	»	True
1161	1172	Perf_Background_DPkg.Opt_Step_Data.Distodest
	»	25.0
1162	1173	Perf_Background_DPkg.Opt_Step_Data.Timetogo
	»	5.0
1163	1174	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
	»	0.0
1164	1175	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
	»	0.0
1165	1176	Perf_Background_Dpkg.Pshmpreddata.Speed
	»	250.0
1166	1177	Perf_Background_Dpkg.Pshmpreddata.Fuel
	»	50.0
1167	1178	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
	»	False
1168	1179	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
	»	0.0
1169	1180	Perf_Background_Dpkg.Pcoptalt.Valid
	»	True
1170	1181	Perf_Background_Dpkg.Pcoptalt.Data
	»	19000.0
1171	1182	Fmcs_Partition_Data_Pkg.Ops_Master_Status
	»	Master
1172	1183	Ctp_Perf_bkgnd_put_bk_data.Boot_Status
	»	rm_Start
1173	1184	Perf_Background_Dpkg.Preds_Output(Active)
	»	True
1174	1185	Perf_Background_Dpkg.Psfinalalt
	»	0.0
1175	1186	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt

Wa

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» 5000	
1176	1187	Perf_Background_Dpkg.Psfpolfnlful	
		» 0.0	
1177	1188	Perf_Background_Dpkg.Psfpolfnltme	
		» 0.0	
1178	1189	Perf_Background_Dpkg.Psfpolfnltg	
		» 0.0	
1179	1190	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel	
		» 40	
1180	1191	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	
		» 50	
1181	1192	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	
		» 60	
1182	1193	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
1183	1194	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
1184	1195	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» False	
1185	1196	Perf_Background_Dpkg.Pcfpln	Scr
		» atchfpln	
1186	1197	Perf_Background_Dpkg.Pcfltphase	
		» Cruise	
1187	1198	Perf_Background_Dpkg.Psfinaldes	
		» True	
1188	1199	Perf_Background_Dpkg.Vert_Auto_Mode	
		» True	
1189	1200	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	
1190	1201	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 55000.0	
1191	1202	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	
1192	1203	Perf_background_Dpkg.Maxalt.Num_Engout	
		» 0	
1193	1204	Perf_Background_Dpkg.Etp_Itin_Ran	
		» True	
1194	1205	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» False	
1195	1206	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» False	
1196	1207	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
1197	1208	Perf_Dpkg.Pstopofcrzfl(Active).Valid	



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1221	1232	» 0000E+00 P		
		Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	False	(N/A)
		» FALSE P		
1222	1233	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	False	(N/A)
		» FALSE P		
1223	1234	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	False	(N/A)
		» FALSE P		
1224	1235	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	False	(N/A)
		» FALSE P		
1225	1236	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	False	(N/A)
		» FALSE P		
1226	1237	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	False	(N/A)
		» FALSE P		
1227	1238	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	True	(N/A)
		» TRUE P		
1228	1239	Perf_Background_Dpkg.Preds_Output(Active)	True	(N/A)
		» TRUE P		
1229	1240			
1230	1241			
1231	1242	====> All 14 Comparisons Passed <====		
1232	1243			
1233	1244			
1234	1245	TESTID: 11		
1235	1246			
1236	1247	Itin is Fuelplansec but preds are not outputed for display because put_block_fuel is given as pilot entered.		
1237	1248	PERF_SDD_1826(PERF_SRD_10167_INT), PERF_SDD_1831(PERF_SRD_10167_INT)		
1238	1249			
1239	1250			
1240	1251	INPUT		VALUE
1241	1252	-----		-----
		» -----		
1242	1253	Perf_Background_Dpkg.Vert_Auto_Mode		
		» True		
1243	1254	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr		
		» 0		
1244	1255	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx		
		» 2		
1245	1256	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change		
		» True		
1246	1257	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec		
		» False		
1247	1258	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec		
		» False		
1248	1259	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec		

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

	»	False
1249	1260	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec
	»	False
1250	1261	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec
	»	False
1251	1262	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
	»	False
1252	1263	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
	»	2
1253	1264	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
	»	0.0
1254	1265	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
	»	0.0
1255	1266	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog
	»	True
1256	1267	Perf_Background_DPkg.Opt_Step_Data.Distodest
	»	25.0
1257	1268	Perf_Background_DPkg.Opt_Step_Data.Timetogo
	»	5.0
1258	1269	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
	»	0.0
1259	1270	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
	»	0.0
1260	1271	Perf_Background_Dpkg.Pshmpreddata.Speed
	»	250.0
1261	1272	Perf_Background_Dpkg.Pshmpreddata.Fuel
	»	50.0
1262	1273	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
	»	False
1263	1274	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
	»	0.0
1264	1275	Perf_Background_Dpkg.Pcoptalt.Valid
	»	True
1265	1276	Perf_Background_Dpkg.Pcoptalt.Data
	»	19000.0
1266	1277	Fmcs_Partition_Data_Pkg.Ops_Master_Status
	»	Master
1267	1278	Ctp_Perf_bkgnd_put_bk_data.Boot_Status
	»	rm_Start
1268	1279	Perf_Background_Dpkg.Preds_Output(Active)
	»	True
1269	1280	Perf_Background_Dpkg.Psfinalalt
	»	0.0
1270	1281	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt

Wa

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» 5000	
1271	1282	Perf_Background_Dpkg.Psfpolfnlful	
		» 0.0	
1272	1283	Perf_Background_Dpkg.Psfpolfnltme	
		» 0.0	
1273	1284	Perf_Background_Dpkg.Psfpolfnltg	
		» 0.0	
1274	1285	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel	
		» 40	
1275	1286	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	
		» 50	
1276	1287	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	
		» 60	
1277	1288	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
1278	1289	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
1279	1290	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» False	
1280	1291	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
1281	1292	Perf_Background_Dpkg.Pcfltphase	
		» Cruise	
1282	1293	Perf_Background_Dpkg.Psfinaldes	
		» True	
1283	1294	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	
1284	1295	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 55000.0	
1285	1296	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	
1286	1297	Perf_background_Dpkg.Maxalt.Num_Engout	
		» 0	
1287	1298	Perf_Background_Dpkg.Etp_Itin_Ran	
		» True	
1288	1299	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» False	
1289	1300	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» False	
1290	1301	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
1291	1302	Perf_Dpkg.Pstopofcrzfl(Active).Valid	
		» False	
1292	1303	Perf_Background_Dpkg.Pcitin.Itinerary	Fuel



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1293	1304	» planact2			
		Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst			
		» False			
1294	1305	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress			
		» False			
1295	1306	Perf_Background_Dpkg.Pcgmtime.Gpc_Time			
		» 2			
1296	1307	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt			
		» 0			
1297	1308	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq			
		» 0			
1298	1309	Perf_Background_Dpkg.Psprddataseq			
		» 3			
1299	1310	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc			
		» True			
1300	1311	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid			
		» False			
1301	1312	Change			
		» False			
1302	1313				
1303	1314				
1304	1315	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
1305	1316	-----	-----	-----	-----
		» -----			
1306	1317	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr	0	(N/A)	
		» 0 P			
1307	1318	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	True	(N/A)	
		» TRUE P			
1308	1319	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	False	(N/A)	
		» FALSE P			
1309	1320	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	False	(N/A)	
		» FALSE P			
1310	1321	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	False	(N/A)	
		» FALSE P			
1311	1322	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	False	(N/A)	
		» FALSE P			
1312	1323	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	False	(N/A)	
		» FALSE P			
1313	1324	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	False	(N/A)	
		» FALSE P			
1314	1325	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)	
		» FALSE P			
1315	1326				

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

1316 1327
1317 1328 ===== All 9 Comparisons Passed =====
1318 1329
1319 1330
1320 1331 TESTID: 12
1321 1332
1322 1333 Itin is active primary but Src_Idx does not equal the Chk_Idx so information is not outputed.
1323 1334 (PERF_SDD_2631_INT)
1324 1335 The ETP predictions-in-progress flag shall be set to false if all of the following conditions are met
1325 1336     1)the current itinerary is the Active Primary Flight Plan Predictions
1326 1337     2)the ETP-itinerary-has-run flag is True
1327 1338 The ETP-itinerary-has-run flag is then reset to false.
1328 1339 (PERF_SDD_3155_INT)
1329 1340
1330 1341 If the current itinerary is Active Primary Flight Plan Predictions, then the last Cruise flight level
1331 1342 shall be sent to IO for output when the flight plan has been completely predicted.
1332 1343 (PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))
1333 1344
1334 1345 If the scratch flight plan is not being used, the predictions-output indication shall be set
1335 1346 according to Table 11.14-4.
1336 1347 In this case Predictions_Output is set to TRUE
1337 1348 (PERF_SDD_4544_INT)
1338 1349
1339 1350
1340 1351 INPUT
1341 1352 -----
1342 1353 » -----
1342 1353 Perf_Background_Dpkg.Pcactorsec                                Fprequestrec_Types.T
1343 1354 » temporary
1343 1354 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr
1343 1354 » 0
1344 1355 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change
1344 1355 » False
1345 1356 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec
1345 1356 » False
1346 1357 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec
1346 1357 » False
1347 1358 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec
1347 1358 » False
1348 1359 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec
1348 1359 » False
1349 1360 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec
1349 1360 » False
1350 1361 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» False
1351	1362	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
		» 2
1352	1363	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
		» 0.0
1353	1364	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
		» 0.0
1354	1365	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog
		» True
1355	1366	Perf_Background_DPkg.Opt_Step_Data.Distodest
		» 25.0
1356	1367	Perf_Background_DPkg.Opt_Step_Data.Timetogo
		» 5.0
1357	1368	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
		» 0.0
1358	1369	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
		» 0.0
1359	1370	Perf_Background_Dpkg.Pshmpreddata.Speed
		» 250.0
1360	1371	Perf_Background_Dpkg.Pshmpreddata.Fuel
		» 50.0
1361	1372	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
		» False
1362	1373	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
		» 0.0
1363	1374	Perf_Background_Dpkg.Pcoptalt.Valid
		» True
1364	1375	Perf_Background_Dpkg.Pcoptalt.Data
		» 19000.0
1365	1376	Fmcs_Partition_Data_Pkg.Ops_Master_Status
		» Master
1366	1377	Ctp_Perf_bkgnd_put_bk_data.Boot_Status
		» rm_Start
1367	1378	Perf_Background_Dpkg.Preds_Output(Active)
		» True
1368	1379	Perf_Background_Dpkg.Psfinalalt
		» 0.0
1369	1380	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt
		» 5000
1370	1381	Perf_Background_Dpkg.Psfpolfnlful
		» 0.0
1371	1382	Perf_Background_Dpkg.Psfpolfnltme
		» 0.0
1372	1383	Perf_Background_Dpkg.Psfpolfnltg

Wa

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» 0.0	
1373	1384	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel	
		» 40	
1374	1385	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	
		» 50	
1375	1386	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	
		» 60	
1376	1387	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
1377	1388	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
1378	1389	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» False	
1379	1390	Perf_Background_Dpkg.Pcfpln	Scr
		» atchFpln	
1380	1391	Perf_Background_Dpkg.Pcfltphase	
		» Cruise	
1381	1392	Perf_Background_Dpkg.Psfinaldes	
		» True	
1382	1393	Perf_Background_Dpkg.Vert_Auto_Mode	
		» True	
1383	1394	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	
1384	1395	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 55000.0	
1385	1396	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	
1386	1397	Perf_background_Dpkg.Maxalt.Num_Engout	
		» 0	
1387	1398	Perf_Background_Dpkg.Etp_Itin_Ran	
		» True	
1388	1399	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» False	
1389	1400	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» False	
1390	1401	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
1391	1402	Perf_Dpkg.Pstopofcrzfl(Active).Valid	
		» False	
1392	1403	Perf_Background_Dpkg.Pcitin.Flight_Plan	
		» Active	
1393	1404	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
1394	1405	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1395	1406	» False			
		Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress			
		» False			
1396	1407	Perf_Background_Dpkg.Pcgmtime.Gpc_Time			
		» 2			
1397	1408	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt			
		» 0			
1398	1409	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq			
		» 0			
1399	1410	Perf_Background_Dpkg.Psprddataseq			
		» 3			
1400	1411	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc			
		» True			
1401	1412	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid			
		» False			
1402	1413	Change			
		» False			
1403	1414	Chk_Idx			
		» 0			
1404	1415				
1405	1416				
1406	1417	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
1407	1418	-----	-----	-----	-----
		» -----			
1408	1419	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr	0	(N/A)	
		» 0 P			
1409	1420	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	False	(N/A)	
		» FALSE P			
1410		<del>Perf_Background_Dpkg.Etp_Itin_Ran</del>	<del>False</del>	<del>(N/A)</del>	
		<del>» TRUE FAIL</del>			
	1421	Perf_Background_Dpkg.Etp_Itin_Ran	False	(N/A)	
		» FALSE P			
1411	1422	Perf_Background_Dpkg.Psfinalalt	0.0	0.001	0.0
		» 0000E+00 P			
1412	1423	Perf_Background_Dpkg.Psfpolfnlful	0.0	0.001	0.0
		» 0000E+00 P			
1413	1424	Perf_Background_Dpkg.Psfpolfnltme	0.0	0.001	0.0
		» 0000E+00 P			
1414	1425	Perf_Background_Dpkg.Psfpolfnltg	0.0	0.001	0.0
		» 0000E+00 P			
1415	1426	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)	
		» FALSE P			
1416	1427	Perf_Background_Dpkg.Preds_Output(Active)	True	(N/A)	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1417	1428	» TRUE P		
		Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	False	(N/A)
1418	1429	» FALSE P		
		Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	False	(N/A)
1419	1430	» FALSE P		
		Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	False	(N/A)
1420	1431	» FALSE P		
1421	1432			
1422		====> 1 of 12 Comparisons Failed <====		
	1433	====> All 12 Comparisons Passed <====		
1423	1434			
1424	1435			
1425	1436	TESTID: 13		
1426	1437			
1427	1438	Itin is active primary and flight phase is descent which is after cruise so 4 different perf legs are not outputed.		
1428	1439	(PERF_SDD_2631_INT,PERF_SDD_2159_INT,PERF_SDD_4543_INT,PERF_SDD_2158_INT,		
1429	1440	PERF_SDD_2289(PERF_SRD_10253,PERF_SRD_10333_INT,PERF_SRD_12092,PERF_SRD_12093,		
1430	1441	PERF_SRD_12094,PERF_SRD_12095,PERF_SRD_9993,PERF_SRD_9994))		
1431	1442	The ETP predictions-in-progress flag shall be set to false if all of the following conditions are met		
1432	1443	1)the current itinerary is the Active Primary Flight Plan Predictions		
1433	1444	2)the ETP-itinerary-has-run flag is true		
1434	1445	The ETP-itinerary-has-run flag is then reset to false.		
1435	1446	(PERF_SDD_3155_INT)		
1436	1447			
1437	1448	If the current itinerary is Active Primary Flight Plan Predictions, then the last Cruise flight level		
1438	1449	shall be sent to IO for output when the flight plan has been completely predicted.		
1439	1450	(PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))		
1440	1451			
1441	1452	-- INPUTS:		
1442	1453			
1443	1454			
1444	1455	INPUT		VALUE
1445	1456	-----		
		» -----		
1446	1457	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr		
		» 0		
1447	1458	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change		
		» False		
1448	1459	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec		
		» False		
1449	1460	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec		
		» False		
1450	1461	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec		

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

	»	False
1451	1462	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec
	»	False
1452	1463	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec
	»	False
1453	1464	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
	»	False
1454	1465	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
	»	2
1455	1466	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
	»	0.0
1456	1467	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
	»	0.0
1457	1468	Perf_Background_DPkg.Opt_Step_Data.Distodest
	»	25.0
1458	1469	Perf_Background_DPkg.Opt_Step_Data.Timetogo
	»	5.0
1459	1470	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
	»	0.0
1460	1471	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
	»	0.0
1461	1472	Perf_Background_Dpkg.Pshmpreddata.Speed
	»	250.0
1462	1473	Perf_Background_Dpkg.Pshmpreddata.Fuel
	»	50.0
1463	1474	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
	»	False
1464	1475	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
	»	0.0
1465	1476	Perf_Background_Dpkg.Pcoptalt.Valid
	»	True
1466	1477	Perf_Background_Dpkg.Pcoptalt.Data
	»	19000.0
1467	1478	Fmcs_Partition_Data_Pkg.Ops_Master_Status
	»	Master
1468	1479	Ctp_Perf_bkgnd_put_bk_data.Boot_Status
	»	rm_Start
1469	1480	Perf_Background_Dpkg.Preds_Output(Active)
	»	True
1470	1481	Perf_Background_Dpkg.Psfinalalt
	»	0.0
1471	1482	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt
	»	5000
1472	1483	Perf_Background_Dpkg.Psfpolfnlful

Wa

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

	»	0.0
1473	1484	Perf_Background_Dpkg.Psfpolfnltme
	»	0.0
1474	1485	Perf_Background_Dpkg.Psfpolfnltg
	»	0.0
1475	1486	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel
	»	40
1476	1487	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time
	»	50
1477	1488	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time
	»	60
1478	1489	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done
	»	True
1479	1490	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid
	»	True
1480	1491	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass
	»	False
1481	1492	Perf_Background_Dpkg.Pcfpln
	»	atchfpln
1482	1493	Perf_Background_Dpkg.Pcfltphase
	»	Descent
1483	1494	Perf_Background_Dpkg.Psfinaldes
	»	True
1484	1495	Perf_Background_Dpkg.Vert_Auto_Mode
	»	True
1485	1496	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data
	»	50000.0
1486	1497	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data
	»	55000.0
1487	1498	Perf_background_Dpkg.Maxalt.Gwt
	»	150000.0
1488	1499	Perf_background_Dpkg.Maxalt.Num_Engout
	»	0
1489	1500	Perf_Background_Dpkg.Etp_Itin_Ran
	»	True
1490	1501	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid
	»	False
1491	1502	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid
	»	False
1492	1503	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode
	»	Single
1493	1504	Perf_Dpkg.Pstopofcrzfl(Active).Valid
	»	False
1494	1505	Perf_Background_Dpkg.Pcitin.Flight_Plan

Scr



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1495	1506	» Active					Prim_Fp
		Perf_Background_Dpkg.Pcitin.Itinerary					
		» ln_Preds					
1496	1507	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst					
		» False					
1497	1508	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress					
		» False					
1498	1509	Perf_Background_Dpkg.Pcgmtime.Gpc_Time					
		» 2					
1499	1510	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt					
		» 0					
1500	1511	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq					
		» 0					
1501	1512	Perf_Background_Dpkg.Psprddataseq					
		» 3					
1502	1513	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc					
		» True					
1503	1514	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid					
		» False					
1504	1515	Change					
		» False					
1505	1516	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx					
		» 2					
1506	1517	Chk_Idx					Ctp_Perf_Bkgnd_Put_Bk_Data
		» .Chk_Idx					
1507	1518	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog					
		» True					
1508	1519						
1509	1520						
1510	1521	OUTPUT	EXPECTED		TOLERANCE		ACTUAL
		» P/F					
1511	1522	-----	-----		-----		-----
		» -----					
1512	1523	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr		32	(N/A)		
		» 32 P					
1513	1524	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	False		(N/A)		
		» FALSE P					
1514		<del>Perf_Background_Dpkg.Etp_Itin_Ran</del>	<del>False</del>		<del>(N/A)</del>		
		<del>» TRUE FAIL</del>					
	1525	Perf_Background_Dpkg.Etp_Itin_Ran	False		(N/A)		
		» FALSE P					
1515	1526	Perf_Background_Dpkg.Psfinalalt	0.0		0.001		0.0
		» 0000E+00 P					
1516	1527	Perf_Background_Dpkg.Psfpolfnlful	0.0		0.001		0.0

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1517	1528	» 0000E+00 P			
		Perf_Background_Dpkg.Psfpolfnltme	0.0	0.001	0.0
1518	1529	» 0000E+00 P			
		Perf_Background_Dpkg.Psfpolfnltg	0.0	0.001	0.0
1519	1530	» 0000E+00 P			
		CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	True	(N/A)	
1520	1531	» TRUE P			
1521	1532				
1522		====> 1 of 8 Comparisons Failed <====			
	1533	====> All 8 Comparisons Passed <====			
1523	1534				
1524	1535				
1525	1536	TESTID: 14			
1526	1537				
1527	1538	Itin is active primary and Vert Auto Mode is not engaged so 2 different perf legs are not outputted.			
1528	1539	(PERF_SDD_2631_INT,PERF_SDD_2159_INT,PERF_SDD_4543_INT,PERF_SDD_2158_INT,			
1529	1540	PERF_SDD_2289(PERF_SRD_10253,PERF_SRD_10333_INT,PERF_SRD_12092,PERF_SRD_12093,			
1530	1541	PERF_SRD_12094,PERF_SRD_12095,PERF_SRD_9993,PERF_SRD_9994))			
1531	1542	The ETP predictions-in-progress flag shall be set to false if all of the following conditions are met			
1532	1543	1)the current itinerary is the Active Primary Flight Plan Predictions			
1533	1544	2)the ETP-itinerary-has-run flag is true			
1534	1545	The ETP-itinerary-has-run flag is then reset to false.			
1535	1546	(PERF_SDD_3155_INT)			
1536	1547				
1537	1548	If the current itinerary is Active Primary Flight Plan Predictions, then the last Cruise flight level			
1538	1549	shall be sent to IO for output when the flight plan has been completely predicted.			
1539	1550	(PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))			
1540	1551				
1541	1552	-- INPUTS:			
1542	1553				
1543	1554				
1544	1555	INPUT			VALUE
1545	1556	-----			
		» -----			
1546	1557	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr			
		» 0			
1547	1558	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx			
		» 2			
1548	1559	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change			
		» False			
1549	1560	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec			
		» False			
1550	1561	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» False	
1551	1562	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	
		» False	
1552	1563	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	
		» False	
1553	1564	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	
		» False	
1554	1565	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	
		» False	
1555	1566	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)	
		» 2	
1556	1567	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest	
		» 0.0	
1557	1568	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo	
		» 0.0	
1558	1569	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	
		» True	
1559	1570	Perf_Background_DPkg.Opt_Step_Data.Distodest	
		» 25.0	
1560	1571	Perf_Background_DPkg.Opt_Step_Data.Timetogo	
		» 5.0	
1561	1572	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed	
		» 0.0	
1562	1573	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel	
		» 0.0	
1563	1574	Perf_Background_Dpkg.Pshmpreddata.Speed	
		» 250.0	
1564	1575	Perf_Background_Dpkg.Pshmpreddata.Fuel	
		» 50.0	
1565	1576	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid	
		» False	
1566	1577	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data	
		» 0.0	
1567	1578	Perf_Background_Dpkg.Pcoptalt.Valid	
		» True	
1568	1579	Perf_Background_Dpkg.Pcoptalt.Data	
		» 19000.0	
1569	1580	Fmcs_Partition_Data_Pkg.Ops_Master_Status	
		» Master	
1570	1581	Ctp_Perf_bkgnd_put_bk_data.Boot_Status	Wa
		» rm_Start	
1571	1582	Perf_Background_Dpkg.Preds_Output(Active)	
		» True	
1572	1583	Perf_Background_Dpkg.Psfinalalt	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

	»	0.0
1573	1584	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt
	»	5000
1574	1585	Perf_Background_Dpkg.Psfpolfnlful
	»	0.0
1575	1586	Perf_Background_Dpkg.Psfpolfnltme
	»	0.0
1576	1587	Perf_Background_Dpkg.Psfpolfnltg
	»	0.0
1577	1588	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel
	»	40
1578	1589	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time
	»	50
1579	1590	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time
	»	60
1580	1591	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done
	»	True
1581	1592	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid
	»	True
1582	1593	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass
	»	False
1583	1594	Perf_Background_Dpkg.Pcfpln
	»	atchfpln
1584	1595	Perf_Background_Dpkg.Pcfltphase
	»	Cruise
1585	1596	Perf_Background_Dpkg.Psfinaldes
	»	True
1586	1597	Perf_Background_Dpkg.Vert_Auto_Mode
	»	False
1587	1598	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data
	»	50000.0
1588	1599	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data
	»	55000.0
1589	1600	Perf_background_Dpkg.Maxalt.Gwt
	»	150000.0
1590	1601	Perf_background_Dpkg.Maxalt.Num_Engout
	»	0
1591	1602	Perf_Background_Dpkg.Etp_Itin_Ran
	»	True
1592	1603	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid
	»	False
1593	1604	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid
	»	False
1594	1605	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode

Scr

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1595	1606	» Single			
		Perf_Dpkg.Pstopofcrzfl(Active).Valid			
		» False			
1596	1607	Perf_Background_Dpkg.Pcitin.Flight_Plan			
		» Active			
1597	1608	Perf_Background_Dpkg.Pcitin.Itinerary			Prim_Fp
		» ln_Preds			
1598	1609	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst			
		» False			
1599	1610	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress			
		» False			
1600	1611	Perf_Background_Dpkg.Pcgmtime.Gpc_Time			
		» 2			
1601	1612	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt			
		» 0			
1602	1613	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq			
		» 0			
1603	1614	Perf_Background_Dpkg.Psprddataseq			
		» 3			
1604	1615	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc			
		» True			
1605	1616	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid			
		» False			
1606	1617	Change			
		» False			
1607	1618	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx			
		» 2			
1608	1619	Chk_Idx			Ctp_Perf_Bkgnd_Put_Bk_Data
		» .Chk_Idx			
1609	1620	Src_Idx			
		» 1			
1610	1621	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog			
		» True			
1611	1622				
1612	1623				
1613	1624	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
1614	1625	-----	-----	-----	-----
		» -----			
1615	1626	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr	34	(N/A)	
		» 34 P			
1616	1627	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	False	(N/A)	
		» FALSE P			
1617		<del>Perf_Background_Dpkg.Etp_Itin_Ran</del>	<del>False</del>	<del>(N/A)</del>	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» <del>TRUE</del> <del>FAIL</del>		
	1628	Perf_Background_Dpkg.Etp_Itin_Ran	False	(N/A)
		» FALSE P		
1618	1629	Perf_Background_Dpkg.Psfinalalt	0.0	0.001
		» 0000E+00 P		
1619	1630	Perf_Background_Dpkg.Psfpolfnlful	0.0	0.001
		» 0000E+00 P		
1620	1631	Perf_Background_Dpkg.Psfpolfnltme	0.0	0.001
		» 0000E+00 P		
1621	1632	Perf_Background_Dpkg.Psfpolfnltg	0.0	0.001
		» 0000E+00 P		
1622	1633	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	True	(N/A)
		» TRUE P		
1623	1634			
1624	1635			
1625		====> 1 of 8 Comparisons Failed <====		
	1636	====> All 8 Comparisons Passed <====		
1626	1637			
1627	1638			
1628	1639	TESTID: 15		
1629	1640			
1630	1641	Itin is active primary but preds are invalidated during processing. Processing terminates at this point.		
1631	1642	(PERF_SDD_2632_INT,PERF_SDD_4543_INT)		
1632	1643			
1633	1644			
1634	1645	INPUT		VALUE
1635	1646	-----		
		» -----		
1636	1647	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr		
		» 0		
1637	1648	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx		
		» 2		
1638	1649	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change		
		» False		
1639	1650	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec		
		» False		
1640	1651	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec		
		» False		
1641	1652	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec		
		» False		
1642	1653	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec		
		» False		
1643	1654	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec		
		» False		

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

1644 1655 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
      » False
1645 1656 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
      » 2
1646 1657 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
      » 0.0
1647 1658 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
      » 0.0
1648 1659 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog
      » True
1649 1660 Perf_Background_DPkg.Opt_Step_Data.Distodest
      » 25.0
1650 1661 Perf_Background_DPkg.Opt_Step_Data.Timetogo
      » 5.0
1651 1662 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
      » 0.0
1652 1663 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
      » 0.0
1653 1664 Perf_Background_Dpkg.Pshmpreddata.Speed
      » 250.0
1654 1665 Perf_Background_Dpkg.Pshmpreddata.Fuel
      » 50.0
1655 1666 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
      » False
1656 1667 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
      » 0.0
1657 1668 Perf_Background_Dpkg.Pcoptalt.Valid
      » True
1658 1669 Perf_Background_Dpkg.Pcoptalt.Data
      » 19000.0
1659 1670 Fmcs_Partition_Data_Pkg.Ops_Master_Status
      » Master
1660 1671 Ctp_Perf_bkgnd_put_bk_data.Boot_Status
      » rm_Start
1661 1672 Perf_Background_Dpkg.Preds_Output(Active)
      » True
1662 1673 Perf_Background_Dpkg.Psfinalalt
      » 0.0
1663 1674 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt
      » 5000
1664 1675 Perf_Background_Dpkg.Psfpolfnlful
      » 0.0
1665 1676 Perf_Background_Dpkg.Psfpolfnltme
      » 0.0

```

Wa

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1666	1677	Perf_Background_Dpkg.Psfpolfnltg
		» 0.0
1667	1678	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel
		» 40
1668	1679	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time
		» 50
1669	1680	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time
		» 60
1670	1681	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done
		» True
1671	1682	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid
		» True
1672	1683	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass
		» False
1673	1684	Perf_Background_Dpkg.Pcfpln
		» atchfpln
1674	1685	Perf_Background_Dpkg.Pcfltphase
		» Cruise
1675	1686	Perf_Background_Dpkg.Psfinaldes
		» True
1676	1687	Perf_Background_Dpkg.Vert_Auto_Mode
		» True
1677	1688	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data
		» 50000.0
1678	1689	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data
		» 55000.0
1679	1690	Perf_background_Dpkg.Maxalt.Gwt
		» 150000.0
1680	1691	Perf_background_Dpkg.Maxalt.Num_Engout
		» 0
1681	1692	Perf_Background_Dpkg.Etp_Itin_Ran
		» True
1682	1693	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid
		» False
1683	1694	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid
		» False
1684	1695	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode
		» Single
1685	1696	Perf_Dpkg.Pstopofcrzfl(Active).Valid
		» False
1686	1697	Perf_Background_Dpkg.Pcitin.Flight_Plan
		» Active
1687	1698	Perf_Background_Dpkg.Pcitin.Itinerary
		» ln_Preds

Scr

Prim\_Fp



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1688	1699	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst			
		» True			
1689	1700	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress			
		» False			
1690	1701	Perf_Background_Dpkg.Pcgmtime.Gpc_Time			
		» 2			
1691	1702	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt			
		» 0			
1692	1703	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq			
		» 0			
1693	1704	Perf_Background_Dpkg.Psprddataseq			
		» 3			
1694	1705	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc			
		» True			
1695	1706	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid			
		» False			
1696	1707	Change			
		» False			
1697	1708				
1698	1709				
1699	1710	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
1700	1711	-----	-----	-----	-----
		» -----			
1701	1712	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr	0	(N/A)	
		» 0 P			
1702	1713	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	True	(N/A)	
		» TRUE P			
1703	1714	Perf_Background_Dpkg.Psfinalalt	0.0	0.001	0.0
		» 0000E+00 P			
1704	1715	Perf_Background_Dpkg.Psfpolfnlful	0.0	0.001	0.0
		» 0000E+00 P			
1705	1716	Perf_Background_Dpkg.Psfpolfnltme	0.0	0.001	0.0
		» 0000E+00 P			
1706	1717	Perf_Background_Dpkg.Psfpolfnltg	0.0	0.001	0.0
		» 0000E+00 P			
1707	1718	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	False	(N/A)	
		» FALSE P			
1708	1719	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	False	(N/A)	
		» FALSE P			
1709	1720	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	False	(N/A)	
		» FALSE P			
1710	1721	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	False	(N/A)	
		» FALSE P			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1711	1722	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	False	(N/A)
		» FALSE P		
1712	1723	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	False	(N/A)
		» FALSE P		
1713	1724	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)
		» FALSE P		
1714	1725			
1715	1726			
1716	1727	====> All 13 Comparisons Passed <====		
1717	1728			
1718	1729			
1719	1730	TESTID: 16		
1720	1731			
1721	1732	Itin is active primary but preds are invalidated during processing. Processing terminates at this point.		
1722	1733	(PERF_SDD_2632_INT,PERF_SDD_4543_INT)		
1723	1734	The predictions-output indication boolean, which indicates the successful copy of the Scratch LGB and Scratch		
1724	1735	Perf Buffers to their appropriate reference buffers, shall be set according to Table 11.14-3		
1725	1736	In this case predictions-output indication is set to false as the prediction interruptions.		
1726	1737	(PERF_SDD_3752_INT)		
1727	1738			
1728	1739			
1729	1740	INPUT		VALUE
1730	1741	-----		-----
		» -----		
1731	1742	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr		
		» 0		
1732	1743	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx		
		» 2		
1733	1744	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change		
		» False		
1734	1745	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec		
		» False		
1735	1746	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec		
		» False		
1736	1747	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec		
		» False		
1737	1748	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec		
		» False		
1738	1749	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec		
		» False		
1739	1750	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec		
		» False		
1740	1751	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)		
		» 2		

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1741	1752	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
		» 0.0
1742	1753	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
		» 0.0
1743	1754	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog
		» True
1744	1755	Perf_Background_DPkg.Opt_Step_Data.Distodest
		» 25.0
1745	1756	Perf_Background_DPkg.Opt_Step_Data.Timetogo
		» 5.0
1746	1757	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
		» 0.0
1747	1758	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
		» 0.0
1748	1759	Perf_Background_Dpkg.Pshmpreddata.Speed
		» 250.0
1749	1760	Perf_Background_Dpkg.Pshmpreddata.Fuel
		» 50.0
1750	1761	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
		» False
1751	1762	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
		» 0.0
1752	1763	Perf_Background_Dpkg.Pcoptalt.Valid
		» True
1753	1764	Perf_Background_Dpkg.Pcoptalt.Data
		» 19000.0
1754	1765	Fmcs_Partition_Data_Pkg.Ops_Master_Status
		» Master
1755	1766	Ctp_Perf_bkgnd_put_bk_data.Boot_Status
		» rm_Start
1756	1767	Perf_Background_Dpkg.Preds_Output(Active)
		» False
1757	1768	Perf_Background_Dpkg.Psfinalalt
		» 0.0
1758	1769	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt
		» 5000
1759	1770	Perf_Background_Dpkg.Psfpolfnlful
		» 0.0
1760	1771	Perf_Background_Dpkg.Psfpolfnltme
		» 0.0
1761	1772	Perf_Background_Dpkg.Psfpolfnltg
		» 0.0
1762	1773	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel
		» 40

Wa

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1763	1774	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	
		» 50	
1764	1775	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	
		» 60	
1765	1776	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
1766	1777	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
1767	1778	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» False	
1768	1779	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
1769	1780	Perf_Background_Dpkg.Pcfltphase	
		» Cruise	
1770	1781	Perf_Background_Dpkg.Psfinaldes	
		» True	
1771	1782	Perf_Background_Dpkg.Vert_Auto_Mode	
		» True	
1772	1783	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	
1773	1784	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 55000.0	
1774	1785	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	
1775	1786	Perf_background_Dpkg.Maxalt.Num_Engout	
		» 0	
1776	1787	Perf_Background_Dpkg.Etp_Itin_Ran	
		» True	
1777	1788	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» False	
1778	1789	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» False	
1779	1790	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
1780	1791	Perf_Dpkg.Pstopofcrzfl(Active).Valid	
		» False	
1781	1792	Perf_Background_Dpkg.Pcitin.Flight_Plan	
		» Active	
1782	1793	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
1783	1794	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst	
		» True	
1784	1795	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress	
		» False	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1785	1796	Perf_Background_Dpkg.Pcgmtime.Gpc_Time			
		» 2			
1786	1797	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt			
		» 0			
1787	1798	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq			
		» 0			
1788	1799	Perf_Background_Dpkg.Psprddataseq			
		» 3			
1789	1800	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc			
		» True			
1790	1801	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid			
		» False			
1791	1802	Change			
		» False			
1792	1803				
1793	1804				
1794	1805	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
1795	1806	-----	-----	-----	-----
		» -----			
1796	1807	Perf_Background_Dpkg.Preds_Output(Active)	False	(N/A)	
		» FALSE P			
1797	1808	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr	0	(N/A)	
		» 0 P			
1798	1809	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	True	(N/A)	
		» TRUE P			
1799	1810	Perf_Background_Dpkg.Psfinalalt	0.0	0.001	0.0
		» 0000E+00 P			
1800	1811	Perf_Background_Dpkg.Psfpolfnlful	0.0	0.001	0.0
		» 0000E+00 P			
1801	1812	Perf_Background_Dpkg.Psfpolfnltme	0.0	0.001	0.0
		» 0000E+00 P			
1802	1813	Perf_Background_Dpkg.Psfpolfnltg	0.0	0.001	0.0
		» 0000E+00 P			
1803	1814	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	False	(N/A)	
		» FALSE P			
1804	1815	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	False	(N/A)	
		» FALSE P			
1805	1816	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	False	(N/A)	
		» FALSE P			
1806	1817	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	False	(N/A)	
		» FALSE P			
1807	1818	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	False	(N/A)	
		» FALSE P			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1808	1819	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	False	(N/A)
		» FALSE P		
1809	1820	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)
		» FALSE P		
1810	1821			
1811	1822			
1812	1823	====> All 14 Comparisons Passed <====		
1813	1824			
1814	1825			
1815	1826	TESTID: 17		
1816	1827			
1817	1828	Itin is active primary and flight phase is climb which is before cruise so 3 different perf legs are not outputed.		
1818	1829	(PERF_SDD_2631_INT,PERF_SDD_4543_INT,PERF_SDD_2159_INT,PERF_SDD_2158_INT,		
1819	1830	PERF_SDD_2289(PERF_SRD_10253,PERF_SRD_10333_INT,PERF_SRD_12092,PERF_SRD_12093,		
1820	1831	PERF_SRD_12094,PERF_SRD_12095,PERF_SRD_9993,PERF_SRD_9994))		
1821	1832	The predictions-output indication boolean, which indicates the successful copy of the Scratch LGB and Scratch		
1822	1833	Perf Buffers to their appropriate reference buffers, shall be set according to Table 11.14-3		
1823	1834	In this case predictions-output indication is set to True.		
1824	1835	(PERF_SDD_3752_INT)		
1825	1836	The ETP predictions-in-progress flag shall be set to false if all of the following conditions are met		
1826	1837	1)the current itinerary is the Active Primary Flight Plan Predictions		
1827	1838	2)the ETP-itinerary-has-run flag is true		
1828	1839	The ETP-itinerary-has-run flag is then reset to false.		
1829	1840	(PERF_SDD_3155_INT)		
1830	1841			
1831	1842	If the current itinerary is Active Primary Flight Plan Predictions, then the last Cruise flight level		
1832	1843	shall be sent to IO for output when the flight plan has been completely predicted.		
1833	1844	(PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))		
1834	1845			
1835	1846	-- INPUTS:		
1836	1847			
1837	1848			
1838	1849	INPUT		VALUE
1839	1850	-----		
		» -----		
1840	1851	Perf_Background_Dpkg.Pcactorsec		
		» Active		
1841	1852	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr		
		» 0		
1842	1853	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change		
		» False		
1843	1854	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec		
		» False		
1844	1855	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec		

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» False	
1845	1856	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	
		» False	
1846	1857	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	
		» False	
1847	1858	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	
		» False	
1848	1859	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	
		» False	
1849	1860	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)	
		» 2	
1850	1861	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest	
		» 0.0	
1851	1862	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo	
		» 0.0	
1852	1863	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	
		» True	
1853	1864	Perf_Background_DPkg.Opt_Step_Data.Distodest	
		» 25.0	
1854	1865	Perf_Background_DPkg.Opt_Step_Data.Timetogo	
		» 5.0	
1855	1866	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed	
		» 0.0	
1856	1867	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel	
		» 0.0	
1857	1868	Perf_Background_Dpkg.Pshmpreddata.Speed	
		» 250.0	
1858	1869	Perf_Background_Dpkg.Pshmpreddata.Fuel	
		» 50.0	
1859	1870	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid	
		» False	
1860	1871	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data	
		» 0.0	
1861	1872	Perf_Background_Dpkg.Pcoptalt.Valid	
		» True	
1862	1873	Perf_Background_Dpkg.Pcoptalt.Data	
		» 19000.0	
1863	1874	Fmcs_Partition_Data_Pkg.Ops_Master_Status	
		» Master	
1864	1875	Ctp_Perf_bkgnd_put_bk_data.Boot_Status	Wa
		» rm_Start	
1865	1876	Perf_Background_Dpkg.Preds_Output(Active)	
		» False	
1866	1877	Perf_Background_Dpkg.Psfinalalt	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

	»	0.0
1867	1878	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt
	»	5000
1868	1879	Perf_Background_Dpkg.Psfpolfnlful
	»	0.0
1869	1880	Perf_Background_Dpkg.Psfpolfnltme
	»	0.0
1870	1881	Perf_Background_Dpkg.Psfpolfnltg
	»	0.0
1871	1882	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel
	»	40
1872	1883	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time
	»	50
1873	1884	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time
	»	60
1874	1885	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done
	»	True
1875	1886	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid
	»	True
1876	1887	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass
	»	False
1877	1888	Perf_Background_Dpkg.Pcfpln
	»	atchfpln
1878	1889	Perf_Background_Dpkg.Pcfltphase
	»	Climb
1879	1890	Perf_Background_Dpkg.Psfinaldes
	»	True
1880	1891	Perf_Background_Dpkg.Vert_Auto_Mode
	»	True
1881	1892	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data
	»	50000.0
1882	1893	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data
	»	55000.0
1883	1894	Perf_background_Dpkg.Maxalt.Gwt
	»	150000.0
1884	1895	Perf_background_Dpkg.Maxalt.Num_Engout
	»	0
1885	1896	Perf_Background_Dpkg.Etp_Itin_Ran
	»	True
1886	1897	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid
	»	False
1887	1898	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid
	»	False
1888	1899	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode

Scr



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1889	1900	» Single			
		Perf_Dpkg.Pstopofcrzfl(Active).Valid			
		» False			
1890	1901	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst			
		» False			
1891	1902	Perf_Background_Dpkg.Pcitin.Flight_Plan			
		» Active			
1892	1903	Perf_Background_Dpkg.Pcitin.Itinerary			Prim_Fp
		» ln_Preds			
1893	1904	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress			
		» False			
1894	1905	Perf_Background_Dpkg.Pcgmtime.Gpc_Time			
		» 2			
1895	1906	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt			
		» 0			
1896	1907	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq			
		» 0			
1897	1908	Perf_Background_Dpkg.Psprddataseq			
		» 3			
1898	1909	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc			
		» True			
1899	1910	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid			
		» False			
1900	1911	Change			
		» False			
1901	1912	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx			
		» 2			
1902	1913	Chk_Idx			Ctp_Perf_Bkgnd_Put_Bk_Data
		» .Chk_Idx			
1903	1914				
1904	1915				
1905	1916	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
1906	1917	-----	-----	-----	-----
		» -----			
1907	1918	Perf_Background_Dpkg.Preds_Output(Active)	True	(N/A)	
		» TRUE P			
1908	1919	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr	33	(N/A)	
		» 33 P			
1909	1920	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	False	(N/A)	
		» FALSE P			
1910		<del>Perf_Background_Dpkg.Etp_Itin_Ran</del>	<del>False</del>	<del>(N/A)</del>	
		<del>» TRUE FAIL</del>			
	1921	Perf_Background_Dpkg.Etp_Itin_Ran	False	(N/A)	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» FALSE P			
1911	1922	Perf_Background_Dpkg.Psfinalalt	0.0	0.001	0.0
		» 0000E+00 P			
1912	1923	Perf_Background_Dpkg.Psfpolfnlful	0.0	0.001	0.0
		» 0000E+00 P			
1913	1924	Perf_Background_Dpkg.Psfpolfnltme	0.0	0.001	0.0
		» 0000E+00 P			
1914	1925	Perf_Background_Dpkg.Psfpolfnltg	0.0	0.001	0.0
		» 0000E+00 P			
1915	1926	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	True	(N/A)	
		» TRUE P			
1916	1927				
1917	1928				
1918		<del>====&gt; 1 of 9 Comparisons Failed &lt;====</del>			
	1929	====> All 9 Comparisons Passed <====			
1919	1930				
1920	1931				
1921	1932	TESTID: 18			
1922	1933				
1923	1934	Initialization occurs for a cold start. Also, itin is active preds and no change occurs that causes an interruption o			
		» n			
1924	1935	preds.Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest checked for Alternate option.			
1925	1936	(PERF_SDD_2094_INT)			
1926	1937	Options_And_Data_Pkg.Fuel_Pred_Final_Dest is equal to "A" and Perf_Background_Dpkg.Pcfinaldest is set to Alternate.			
1927	1938	(PERF_SDD_5614_DR(PERF_SRD_1544_A3XX, PERF_SRD_7463))			
1928	1939				
1929	1940				
1930	1941	INPUT			VALUE
1931	1942	-----			
		» -----			
1932	1943	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr			
		» 0			
1933	1944	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx			
		» 2			
1934	1945	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change			
		» False			
1935	1946	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec			
		» False			
1936	1947	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec			
		» False			
1937	1948	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec			
		» False			
1938	1949	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec			
		» False			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1939	1950	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec
		» False
1940	1951	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
		» False
1941	1952	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
		» 2
1942	1953	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
		» 0.0
1943	1954	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
		» 0.0
1944	1955	Perf_Background_DPkg.Opt_Step_Data.Distodest
		» 25.0
1945	1956	Perf_Background_DPkg.Opt_Step_Data.Timetogo
		» 5.0
1946	1957	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
		» 0.0
1947	1958	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
		» 0.0
1948	1959	Perf_Background_Dpkg.Pshmpreddata.Speed
		» 250.0
1949	1960	Perf_Background_Dpkg.Pshmpreddata.Fuel
		» 50.0
1950	1961	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
		» False
1951	1962	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
		» 0.0
1952	1963	Perf_Background_Dpkg.Pcoptalt.Valid
		» True
1953	1964	Perf_Background_Dpkg.Pcoptalt.Data
		» 19000.0
1954	1965	Fmcs_Partition_Data_Pkg.Ops_Master_Status
		» Master
1955	1966	Ctp_Perf_bkgnd_put_bk_data.Boot_Status
		» ld_Start
1956	1967	Perf_Background_Dpkg.Preds_Output(Active)
		» True
1957	1968	Perf_Background_Dpkg.Psfinalalt
		» 0.0
1958	1969	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt
		» 5000
1959	1970	Perf_Background_Dpkg.Psfpolfnlful
		» 0.0
1960	1971	Perf_Background_Dpkg.Psfpolfnltme
		» 0.0

Co

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1961	1972	Perf_Background_Dpkg.Psfpolfnltg	
		» 0.0	
1962	1973	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel	
		» 40	
1963	1974	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	
		» 50	
1964	1975	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	
		» 60	
1965	1976	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Percent	
		» 100.0	
1966	1977	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Upper_Limit	
		» 4.0	
1967	1978	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Lower_Limit	
		» 1.0	
1968	1979	Options_And_Data_Pkg:body.All_Options.Ats_Enable	
		» True	
1969	1980	Options_And_Data_Pkg:body.All_Options.Alt_Trip_In_Rsv_Enb	
		» True	
1970	1981	Options_And_Data_Pkg:body.All_Options.Cmp_Rsv_In_Flt_Enb	
		» True	
1971	1982	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
1972	1983	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
1973	1984	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» True	
1974	1985	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
1975	1986	Perf_Background_Dpkg.Pcfltphase	P
		» reflight	
1976	1987	Perf_Background_Dpkg.Psfinaldes	
		» True	
1977	1988	Perf_Background_Dpkg.Vert_Auto_Mode	
		» True	
1978	1989	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	
1979	1990	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 55000.0	
1980	1991	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	
1981	1992	Perf_background_Dpkg.Maxalt.Num_Engout	
		» 0	
1982	1993	Perf_Background_Dpkg.Etp_Itin_Ran	
		» True	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

1983	1994	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» False	
1984	1995	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» False	
1985	1996	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
1986	1997	Perf_Dpkg.Pstopofcrzfl(Active).Valid	
		» False	
1987	1998	Perf_Background_Dpkg.Pcitin.Flight_Plan	
		» Active	
1988	1999	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
1989	2000	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfregst	
		» False	
1990	2001	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress	
		» False	
1991	2002	Perf_Background_Dpkg.Pcgmtime.Gpc_Time	
		» 2	
1992	2003	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	
		» 0	
1993	2004	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq	
		» 0	
1994	2005	Perf_Background_Dpkg.Psprddataseq	
		» 3	
1995	2006	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc	
		» True	
1996	2007	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	
		» False	
1997	2008	Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest	
		» "A"	
1998	2009	Perf_Background_Dpkg.Ats_Enable	
		» False	
1999	2010	Perf_Background_Dpkg.Psrsvaltn	
		» False	
2000	2011	Perf_Background_Dpkg.Psrsvinflt	
		» False	
2001	2012	Perf_Background_Dpkg.Psrtersvpctg	
		» 0.0	
2002	2013	Perf_Background_Dpkg.Psmaxrtersv	
		» 0.0	
2003	2014	Perf_Background_Dpkg.Psminrtersv	
		» 0.0	
2004	2015	Perf_Background_Dpkg.Pcfinaldest	Perf_Ext_Tpkg
		» .Primary	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2005	2016	Change			
		» True			
2006	2017	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog			
		» True			
2007	2018				
2008	2019				
2009	2020	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
2010	2021	-----	-----	-----	-----
		» -----			
2011	2022	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr	0	(N/A)	
		» 0 P			
2012	2023	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	False	(N/A)	
		» FALSE P			
2013	2024	Perf_Background_Dpkg.Psfinalalt	5000.0	0.001	5.0
		» 0000E+03 P			
2014	2025	Perf_Background_Dpkg.Psfpolfnlful	40.0	0.001	4.0
		» 0000E+01 P			
2015	2026	Perf_Background_Dpkg.Psfpolfnltme	50.0	0.001	5.0
		» 0000E+01 P			
2016	2027	Perf_Background_Dpkg.Psfpolfnltg	60.0	0.001	6.0
		» 0000E+01 P			
2017	2028	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	False	(N/A)	
		» FALSE P			
2018	2029	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	False	(N/A)	
		» FALSE P			
2019	2030	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	False	(N/A)	
		» FALSE P			
2020	2031	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	False	(N/A)	
		» FALSE P			
2021	2032	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	False	(N/A)	
		» FALSE P			
2022	2033	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	False	(N/A)	
		» FALSE P			
2023	2034	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)	
		» FALSE P			
2024	2035	Perf_Background_Dpkg.Ats_Enable	True	(N/A)	
		» TRUE P			
2025	2036	Perf_Background_Dpkg.Psrsvalt	True	(N/A)	
		» TRUE P			
2026	2037	Perf_Background_Dpkg.Psrsvinflt	True	(N/A)	
		» TRUE P			
2027	2038	Perf_Background_Dpkg.Pcfinaldest	Alternate	(N/A)	A
		» LTERNATE P			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

2028 2039
2029 2040
2030 2041 ===== All 17 Comparisons Passed =====
2031 2042
2032 2043
2033 2044 TESTID: 19
2034 2045
2035 2046 Initialization occurs for a cold start. Also, itin is active preds and no change occurs that causes an interruption o
» n
2036 2047 preds.Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest checked for other than Primary and Alternate option.
2037 2048 (PERF_SDD_2094_INT)
2038 2049 Options_And_Data_Pkg.Fuel_Pred_Final_Dest is other than "P" and "A" and Perf_Background_Dpkg.Pcfinaldest is set to Alt
» ernate.
2039 2050 (PERF_SDD_5614_DR(PERF_SRD_1544_A3XX, PERF_SRD_7463))
2040 2051
2041 2052
2042 2053 INPUT VALUE
2043 2054 -----
» -----
2044 2055 Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr
» 0
2045 2056 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx
» 2
2046 2057 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change
» False
2047 2058 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec
» False
2048 2059 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec
» False
2049 2060 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec
» False
2050 2061 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec
» False
2051 2062 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec
» False
2052 2063 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
» False
2053 2064 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
» 2
2054 2065 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
» 0.0
2055 2066 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
» 0.0
2056 2067 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» True	
2057	2068	Perf_Background_DPkg.Opt_Step_Data.Distodest	
		» 25.0	
2058	2069	Perf_Background_DPkg.Opt_Step_Data.Timetogo	
		» 5.0	
2059	2070	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed	
		» 0.0	
2060	2071	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel	
		» 0.0	
2061	2072	Perf_Background_Dpkg.Pshmpreddata.Speed	
		» 250.0	
2062	2073	Perf_Background_Dpkg.Pshmpreddata.Fuel	
		» 50.0	
2063	2074	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid	
		» False	
2064	2075	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data	
		» 0.0	
2065	2076	Perf_Background_Dpkg.Pcoptalt.Valid	
		» True	
2066	2077	Perf_Background_Dpkg.Pcoptalt.Data	
		» 19000.0	
2067	2078	Fmcs_Partition_Data_Pkg.Ops_Master_Status	
		» Master	
2068	2079	Ctp_Perf_bkgnd_put_bk_data.Boot_Status	Co
		» ld_Start	
2069	2080	Perf_Background_Dpkg.Preds_Output(Active)	
		» True	
2070	2081	Perf_Background_Dpkg.Psfinalalt	
		» 0.0	
2071	2082	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt	
		» 5000	
2072	2083	Perf_Background_Dpkg.Psfpolfnlful	
		» 0.0	
2073	2084	Perf_Background_Dpkg.Psfpolfnltme	
		» 0.0	
2074	2085	Perf_Background_Dpkg.Psfpolfnltg	
		» 0.0	
2075	2086	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel	
		» 40	
2076	2087	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	
		» 50	
2077	2088	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	
		» 60	
2078	2089	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Percent	



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» 100.0	
2079	2090	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Upper_Limit	
		» 4.0	
2080	2091	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Lower_Limit	
		» 1.0	
2081	2092	Options_And_Data_Pkg:body.All_Options.Ats_Enable	
		» True	
2082	2093	Options_And_Data_Pkg:body.All_Options.Alt_Trip_In_Rsv_Enb	
		» True	
2083	2094	Options_And_Data_Pkg:body.All_Options.Cmp_Rsv_In_Flt_Enb	
		» True	
2084	2095	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
2085	2096	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
2086	2097	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» True	
2087	2098	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
2088	2099	Perf_Background_Dpkg.Pcfltphase	P
		» reflight	
2089	2100	Perf_Background_Dpkg.Psfinaldes	
		» True	
2090	2101	Perf_Background_Dpkg.Vert_Auto_Mode	
		» True	
2091	2102	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	
2092	2103	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 55000.0	
2093	2104	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	
2094	2105	Perf_background_Dpkg.Maxalt.Num_Engout	
		» 0	
2095	2106	Perf_Background_Dpkg.Etp_Itin_Ran	
		» True	
2096	2107	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» False	
2097	2108	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» False	
2098	2109	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
2099	2110	Perf_Dpkg.Pstopofcrzfl(Active).Valid	
		» False	
2100	2111	Perf_Background_Dpkg.Pcitin.Flight_Plan	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2101	2112	» Active					Prim_Fp
		Perf_Background_Dpkg.Pcitin.Itinerary					
		» ln_Preds					
2102	2113	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst					
		» False					
2103	2114	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress					
		» False					
2104	2115	Perf_Background_Dpkg.Pcgmtime.Gpc_Time					
		» 2					
2105	2116	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt					
		» 0					
2106	2117	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq					
		» 0					
2107	2118	Perf_Background_Dpkg.Psprddataseq					
		» 3					
2108	2119	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc					
		» True					
2109	2120	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid					
		» False					
2110	2121	Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest					
		» "I"					
2111	2122	Perf_Background_Dpkg.Ats_Enable					
		» False					
2112	2123	Perf_Background_Dpkg.Psrsvaltn					
		» False					
2113	2124	Perf_Background_Dpkg.Psrsvinflt					
		» False					
2114	2125	Perf_Background_Dpkg.Psrtersvpctg					
		» 0.0					
2115	2126	Perf_Background_Dpkg.Psmxrtersv					
		» 0.0					
2116	2127	Perf_Background_Dpkg.Psminrtersv					
		» 0.0					
2117	2128	Perf_Background_Dpkg.Pcfinaldest					Perf_Ext_Tpkg
		» .Primary					
2118	2129						
2119	2130						
2120	2131	OUTPUT	EXPECTED		TOLERANCE		ACTUAL
		» P/F					
2121	2132	-----	-----		-----		-----
		» -----					
2122	2133	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr		0	(N/A)		
		» 0 P					
2123	2134	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	False		(N/A)		

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2124	2135	» FALSE P			
		Perf_Background_Dpkg.Psfinalalt	5000.0	0.001	5.0
		» 0000E+03 P			
2125	2136	Perf_Background_Dpkg.Psfpolfnlful	40.0	0.001	4.0
		» 0000E+01 P			
2126	2137	Perf_Background_Dpkg.Psfpolfnltme	50.0	0.001	5.0
		» 0000E+01 P			
2127	2138	Perf_Background_Dpkg.Psfpolfnltg	60.0	0.001	6.0
		» 0000E+01 P			
2128	2139	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	False	(N/A)	
		» FALSE P			
2129	2140	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	False	(N/A)	
		» FALSE P			
2130	2141	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	False	(N/A)	
		» FALSE P			
2131	2142	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	False	(N/A)	
		» FALSE P			
2132	2143	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	False	(N/A)	
		» FALSE P			
2133	2144	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	False	(N/A)	
		» FALSE P			
2134	2145	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)	
		» FALSE P			
2135	2146	Perf_Background_Dpkg.Ats_Enable	True	(N/A)	
		» TRUE P			
2136	2147	Perf_Background_Dpkg.Psrsvaltn	True	(N/A)	
		» TRUE P			
2137	2148	Perf_Background_Dpkg.Psrsvinflt	True	(N/A)	
		» TRUE P			
2138	2149	Perf_Background_Dpkg.Psrtersvpctg	1.0	0.001	1.0
		» 0000E+00 P			
2139	2150	Perf_Background_Dpkg.Psmaxrtersv	4.0	0.001	4.0
		» 0000E+00 P			
2140	2151	Perf_Background_Dpkg.Psminrtersv	1.0	0.001	1.0
		» 0000E+00 P			
2141	2152	Perf_Background_Dpkg.Pcfinaldest	Perf_Ext_Tpkg.Alternate	(N/A)	A
		» LTERNATE P			
2142	2153				
2143	2154				
2144	2155	====> All 20 Comparisons Passed <====			
2145	2156				
2146	2157				
2147	2158	TESTID: 20			
2148	2159				

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2149	2160	Time Constraint Processing :	
2150	2161	Cost Index computation is for Active fpln TIME CSTR.	
2151	2162	Performance Cost index is ready for release to the system, the RTA working and control data have been output	
2152	2163	through the Perf RTA object manager.	
2153	2164	(PERF_SDD_3520_INT).	
2154	2165	Time Constraint Control data is stored out to the object manager after each pass of Predictions	
2155	2166	(PERF_SDD_3106_INT).	
2156	2167		
2157	2168		
2158	2169	INPUT	VALUE
2159	2170	-----	-----
		» -----	
2160	2171	Perf_Background_Dpkg.Pcitin.Itinerary	Time_Constra
		» int_Eval	
2161	2172	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	
		» False	
2162	2173	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Adjcostidx	
		» 10.0	
2163	2174	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Lastphase	
		» Descent	
2164	2175	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Glidx	
		» 100	
2165	2176	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Fpln	S
		» econdary	
2166	2177	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Valid	
		» False	
2167	2178	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Eval_Done	
		» False	
2168	2179	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Env_Limit	
		» False	
2169	2180	Perf_Background_Dpkg.Pctcstrctrl(Active).Adjcostidx	
		» 20.0	
2170	2181	Perf_Background_Dpkg.Pctcstrctrl(Active).Lastphase	
		» Cruise	
2171	2182	Perf_Background_Dpkg.Pctcstrctrl(Active).Glidx	
		» 2	
2172	2183	Perf_Background_Dpkg.Pcactorsec	
		» Active	
2173	2184	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
2174	2185	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
2175	2186	Perf_Background_Dpkg.Pctcstrctrl(Active).Envelope_Limit	
		» True	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2176	2187	Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit			
		» True			
2177	2188				
2178	2189				
2179	2190	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
2180	2191	-----	-----	-----	-----
		» -----			
2181	2192	Bp_Code	Pseudo_Bp_Pkg.Pb_Act_Cic	(N/A)	
		» 151 P			
2182	2193	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Adjcostidx	20.0	0.001	2.0
		» 0000E+01 P			
2183	2194	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Lastphase	Cruise	(N/A)	
		» CRUISE P			
2184	2195	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Glidx	2	(N/A)	
		» 2 P			
2185	2196	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Fpln	Active	(N/A)	
		» ACTIVE P			
2186	2197	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Valid	True	(N/A)	
		» TRUE P			
2187	2198	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Eval_Done	True	(N/A)	
		» TRUE P			
2188	2199	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Env_Limit	True	(N/A)	
		» TRUE P			
2189	2200	Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit	False	(N/A)	
		» FALSE P			
2190	2201	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	True	(N/A)	
		» TRUE P			
2191	2202	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)	
		» FALSE P			
2192	2203				
2193	2204				
2194	2205	====> All 11 Comparisons Passed <====			
2195	2206				
2196	2207				
2197	2208	TESTID: 21			
2198	2209				
2199	2210	Itin is Go Around, destination ETE and EFOB have been output to CDCK for display on the FPLN page.			
2200	2211	(PERF_SDD_3392_INT)			
2201	2212	GMT time snapshot taken at the beginning of the pass of predictions has been output to CDCK.			
2202	2213	(PERF_SDD_3393_INT,PERF_SDD_3052_INT)			
2203	2214	Predictied time to the primary destination and it's validity has been processed for use by the I/O function.			
2204	2215	(PERF_SDD_3027 (PERF_SRD_10869))			
2205	2216				



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2230	2241	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	2	(N/A)	
		» 2 P			
2231	2242	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Data	20.0	0.001	2.0
		» 0000E+01 P			
2232	2243	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Valid	True	(N/A)	
		» TRUE P			
2233	2244	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Ete.Data	50.0	0.001	5.0
		» 0000E+01 P			
2234	2245	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Ete.Valid	True	(N/A)	
		» TRUE P			
2235	2246	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Firstpass	True	(N/A)	
		» TRUE P			
2236	2247	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)	
		» FALSE P			
2237	2248				
2238	2249				
2239	2250	====> All 9 Comparisons Passed <====			
2240	2251				
2241	2252				
2242	2253	TESTID: 22			
2243	2254				
2244	2255	The indication to CDCK that the asterisk may be displayed on the FPLN A page shall be set True,			
2245	2256	when the CI modification is complete and missed/made is updated in LGB (Flight phase is not Transitioned to Descent)			
2246	2257	(PERF_SDD_3516_INT).			
2247	2258	RTA Prddataseq Counter is set when evaluation of the time constraint has completed and Processing is not on the first			
2248	2259	pass through the flight plan after a change			
2249	2260	(PERF_SDD_3107_INT).			
2250	2261	The transmit status of the RTA control data is not reset to False			
2251	2262	(PERF_SDD_3519_INT).			
2252	2263	ETT data have been transmitted from the slave FM to the Master when			
2253	2264	Current Fm is not the master FM in the dual Configuration			
2254	2265	A valid ETT has been computed on this pass of predictions.			
2255	2266	(PERF_SDD_3518_INT).			
2256	2267	Time constraint working data is output			
2257	2268	ETT data output processing has been performed			
2258	2269	(PERF_SDD_3515_INT,PERF_SDD_3516_INT,PERF_SDD_2095_INT).			
2259	2270				
2260	2271				
2261	2272	INPUT			VALUE
2262	2273	-----			-----
		» -----			
2263	2274	Fmcs_Partition_Data_Pkg.Ops_Master_Status			
		» Master			
2264	2275	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» True	
2265	2276	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
2266	2277	Perf_Background_Dpkg.Timeconmiss_Updated	
		» True	
2267	2278	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
2268	2279	Perf_Background_Dpkg.Pcfltphase	
		» Climb	
2269	2280	Perf_Background_Dpkg.Pccompett(Active)	
		» True	
2270	2281	Perf_Background_Dpkg.Ett(Active).Data	
		» 20.0	
2271	2282	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data	
		» 10.0	
2272	2283	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status	
		» Invalid	
2273	2284	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh	
		» False	
2274	2285	Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk	
		» False	
2275	2286	Perf_Background_Dpkg.Ett(Active).Status	
		» Valid	
2276	2287	Perf_Background_Dpkg.Pctcstridx	
		» 1	
2277	2288	Perf_Background_Dpkg.Pcdestglidx	
		» 0	
2278	2289	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
2279	2290	Perf_Dpkg.Pstopofcrzfl(Active).Valid	
		» False	
2280	2291	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
2281	2292	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst	
		» False	
2282	2293	Perf_Background_Dpkg.Pcgmtime.Gpc_Time	
		» 2	
2283	2294	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	
		» 0	
2284	2295	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq	
		» 0	
2285	2296	Perf_Background_Dpkg.Psprddataseq	
		» 3	
2286	2297	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2287	2298	» False			
		Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit			
		» True			
2288	2299	Perf_Background_Dpkg.Rta.Eval_Done			
		» True			
2289	2300	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass			
		» False			
2290	2301	Perf_Background_Dpkg.Pctcstrctrl(Active).Timeonly			
		» False			
2291	2302	Perf_Background_Dpkg.Rta.Missed			
		» True			
2292	2303	Perf_Background_Dpkg.Pcperflegs(18).Included			
		» False			
2293	2304	Perf_Background_Dpkg.Pcperflegs(18).Dist			
		» 400.0			
2294	2305	Perf_Background_Dpkg.Pcstartpt.Dist			
		» 600.0			
2295	2306	Change			
		» False			
2296	2307	Change			
		» False			
2297	2308	Change			
		» False			
2298	2309				
2299	2310				
2300	2311	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
2301	2312	-----	-----	-----	-----
		» -----			
2302	2313	Bp_Code	Pseudo_Bp_Pkg.Pb_Calc_Ett	(N/A)	
		» 153 P			
2303	2314	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	True	(N/A)	
		» TRUE P			
2304	2315	Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk	True	(N/A)	
		» TRUE P			
2305	2316	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq	3	(N/A)	
		» 3 P			
2306	2317	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data	20.0	0.001	2.0
		» 0000E+01 P			
2307	2318	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status	Valid	(N/A)	
		» VALID P			
2308	2319	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh	True	(N/A)	
		» TRUE P			
2309	2320	Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit	True	(N/A)	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2310	2321	» TRUE P		
		Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	2	(N/A)
		» 2 P		
2311	2322	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)
		» FALSE P		
2312	2323			
2313	2324			
2314	2325	====> All 10 Comparisons Passed <====		
2315	2326			
2316	2327			
2317	2328	TESTID: 23		
2318	2329			
2319	2330	The indication to CDCK that the asterisk may be displayed on the FPLN A page shall be set True		
2320	2331	when Flight phase has transitioned to Descent.(PERF_SDD_3516_INT).		
2321	2332	ETT data have been transmitted from the slave FM to the Master when		
2322	2333	- Current Fm is not the master FM in the dual Configuration		
2323	2334	- A valid ETT has been computed on this pass of predictions.(PERF_SDD_3518_INT).		
2324	2335	RTA control data has not been transmitted from the slave FM to the Master		
2325	2336	(PERF_SDD_3517_INT).		
2326	2337	The transmit status of the RTA control data is not reset to False		
2327	2338	(PERF_SDD_3519_INT).		
2328	2339	Time constraint working data is output		
2329	2340	ETT data output processing has been performed		
2330	2341	(PERF_SDD_3515_INT).		
2331	2342			
2332	2343			
2333	2344	INPUT		VALUE
2334	2345	-----		
		» -----		
2335	2346	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst		
		» False		
2336	2347	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid		
		» False		
2337	2348	Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk		
		» False		
2338	2349	Perf_Background_Dpkg.Pcgmtime.Gpc_Time		
		» 2		
2339	2350	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt		
		» 0		
2340	2351	Perf_Background_Dpkg.Pcitin.Itinerary		Prim_Fp
		» ln_Preds		
2341	2352	Perf_Background_Dpkg.Pctcstridx		
		» 1		
2342	2353	Perf_Background_Dpkg.Pcdestglidx		

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» 0	
2343	2354	Perf_Background_Dpkg.Pctcstrctrl(Active).Timeonly	
		» False	
2344	2355	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» False	
2345	2356	Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit	
		» True	
2346	2357	Perf_Background_Dpkg.Rta.Missed	
		» True	
2347	2358	Perf_Background_Dpkg.Pcperflegs(18).Included	
		» False	
2348	2359	Perf_Background_Dpkg.Pcperflegs(18).Dist	
		» 400.0	
2349	2360	Perf_Background_Dpkg.Pcstartpt.Dist	
		» 600.0	
2350	2361	Perf_Background_Dpkg.Pcfltphase	
		» Descent	
2351	2362	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
2352	2363	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
2353	2364	Perf_Background_Dpkg.Pccompett(Active)	
		» True	
2354	2365	Perf_Background_Dpkg.Ett(Active).Data	
		» 30.0	
2355	2366	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data	
		» 5.0	
2356	2367	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status	
		» Invalid	
2357	2368	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh	
		» False	
2358	2369	Perf_Background_Dpkg.Ett(Active).Status	
		» Valid	
2359	2370	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
2360	2371	Fmcs_Partition_Data_Pkg.Ops_Master_Status	
		» Master	
2361	2372	Change	
		» False	
2362	2373	Change	
		» False	
2363	2374	Change	
		» False	
2364	2375		

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		EXPECTED	TOLERANCE	ACTUAL
2365	2376			
2366	2377	OUTPUT		
		» P/F		
2367	2378	-----	-----	-----
		» -----		
2368	2379	Bp_Code Pseudo_Bp_Pkg.Pb_Calc_Ett	(N/A)	
		» 153 P		
2369	2380	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	True	(N/A)
		» TRUE P		
2370	2381	Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk	True	(N/A)
		» TRUE P		
2371	2382	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data	30.0	0.001 3.0
		» 0000E+01 P		
2372	2383	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status	Valid	(N/A)
		» VALID P		
2373	2384	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh	True	(N/A)
		» TRUE P		
2374	2385	Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit	True	(N/A)
		» TRUE P		
2375	2386	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	2	(N/A)
		» 2 P		
2376	2387	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)
		» FALSE P		
2377	2388			
2378	2389			
2379	2390	====> All 9 Comparisons Passed <====		
2380	2391			
2381	2392			
2382	2393	TESTID: 24		
2383	2394			
2384	2395	The indication to CDCK that the asterisk may be displayed on the FPLN A page shall be set True		
2385	2396	when A/C is within 40 NM point when T/D is included		
2386	2397	(PERF_SDD_3516_INT).		
2387	2398	RTA control data has not been transmitted from the slave FM to the Master		
2388	2399	(PERF_SDD_3517_INT).		
2389	2400	The transmit status of the RTA control data is not reset to False		
2390	2401	(PERF_SDD_3519_INT).		
2391	2402	Time constraint working data is output		
2392	2403	ETT data has not been transmitted from the slave FM to the Master		
2393	2404	(PERF_SDD_3518_INT).		
2394	2405	Itin is a maxalt and partition is in Dual_Slave mode.		
2395	2406	This test case is written to cover the sdd anchor PERF_SDD_3523_INT.		
2396	2407	Prf_Int_Utils.Dual_Status is a function that shall return the master/slave and dual		
2397	2408	indication via a single data item based on IO/OPS status items.		

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2398	2409	(PERF_SDD_3523_INT)	
2399	2410		
2400	2411		
2401	2412	INPUT	VALUE
2402	2413	-----	-----
		» -----	
2403	2414	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst	
		» False	
2404	2415	Fmcs_Partition_Data_Pkg.Ops_Master_Status	
		» Master	
2405	2416	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Dual	
2406	2417	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	
		» False	
2407	2418	Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk	
		» False	
2408	2419	Perf_Background_Dpkg.Pcgmtime.Gpc_Time	
		» 2	
2409	2420	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	
		» 0	
2410	2421	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
2411	2422	Perf_Background_Dpkg.Pctcstridx	
		» 1	
2412	2423	Perf_Background_Dpkg.Pcdestglidx	
		» 0	
2413	2424	Perf_Background_Dpkg.Pctcstrctrl(Active).Timeonly	
		» True	
2414	2425	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» False	
2415	2426	Perf_Background_Dpkg.Pcfltphase	
		» Cruise	
2416	2427	Perf_Background_Dpkg.Rta.Missed	
		» False	
2417	2428	Perf_Background_Dpkg.Pcperflegs(18).Included	
		» True	
2418	2429	Perf_Background_Dpkg.Pcperflegs(18).Dist	
		» 600.0	
2419	2430	Perf_Background_Dpkg.Pcstartpt.Dist	
		» 600.0	
2420	2431	Perf_Background_Dpkg.Pccompett(Active)	
		» False	
2421	2432	Perf_Background_Dpkg.Rta.Eval_Done	
		» True	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2422	2433	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid			
		» True			
2423	2434	Perf_Background_Dpkg.Ett(Active).Data			
		» 20.0			
2424	2435	Perf_Background_Dpkg.Ett(Active).Status			
		» Valid			
2425	2436	Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit			
		» True			
2426	2437	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data			
		» 5.0			
2427	2438	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status			
		» Invalid			
2428	2439	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh			
		» False			
2429	2440	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status			Perf_Int_Base_Tpkg.Du
		» al_Slave			
2430	2441	Change			
		» False			
2431	2442	Change			
		» False			
2432	2443	Change			
		» False			
2433	2444				
2434	2445				
2435	2446	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
2436	2447	-----	-----	-----	-----
		» -----			
2437	2448	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	True	(N/A)	
		» TRUE P			
2438	2449	Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk	True	(N/A)	
		» TRUE P			
2439	2450	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	2	(N/A)	
		» 2 P			
2440	2451	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data	5.0	0.001	5.0
		» 0000E+00 P			
2441	2452	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status	Invalid	(N/A)	
		» INVALID P			
2442	2453	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh	False	(N/A)	
		» FALSE P			
2443	2454	Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit	True	(N/A)	
		» TRUE P			
2444	2455	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)	
		» FALSE P			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2445	2456	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status	Perf_Int_Base_Tpkg.Dual_Master	(N/A)	DUA
		» L_MASTER P			
2446	2457				
2447	2458				
2448	2459	====> All 9 Comparisons Passed <====			
2449	2460				
2450	2461				
2451	2462	TESTID: 25			
2452	2463				
2453	2464	RTA Prddataseq Counter is set when evaluation of the time constraint has completed and Processing is not on the first			
2454	2465	pass through the flight plan after a change			
2455	2466	(PERF_SDD_3107_INT).			
2456	2467	RTA control data have been transmitted from the slave FM to the Master when			
2457	2468	- Current Fm is not the master FM in the dual Configuration			
2458	2469	- CI adjustment has resulte in the RTA being made during this pass of predictions.			
2459	2470	(PERF_SDD_3517_INT).			
2460	2471	ETT data has not been transmitted from the slave FM to the Master			
2461	2472	(PERF_SDD_3518_INT).			
2462	2473	The transmit status of the RTA control data is reset to False			
2463	2474	(PERF_SDD_3519_INT).			
2464	2475	Time constraint working data is output			
2465	2476	RTA data output processing has been performed			
2466	2477	(PERF_SDD_3515_INT,PERF_SDD_3516_INT).			
2467	2478				
2468	2479	The indication to CDCK that the asterisk may be displayed on the FPLN A page shall be set True when any of the followi			
		» ng			
2469	2480	conditions are met: (here A/C is within 40 NM point when T/D is included.)			
2470	2481	- The CI modification is complete and missed/made is updated in LGB			
2471	2482	- Flight phase has transitioned to Descent			
2472	2483	- T/D pseudo-waypoint is not included and a destination exists			
2473	2484	(This indicates that the A/C has sequenced the T/D but not started down)			
2474	2485	- A/C is within 40 NM point when T/D is included.			
2475	2486	(PERF_SDD_3516_INT)			
2476	2487	Background Performance shall signal Demand processing that Background Performance has gathered			
2477	2488	Flight Plan predicted data for the currently entered RTA.			
2478	2489	(PERF_SDD_3739_INT)			
2479	2490				
2480	2491				
2481	2492	INPUT			VALUE
2482	2493	-----			-----
		» -----			
2483	2494	Fmcs_Partition_Data_Pkg.Ops_Master_Status			
		» Master			
2484	2495	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» True	
2485	2496	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
2486	2497	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
2487	2498	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» False	
2488	2499	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
2489	2500	Perf_Background_Dpkg.Pcfltphase	
		» Climb	
2490	2501	Perf_Background_Dpkg.Psfinaldes	
		» True	
2491	2502	Perf_Background_Dpkg.Pccompett(Active)	
		» False	
2492	2503	Perf_Background_Dpkg.Ett(Active).Data	
		» 20.0	
2493	2504	Perf_Background_Dpkg.Pctcstridx	
		» 1	
2494	2505	Perf_Background_Dpkg.Pcdestglidx	
		» 0	
2495	2506	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
2496	2507	Perf_Background_Dpkg.Pcitin.Flight_Plan	
		» Active	
2497	2508	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
2498	2509	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst	
		» False	
2499	2510	Perf_Background_Dpkg.Pcgmttime.Gpc_Time	
		» 2	
2500	2511	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	
		» 0	
2501	2512	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq	
		» 0	
2502	2513	Perf_Background_Dpkg.Psprddataseq	
		» 3	
2503	2514	Perf_Background_Dpkg.Pcperflegs(18).Included	
		» True	
2504	2515	Perf_Background_Dpkg.Pcperflegs(18).Dist	
		» 4000.0	
2505	2516	Perf_Background_Dpkg.Pcstartpt.Dist	
		» 600.0	
2506	2517	Perf_Background_Dpkg.Ett(Active).Data	



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» 20.0	
2507	2518	Perf_Background_Dpkg.Ett(Active).Status	
		» Valid	
2508	2519	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	
		» False	
2509	2520	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Adjcostidx	
		» 10.0	
2510	2521	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Lastphase	
		» Descent	
2511	2522	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Glidx	
		» 100	
2512	2523	Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk	
		» False	
2513	2524	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Fpln	S
		» econdary	
2514	2525	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Valid	
		» False	
2515	2526	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Eval_Done	
		» False	
2516	2527	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Env_Limit	
		» False	
2517	2528	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Flat	
		» False	
2518	2529	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Flat_Count	
		» 5	
2519	2530	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data	
		» 5.0	
2520	2531	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status	
		» Invalid	
2521	2532	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh	
		» False	
2522	2533	Perf_Background_Dpkg.Pctcstrctrl(Active).Adjcostidx	
		» 20.0	
2523	2534	Perf_Background_Dpkg.Pctcstrctrl(Active).Lastphase	
		» Cruise	
2524	2535	Perf_Background_Dpkg.Pctcstrctrl(Active).Glidx	
		» 2	
2525	2536	Perf_Background_Dpkg.Pctcstrctrl(Active).Flat	
		» True	
2526	2537	Perf_Background_Dpkg.Pctcstrctrl(Active).Flat_Count	
		» 4	
2527	2538	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
2528	2539	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2529	2540	» True			
		Perf_Background_Dpkg.Pctcstrctrl(Active).Envelope_Limit			
		» True			
2530	2541	Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit			
		» True			
2531	2542	Perf_Background_Dpkg.Pctcstrctrl(Active).Timeonly			
		» True			
2532	2543	Perf_Background_Dpkg.Rta.Eval_Done			
		» False			
2533	2544	Perf_Background_Dpkg.Pcactorsec			
		» Active			
2534	2545	Perf_Background_Dpkg.Rta.Missed			
		» True			
2535	2546	Perf_Dpkg.Rta_Data_Gathered			
		» False			
2536	2547	Change			
		» False			
2537	2548	Change			
		» False			
2538	2549	Change			
		» False			
2539	2550				
2540	2551				
2541	2552	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
2542	2553	-----	-----	-----	-----
		» -----			
2543	2554	Bp_Code	Pseudo_Bp_Pkg.Pb_Act_Cic	(N/A)	
		» 151 P			
2544	2555	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Env_Limit	True	(N/A)	
		» TRUE P			
2545	2556	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Eval_Done	True	(N/A)	
		» TRUE P			
2546	2557	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Valid	True	(N/A)	
		» TRUE P			
2547	2558	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Glidx	2	(N/A)	
		» 2 P			
2548	2559	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Lastphase	Cruise	(N/A)	
		» CRUISE P			
2549	2560	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Adjcostidx	20.0	0.001	2.0
		» 0000E+01 P			
2550	2561	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Flat_Count	4	(N/A)	
		» 4 P			
2551	2562	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Flat	True	(N/A)	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2552	2563	» TRUE P			
		Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit	False	(N/A)	
		» FALSE P			
2553	2564	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Fpln	Active	(N/A)	
		» ACTIVE P			
2554	2565	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	True	(N/A)	
		» TRUE P			
2555	2566	Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk	True	(N/A)	
		» TRUE P			
2556	2567	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq	3	(N/A)	
		» 3 P			
2557	2568	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	2	(N/A)	
		» 2 P			
2558	2569	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data	5.0	0.001	5.0
		» 0000E+00 P			
2559	2570	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status	Invalid	(N/A)	
		» INVALID P			
2560	2571	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh	False	(N/A)	
		» FALSE P			
2561	2572	Perf_Dpkg.Rta_Data_Gathered	True	(N/A)	
		» TRUE P			
2562	2573	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)	
		» FALSE P			
2563	2574				
2564	2575				
2565	2576	====> All 20 Comparisons Passed <====			
2566	2577				
2567	2578				
2568	2579	TESTID: 26			
2569	2580				
2570	2581	The indication to CDCK that the asterisk may be displayed on the FPLN A page shall be set True when			
2571	2582	- T/D pseudo-waypoint is not included and a destination exists			
2572	2583	(This indicates that the A/C has sequenced the T/D but not started down)			
2573	2584	(PERF_SDD_3516_INT)			
2574	2585	RTA control data have been transmitted from the slave FM to the Master when Current Fm is not			
2575	2586	the master FM in the dual Configuration CI adjustment has resulted in the RTA being made			
2576	2587	during this pass of predictions.(PERF_SDD_3517_INT).			
2577	2588	RTA Prddataseq Counter is set when evaluation of the time constraint has completed and Processing is			
2578	2589	not on the first pass through the flight plan after a change			
2579	2590	(PERF_SDD_3107_INT).			
2580	2591	The transmit status of the RTA control data is reset to False			
2581	2592	(PERF_SDD_3519_INT).			
2582	2593	ETT data has not been transmitted from the slave FM to the Master			
2583	2594	(PERF_SDD_3518_INT).			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2584	2595	Time constraint working data is output	
2585	2596	RTA data output processing has been performed	
2586	2597	(PERF_SDD_3515_INT).	
2587	2598		
2588	2599		
2589	2600	INPUT	VALUE
2590	2601	-----	
		» -----	
2591	2602	Perf_Background_Dpkg.Rta.Missed	
		» True	
2592	2603	Perf_Background_Dpkg.Pcperflegs(18).Included	
		» False	
2593	2604	Perf_Background_Dpkg.Pcperflegs(18).Dist	
		» 400.0	
2594	2605	Perf_Background_Dpkg.Pcstartpt.Dist	
		» 600.0	
2595	2606	Perf_Background_Dpkg.Pccompett(Active)	
		» False	
2596	2607	Perf_Background_Dpkg.Pctcstridx	
		» 1	
2597	2608	Perf_Background_Dpkg.Pcdestglidx	
		» 1	
2598	2609	Perf_Background_Dpkg.Rta.Eval_Done	
		» False	
2599	2610	Perf_Background_Dpkg.Pcitin.Flight_Plan	
		» Active	
2600	2611	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
2601	2612	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
2602	2613	Perf_Background_Dpkg.Etp_Itin_Ran	
		» False	
2603	2614	Perf_Background_Dpkg.Pcgmtime.Gpc_Time	
		» 2	
2604	2615	Perf_Background_Dpkg.Psprddataseq	
		» 3	
2605	2616	Perf_Background_Dpkg.Pcfltphase	
		» Climb	
2606	2617	Perf_Background_Dpkg.Pctcstrctrl(Active).Timeonly	
		» True	
2607	2618	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
2608	2619	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» False	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2609	2620	Perf_Background_Dpkg.Pctcstrctrl(Active).Adjcostidx
		» 20.0
2610	2621	Perf_Background_Dpkg.Pctcstrctrl(Active).Lastphase
		» Cruise
2611	2622	Perf_Background_Dpkg.Pctcstrctrl(Active).Glidx
		» 2
2612	2623	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done
		» True
2613	2624	Perf_Background_Dpkg.Pctcstrctrl(Active).Envelope_Limit
		» True
2614	2625	Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit
		» True
2615	2626	Perf_Background_Dpkg.Ett(Active).Data
		» 20.0
2616	2627	Perf_Background_Dpkg.Ett(Active).Status
		» Valid
2617	2628	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst
		» False
2618	2629	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt
		» 0
2619	2630	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq
		» 0
2620	2631	Fmcs_Partition_Data_Pkg.Ops_Master_Status
		» Master
2621	2632	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode
		» Single
2622	2633	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid
		» False
2623	2634	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Adjcostidx
		» 50.0
2624	2635	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Lastphase
		» Descent
2625	2636	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Glidx
		» 100
2626	2637	Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk
		» False
2627	2638	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Fpln
		» econdary
2628	2639	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Valid
		» False
2629	2640	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Eval_Done
		» False
2630	2641	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Env_Limit
		» False

S

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2631	2642	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data			
		» 5.0			
2632	2643	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status			
		» Invalid			
2633	2644	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh			
		» False			
2634	2645	Change			
		» False			
2635	2646	Change			
		» False			
2636	2647	Change			
		» False			
2637	2648				
2638	2649				
2639	2650	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
2640	2651	-----	-----	-----	-----
		» -----			
2641	2652	Bp_Code	Pseudo_Bp_Pkg.Pb_Act_Cic	(N/A)	
		» 151 P			
2642	2653	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Env_Limit	True	(N/A)	
		» TRUE P			
2643	2654	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Eval_Done	True	(N/A)	
		» TRUE P			
2644	2655	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Valid	True	(N/A)	
		» TRUE P			
2645	2656	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Glidx	2	(N/A)	
		» 2 P			
2646	2657	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Lastphase	Cruise	(N/A)	
		» CRUISE P			
2647	2658	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Adjcostidx	20.0	0.001	2.0
		» 0000E+01 P			
2648	2659	Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit	False	(N/A)	
		» FALSE P			
2649	2660	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Fpln	Active	(N/A)	
		» ACTIVE P			
2650	2661	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	True	(N/A)	
		» TRUE P			
2651	2662	Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk	True	(N/A)	
		» TRUE P			
2652	2663	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq	3	(N/A)	
		» 3 P			
2653	2664	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	2	(N/A)	
		» 2 P			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2654	2665	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data	5.0	0.001	5.0
		» 0000E+00 P			
2655	2666	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status	Invalid	(N/A)	
		» INVALID P			
2656	2667	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh	False	(N/A)	
		» FALSE P			
2657	2668	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)	
		» FALSE P			
2658	2669				
2659	2670				
2660	2671	====> All 17 Comparisons Passed <====			
2661	2672				
2662	2673				
2663	2674	TESTID: 27			
2664	2675				
2665	2676	CDCK is not indicated to display the Asterisk, the indication to CDCK that the asterisk			
2666	2677	may be displayed on the FPLN A page shall be set False, when none of the following conditions are met:			
2667	2678	- The CI modification is complete and missed/made is updated in LGB			
2668	2679	- Flight phase has transitioned to Descent			
2669	2680	- T/D pseudo-waypoint is not included and a destination exists			
2670	2681	(This indicates that the A/C has sequenced the T/D but not started down)			
2671	2682	- A/C is within 40 NM point when T/D is included.			
2672	2683	(PERF_SDD_3516_INT, PERF_SDD_07394_INT)			
2673	2684				
2674	2685	RTA control data has not been transmitted from the slave FM to the Master			
2675	2686	(PERF_SDD_3517_INT).			
2676	2687	The transmit status of the RTA control data is not reset to False			
2677	2688	(PERF_SDD_3519_INT).			
2678	2689	ETT data have been transmitted from the slave FM to the Master when			
2679	2690	- Current Fm is not the master FM in the dual Configuration			
2680	2691	- A valid ETT has been computed on this pass of predictions.			
2681	2692	(PERF_SDD_3518_INT).			
2682	2693	Time constraint working data is output			
2683	2694	ETT data output processing has been performed			
2684	2695	(PERF_SDD_3515_INT).			
2685	2696				
2686	2697				
2687	2698	INPUT			VALUE
2688	2699	-----			-----
		» -----			
2689	2700	Perf_Background_Dpkg.Pcitin.Itinerary			Prim_Fp
		» ln_Preds			
2690	2701	Perf_Background_Dpkg.Pcfpln			Ac
		» tprimary			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2691	2702	Perf_Background_Dpkg.Pcgmtime.Gpc_Time
		» 2
2692	2703	Perf_Background_Dpkg.Pcfltphase
		» Climb
2693	2704	Perf_Background_Dpkg.Rta.Missed
		» True
2694	2705	Perf_Background_Dpkg.Rta.Eval_Done
		» True
2695	2706	Perf_Background_Dpkg.Pcperflegs(18).Included
		» False
2696	2707	Perf_Background_Dpkg.Pcperflegs(18).Dist
		» 400.0
2697	2708	Perf_Background_Dpkg.Pcstartpt.Dist
		» 600.0
2698	2709	Perf_Background_Dpkg.Pccompett(Active)
		» True
2699	2710	Perf_Background_Dpkg.Pctcstridx
		» 1
2700	2711	Perf_Background_Dpkg.Pcdestglidx
		» 0
2701	2712	Perf_Background_Dpkg.Ett(Active).Data
		» 20.0
2702	2713	Perf_Background_Dpkg.Ett(Active).Status
		» Valid
2703	2714	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done
		» False
2704	2715	Perf_Background_Dpkg.Pctcstrctrl(Active).Timeonly
		» True
2705	2716	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid
		» True
2706	2717	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass
		» False
2707	2718	Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit
		» True
2708	2719	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst
		» False
2709	2720	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt
		» 0
2710	2721	Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk
		» True
2711	2722	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data
		» 5.0
2712	2723	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status
		» Invalid



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2713	2724	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh			
		» False			
2714	2725	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid			
		» False			
2715	2726	Fmcs_Partition_Data_Pkg.Ops_Master_Status			
		» Master			
2716	2727	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode			
		» Single			
2717	2728	Change			
		» False			
2718	2729	Change			
		» False			
2719	2730	Change			
		» False			
2720	2731				
2721	2732				
2722	2733	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
2723	2734	-----	-----	-----	-----
		» -----			
2724	2735	Bp_Code	Pseudo_Bp_Pkg.Pb_Calc_Ett	(N/A)	
		» 153 P			
2725	2736	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	True	(N/A)	
		» TRUE P			
2726	2737	Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk	False	(N/A)	
		» FALSE P			
2727	2738	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data	20.0	0.001	2.0
		» 0000E+01 P			
2728	2739	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status	Valid	(N/A)	
		» VALID P			
2729	2740	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh	True	(N/A)	
		» TRUE P			
2730	2741	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	2	(N/A)	
		» 2 P			
2731	2742	Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit	True	(N/A)	
		» TRUE P			
2732	2743	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)	
		» FALSE P			
2733	2744				
2734	2745				
2735	2746	====> All 9 Comparisons Passed <====			
2736	2747				
2737	2748				
2738	2749	TESTID: 28			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

2739 2750
2740 2751 This test case is same as test case 2 except for new input variables that have been added to test
2741 2752 the anchor PERF_SDD_3500_INT. In the procedure Prf_Int_Utills.Update_Refresh_Timer updates the passed-in timer's
2742 2753 record data. The passed in timer's refresh time shall be set to the difference between the current FM time
2743 2754 and the timer's reference start time, and the timer's reference start time set equal to the current FM time.
2744 2755 (PERF_SDD_3500_INT)
2745 2756
2746 2757 This test case is also written to cover the anchor PERF_SDD_3501_INT. A running average of the most recent refresh
2747 2758 time data points (up to five) shall be computed and stored in the passed-in timer's record data, along with the actual
2748 2759 refresh time data points (up to five) used to compute the average.
2749 2760 (PERF_SDD_3501_INT)
2750 2761
2751 2762
2752 2763 INPUT
2753 2764 -----
2754 2765 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx
2755 2766 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change
2756 2767 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec
2757 2768 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec
2758 2769 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec
2759 2770 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec
2760 2771 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec
2761 2772 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
2762 2773 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
2763 2774 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
2764 2775 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
2765 2776 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog
2766 2777 Perf_Background_DPkg.Opt_Step_Data.Distodest
2767 2778 Perf_Background_DPkg.Opt_Step_Data.Timetogo

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2768	2779	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
		» 0.0
2769	2780	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
		» 0.0
2770	2781	Perf_Background_Dpkg.Pshmpreddata.Speed
		» 250.0
2771	2782	Perf_Background_Dpkg.Pshmpreddata.Fuel
		» 50.0
2772	2783	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
		» False
2773	2784	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
		» 0.0
2774	2785	Perf_Background_Dpkg.Pcoptalt.Valid
		» True
2775	2786	Perf_Background_Dpkg.Pcoptalt.Data
		» 19000.0
2776	2787	Fmcs_Partition_Data_Pkg.Ops_Master_Status
		» Master
2777	2788	Ctp_Perf_bkgnd_put_bk_data.Boot_Status
		» rm_Start
2778	2789	Perf_Background_Dpkg.Preds_Output(Active)
		» True
2779	2790	Perf_Background_Dpkg.Psfinalalt
		» 0.0
2780	2791	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt
		» 5000
2781	2792	Perf_Background_Dpkg.Psfpolfnlful
		» 0.0
2782	2793	Perf_Background_Dpkg.Psfpolfnltme
		» 0.0
2783	2794	Perf_Background_Dpkg.Psfpolfnltg
		» 0.0
2784	2795	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel
		» 40
2785	2796	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time
		» 50
2786	2797	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time
		» 60
2787	2798	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done
		» True
2788	2799	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid
		» True
2789	2800	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass
		» False

Wa

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2790	2801	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
2791	2802	Perf_Background_Dpkg.Pcfltphase	
		» Cruise	
2792	2803	Perf_Background_Dpkg.Psfinaldes	
		» True	
2793	2804	Perf_Background_Dpkg.Vert_Auto_Mode	
		» True	
2794	2805	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	
2795	2806	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 55000.0	
2796	2807	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	
2797	2808	Perf_background_Dpkg.Maxalt.Num_Engout	
		» 0	
2798	2809	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» False	
2799	2810	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» False	
2800	2811	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
2801	2812	Perf_Dpkg.Pstopofcrzfl(Active).Valid	
		» False	
2802	2813	Perf_Background_Dpkg.Pcitin.Flight_Plan	
		» Active	
2803	2814	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
2804	2815	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst	
		» False	
2805	2816	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress	
		» False	
2806	2817	Perf_Background_Dpkg.Pcgmtime.Gpc_Time	
		» 2	
2807	2818	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	
		» 0	
2808	2819	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq	
		» 0	
2809	2820	Perf_Background_Dpkg.Psprddataseq	
		» 3	
2810	2821	Perf_Background_Dpkg.Etp_Itin_Ran	
		» False	
2811	2822	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc	
		» True	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2812	2823	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid			
		» False			
2813	2824	CTP_PERF_BKGND_PUT_BK_DATA.Fpln			
		» Active			
2814	2825	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Number_Of_Points			
		» 3			
2815	2826	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(1)			
		» 4.0			
2816	2827	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(2)			
		» 3.0			
2817	2828	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(3)			
		» 2.0			
2818	2829	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Average_Refresh_Time			
		» 0.0			
2819	2830	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Start_Time			
		» 0			
2820	2831	Fmcs_Partition_Data_Pkg.Ops_Time.Gpc_Time			
		» 20			
2821	2832	Ops_Timer_Pkg:body.Ops_time.Gpc_Time			
		» 30			
2822	2833	Change			
		» False			
2823	2834	Change			
		» False			
2824	2835				
2825	2836				
2826	2837	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
2827	2838	-----	-----	-----	-----
		» -----			
2828	2839	Timer.Start_Time		30	(N/A)
		» 30 P			
2829	2840	Timer.Refresh_Time	1.00000E-03	0.001	1.0
		» 0000E-03 P			
2830	2841	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Average_Refresh_Time	2.25025E+00	0.001	2.2
		» 5025E+00 P			
2831	2842				
2832	2843				
2833	2844	INPUT			VALUE
2834	2845	-----	-----	-----	-----
		» -----			
2835	2846	Change			
		» False			
2836	2847				

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

2837 2848
2838 2849 ===== All 3 Comparisons Passed =====
2839 2850
2840 2851
2841 2852 TESTID: 29
2842 2853
2843 2854 This test case is same as test case 2 except for new input variables that have been added to test
2844 2855 the anchor PERF_SDD_3500_INT. In the procedure Prf_Int_Utills.Update_Refresh_Timer updates the passed-in timer's
2845 2856 record data. The passed in timer's refresh time shall be set to the difference between the current FM time
2846 2857 and the timer's reference start time, and the timer's reference start time set equal to the current FM time.
2847 2858 (PERF_SDD_3500_INT)
2848 2859 This test case is also written to cover the anchor PERF_SDD_3501_INT. A running average of the most recent refresh
2849 2860 time data points (up to five) shall be computed and stored in the passed-in timer's record data, along with the actual
2850 2861 refresh time data points (up to five) used to compute the average.
2851 2862 (PERF_SDD_3501_INT)
2852 2863
2853 2864
2854 2865 INPUT
2855 2866 -----
2856 2867 » -----
2856 2867 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx
2856 2867 » 2
2857 2868 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change
2857 2868 » False
2858 2869 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec
2858 2869 » False
2859 2870 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec
2859 2870 » False
2860 2871 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec
2860 2871 » False
2861 2872 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec
2861 2872 » False
2862 2873 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec
2862 2873 » False
2863 2874 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
2863 2874 » False
2864 2875 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
2864 2875 » 2
2865 2876 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
2865 2876 » 0.0
2866 2877 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
2866 2877 » 0.0
2867 2878 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog
2867 2878 » True

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2868	2879	Perf_Background_DPkg.Opt_Step_Data.Distodest
		» 25.0
2869	2880	Perf_Background_DPkg.Opt_Step_Data.Timetogo
		» 5.0
2870	2881	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
		» 0.0
2871	2882	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
		» 0.0
2872	2883	Perf_Background_Dpkg.Pshmpreddata.Speed
		» 250.0
2873	2884	Perf_Background_Dpkg.Pshmpreddata.Fuel
		» 50.0
2874	2885	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
		» False
2875	2886	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
		» 0.0
2876	2887	Perf_Background_Dpkg.Pcoptalt.Valid
		» True
2877	2888	Perf_Background_Dpkg.Pcoptalt.Data
		» 19000.0
2878	2889	Fmcs_Partition_Data_Pkg.Ops_Master_Status
		» Master
2879	2890	Ctp_Perf_bkgnd_put_bk_data.Boot_Status
		» rm_Start
2880	2891	Perf_Background_Dpkg.Preds_Output(Active)
		» True
2881	2892	Perf_Background_Dpkg.Psfinalalt
		» 0.0
2882	2893	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt
		» 5000
2883	2894	Perf_Background_Dpkg.Psfpolfnlful
		» 0.0
2884	2895	Perf_Background_Dpkg.Psfpolfnltme
		» 0.0
2885	2896	Perf_Background_Dpkg.Psfpolfnltg
		» 0.0
2886	2897	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel
		» 40
2887	2898	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time
		» 50
2888	2899	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time
		» 60
2889	2900	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done
		» True

Wa

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2890	2901	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
2891	2902	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» False	
2892	2903	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
2893	2904	Perf_Background_Dpkg.Pcfltphase	
		» Cruise	
2894	2905	Perf_Background_Dpkg.Psfinaldes	
		» True	
2895	2906	Perf_Background_Dpkg.Vert_Auto_Mode	
		» True	
2896	2907	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	
2897	2908	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 55000.0	
2898	2909	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	
2899	2910	Perf_background_Dpkg.Maxalt.Num_Engout	
		» 0	
2900	2911	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» False	
2901	2912	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» False	
2902	2913	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
2903	2914	Perf_Dpkg.Pstopofcrzfl(Active).Valid	
		» False	
2904	2915	Perf_Background_Dpkg.Pcitin.Flight_Plan	
		» Active	
2905	2916	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
2906	2917	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfregst	
		» False	
2907	2918	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress	
		» False	
2908	2919	Perf_Background_Dpkg.Pcgmtime.Gpc_Time	
		» 2	
2909	2920	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	
		» 0	
2910	2921	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq	
		» 0	
2911	2922	Perf_Background_Dpkg.Psprddataseq	
		» 3	



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2912	2923	Perf_Background_Dpkg.Etp_Itin_Ran			
		» False			
2913	2924	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc			
		» True			
2914	2925	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid			
		» False			
2915	2926	CTP_PERF_BKGND_PUT_BK_DATA.Fpln			
		» Active			
2916	2927	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Number_Of_Points			
		» 5			
2917	2928	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(1)			
		» 4.0			
2918	2929	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(2)			
		» 3.0			
2919	2930	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(3)			
		» 2.0			
2920	2931	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Average_Refresh_Time			
		» 0.0			
2921	2932	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Start_Time			
		» 0			
2922	2933	Fmcs_Partition_Data_Pkg.Ops_Time.Gpc_Time			
		» 20			
2923	2934	Ops_Timer_Pkg:body.Ops_time.Gpc_Time			
		» 30			
2924	2935	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(4)			
		» 1.0			
2925	2936	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(5)			
		» 0.5			
2926	2937	Change			
		» False			
2927	2938	Change			
		» False			
2928	2939				
2929	2940				
2930	2941	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
2931	2942	-----	-----	-----	-----
		» -----			
2932	2943	Timer.Start_Time			
		» 30 P		(N/A)	
2933	2944	Timer.Refresh_Time	1.00000E-03	0.001	1.0
		» 0000E-03 P			
2934	2945	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Average_Refresh_Time	1.30020E+00	0.001	1.3
		» 0020E+00 P			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

2935	2946		
2936	2947		
2937	2948	INPUT	VALUE
2938	2949	-----	-----
		» -----	
2939	2950	Change	
		» False	
2940	2951		
2941	2952		
2942	2953	====> All 3 Comparisons Passed <====	
2943	2954		
2944	2955		
2945	2956	TESTID: 30	
2946	2957		
2947	2958	Itin is a maxalt and partition is in Dual_Slave mode.	
2948	2959	Prf_Int_Utills.Dual_Status is a function that shall return the master/slave and dual	
2949	2960	indication via a single data item based on IO/OPS status items.	
2950	2961	(PERF_SDD_3523_INT)	
2951	2962		
2952	2963		
2953	2964	INPUT	VALUE
2954	2965	-----	-----
		» -----	
2955	2966	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx	
		» 2	
2956	2967	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change	
		» False	
2957	2968	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	
		» False	
2958	2969	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	
		» False	
2959	2970	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	
		» False	
2960	2971	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	
		» False	
2961	2972	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	
		» False	
2962	2973	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	
		» False	
2963	2974	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)	
		» 2	
2964	2975	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest	
		» 0.0	
2965	2976	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» 0.0	
2966	2977	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	
		» True	
2967	2978	Perf_Background_DPkg.Opt_Step_Data.Distodest	
		» 25.0	
2968	2979	Perf_Background_DPkg.Opt_Step_Data.Timetogo	
		» 5.0	
2969	2980	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed	
		» 0.0	
2970	2981	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel	
		» 0.0	
2971	2982	Perf_Background_Dpkg.Pshmpreddata.Speed	
		» 250.0	
2972	2983	Perf_Background_Dpkg.Pshmpreddata.Fuel	
		» 50.0	
2973	2984	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid	
		» False	
2974	2985	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data	
		» 0.0	
2975	2986	Perf_Background_Dpkg.Pcoptalt.Valid	
		» True	
2976	2987	Perf_Background_Dpkg.Pcoptalt.Data	
		» 19000.0	
2977	2988	Fmcs_Partition_Data_Pkg.Ops_Master_Status	Base_Domain_Services_Tp
		» kg.Spare	
2978	2989	Ctp_Perf_bkgnd_put_bk_data.Boot_Status	Wa
		» rm_Start	
2979	2990	Perf_Background_Dpkg.Preds_Output(Active)	
		» True	
2980	2991	Perf_Background_Dpkg.Psffinalalt	
		» 0.0	
2981	2992	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt	
		» 5000	
2982	2993	Perf_Background_Dpkg.Psfpolfnlful	
		» 0.0	
2983	2994	Perf_Background_Dpkg.Psfpolfnltme	
		» 0.0	
2984	2995	Perf_Background_Dpkg.Psfpolfnltg	
		» 0.0	
2985	2996	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel	
		» 40	
2986	2997	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	
		» 50	
2987	2998	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» 60
2988	2999	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done
		» True
2989	3000	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid
		» True
2990	3001	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass
		» False
2991	3002	Perf_Background_Dpkg.Pcfpln
		» tprimary
2992	3003	Perf_Background_Dpkg.Pcfltphase
		» Cruise
2993	3004	Perf_Background_Dpkg.Psfinaldes
		» True
2994	3005	Perf_Background_Dpkg.Vert_Auto_Mode
		» True
2995	3006	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data
		» 50000.0
2996	3007	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data
		» 55000.0
2997	3008	Perf_background_Dpkg.Maxalt.Gwt
		» 150000.0
2998	3009	Perf_background_Dpkg.Maxalt.Num_Engout
		» 0
2999	3010	Perf_Background_Dpkg.Etp_Itin_Ran
		» True
3000	3011	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid
		» False
3001	3012	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid
		» False
3002	3013	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode
		» Dual
3003	3014	Perf_Dpkg.Pstopofcrzfl(Active).Valid
		» False
3004	3015	Perf_Background_Dpkg.Pcitin.Itinerary
		» Maxalt
3005	3016	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst
		» False
3006	3017	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress
		» False
3007	3018	Perf_Background_Dpkg.Pcgmtime.Gpc_Time
		» 2
3008	3019	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt
		» 0
3009	3020	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq

Ac

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3010	3021	» 0			
		Perf_Background_Dpkg.Psprddataseq			
		» 3			
3011	3022	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc			
		» True			
3012	3023	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid			
		» False			
3013	3024	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status		Perf_Int_Base_Tpkg.Dua	
		» 1_Master			
3014	3025	Change			
		» False			
3015	3026				
3016	3027				
3017	3028	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
3018	3029	-----	-----	-----	-----
		» -----			
3019	3030	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status	Perf_Int_Base_Tpkg.Dual_Slave	(N/A)	DU
		» AL_SLAVE P			
3020	3031				
3021	3032				
3022	3033	====> All 1 Comparisons Passed <====			
3023	3034				
3024	3035				
3025	3036	TESTID: 31			
3026	3037				
3027	3038	If the first legs match, then the Lateral Offset Data Point data shall be copied from Perf's working data to the appro			
		» priate			
3028	3039	Active or Secondary LGB header.Prf_Bkgnd_Pkg.Put_Bk_Data consist Store out the Lateral Offset Data Points.			
3029	3040	In this test case Active Primary Flt Plan and CAPTURE_PATH_START Lateral Offset Data Points are considered			
3030	3041	(PERF_SDD_3968_INT)			
3031	3042				
3032	3043				
3033	3044	INPUT			VALUE
3034	3045	-----			-----
		» -----			
3035	3046	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx			
		» 2			
3036	3047	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change			
		» False			
3037	3048	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec			
		» False			
3038	3049	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec			
		» False			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3039	3050	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec
		» False
3040	3051	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec
		» False
3041	3052	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec
		» False
3042	3053	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
		» False
3043	3054	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
		» 2
3044	3055	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
		» 0.0
3045	3056	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
		» 0.0
3046	3057	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog
		» True
3047	3058	Perf_Background_DPkg.Opt_Step_Data.Distodest
		» 25.0
3048	3059	Perf_Background_DPkg.Opt_Step_Data.Timetogo
		» 5.0
3049	3060	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
		» 0.0
3050	3061	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
		» 0.0
3051	3062	Perf_Background_Dpkg.Pshmpreddata.Speed
		» 250.0
3052	3063	Perf_Background_Dpkg.Pshmpreddata.Fuel
		» 50.0
3053	3064	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
		» False
3054	3065	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
		» 0.0
3055	3066	Perf_Background_Dpkg.Pcoptalt.Valid
		» True
3056	3067	Perf_Background_Dpkg.Pcoptalt.Data
		» 19000.0
3057	3068	Fmcs_Partition_Data_Pkg.Ops_Master_Status
		» Master
3058	3069	Ctp_Perf_bkgnd_put_bk_data.Boot_Status
		» ld_Start
3059	3070	Perf_Background_Dpkg.Preds_Output(Active)
		» True
3060	3071	Perf_Background_Dpkg.Psfinalalt
		» 0.0

Co

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3061	3072	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt	
		» 5000	
3062	3073	Perf_Background_Dpkg.Psfpolfnlful	
		» 0.0	
3063	3074	Perf_Background_Dpkg.Psfpolfnltme	
		» 0.0	
3064	3075	Perf_Background_Dpkg.Psfpolfnltg	
		» 0.0	
3065	3076	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel	
		» 40	
3066	3077	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	
		» 50	
3067	3078	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Percent	
		» 100.0	
3068	3079	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Upper_Limit	
		» 4.0	
3069	3080	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Lower_Limit	
		» 1.0	
3070	3081	Options_And_Data_Pkg:body.All_Options.Ats_Enable	
		» True	
3071	3082	Options_And_Data_Pkg:body.All_Options.Alt_n_Trip_In_Rsv_Enb	
		» True	
3072	3083	Options_And_Data_Pkg:body.All_Options.Cmp_Rsv_In_Flt_Enb	
		» True	
3073	3084	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	
		» 60	
3074	3085	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
3075	3086	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
3076	3087	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» True	
3077	3088	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
3078	3089	Perf_Background_Dpkg.Pcfltphase	P
		» reflight	
3079	3090	Perf_Background_Dpkg.Psfinaldes	
		» True	
3080	3091	Perf_Background_Dpkg.Vert_Auto_Mode	
		» True	
3081	3092	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	
3082	3093	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 55000.0	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3083	3094	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	
3084	3095	Perf_background_Dpkg.Maxalt.Num_Engout	
		» 0	
3085	3096	Perf_Background_Dpkg.Etp_Itin_Ran	
		» True	
3086	3097	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» False	
3087	3098	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» False	
3088	3099	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
3089	3100	Perf_Background_Dpkg.Pcitin.Flight_Plan	
		» Active	
3090	3101	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
3091	3102	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst	
		» False	
3092	3103	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress	
		» False	
3093	3104	Perf_Background_Dpkg.Pcgmtime.Gpc_Time	
		» 2	
3094	3105	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	
		» 0	
3095	3106	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq	
		» 0	
3096	3107	Perf_Background_Dpkg.Psprddataseq	
		» 3	
3097	3108	Perf_Dpkg.Pstopofcrzfl(Active).Valid	
		» False	
3098	3109	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc	
		» True	
3099	3110	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	
		» False	
3100	3111	Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest	
		» "P"	
3101	3112	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status	Perf_Int_Base_Tpkg.Dua
		» 1_Master	
3102	3113	Perf_Background_Dpkg.Ats_Enable	
		» False	
3103	3114	Perf_Background_Dpkg.Psrsvaltn	
		» False	
3104	3115	Perf_Background_Dpkg.Psrsvinflt	
		» False	



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3105	3116	Perf_Background_Dpkg.Psrtersvpctg			
		» 0.0			
3106	3117	Perf_Background_Dpkg.Psmaxrtersv			
		» 0.0			
3107	3118	Perf_Background_Dpkg.Psminrtersv			
		» 0.0			
3108	3119	Perf_Background_Dpkg.Ref_Flight_Plan			
		» 1			
3109	3120	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).PRDTAS			
		» 65.0			
3110	3121	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Prd_Wind_Mag			
		» 66.0			
3111	3122	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Prd_Wind_True_Brg			
		» 68.0			
3112	3123	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Prddataseq			
		» 5			
3113	3124	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Prdalt			
		» 1000.0			
3114	3125	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Prdgwttofix			
		» 69.0			
3115	3126	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Fixdistodest			
		» 70.0			
3116	3127	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Fixdtdbias			
		» 80.0			
3117	3128	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Fltphasefix			P
		» REFLIGHT			
3118	3129	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Prdterm			
		» TRUE			
3119	3130	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_START).Firstpass			
		» FALSE			
3120	3131	Change			
		» False			
3121	3132	Change			
		» False			
3122	3133	Change			
		» False			
3123	3134				
3124	3135				
3125	3136	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
3126	3137	-----	-----	-----	-----
		» -----			
3127	3138	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.PRDTAS			
3128	3139		0.0	0.001	0.0

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3129	3140	» 0000E+00 P			
3130	3141	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Prd_Wind_Mag	0.0	0.001	0.0
		» 0000E+00 P			
3131	3142	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Prd_Wind_True_Brg	0.0	0.001	0.0
3132	3143				
		» 0000E+00 P			
3133	3144	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Prddataseq	0	(N/A)	
3134	3145				
		» 0 P			
3135	3146	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Prddalt	0.0	0.001	0.0
3136	3147				
		» 0000E+00 P			
3137	3148	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Prdgwttofix	0.0	0.001	0.0
3138	3149				
		» 0000E+00 P			
3139	3150	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Fixdistodest	0.0	0.001	0.0
3140	3151				
		» 0000E+00 P			
3141	3152	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Fixdtdbias	0.0	0.001	0.0
3142	3153				
		» 0000E+00 P			
3143	3154	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Fltphasefix			
3144	3155	PREFLIGHT	(N/A)		P
		» REFLIGHT P			
3145	3156	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Prdterm			
3146	3157	False	(N/A)		
		» FALSE P			
3147	3158	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(1).Lateral_Offset.Capture_Path_Start_Pt.Firstpass			
3148	3159	FALSE	(N/A)		
		» FALSE P			
3149	3160				
3150	3161				
3151	3162	====> All 11 Comparisons Passed <====			
3152	3163				
3153	3164				
3154	3165	TESTID: 32			
3155	3166				
3156	3167	If the first legs match, then the Lateral Offset Data Point data shall be copied from Perf's working data to the appro			
		» priate			
3157	3168	Active or Secondary LGB header. This test case is written to cover the			
3158	3169	sdd anchor PERF_SDD_3968_INT. Prf_Bkgnd_Pkg.Put_Bk_Data consist Store out the Lateral Offset Data Points,			
3159	3170	In this test case Active Alternate Primary Flt Plan and CAPTURE_PATH_END Lateral Offset Data Points are considered			
3160	3171	(PERF_SDD_3968_INT)			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3161	3172		
3162	3173		
3163	3174	INPUT	VALUE
3164	3175	-----	-----
		» -----	
3165	3176	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx	
		» 2	
3166	3177	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change	
		» False	
3167	3178	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	
		» False	
3168	3179	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	
		» False	
3169	3180	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	
		» False	
3170	3181	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	
		» False	
3171	3182	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	
		» False	
3172	3183	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	
		» False	
3173	3184	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)	
		» 2	
3174	3185	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest	
		» 0.0	
3175	3186	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo	
		» 0.0	
3176	3187	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	
		» True	
3177	3188	Perf_Background_DPkg.Opt_Step_Data.Distodest	
		» 25.0	
3178	3189	Perf_Background_DPkg.Opt_Step_Data.Timetogo	
		» 5.0	
3179	3190	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed	
		» 0.0	
3180	3191	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel	
		» 0.0	
3181	3192	Perf_Background_Dpkg.Pshmpreddata.Speed	
		» 250.0	
3182	3193	Perf_Background_Dpkg.Pshmpreddata.Fuel	
		» 50.0	
3183	3194	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid	
		» False	
3184	3195	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» 0.0	
3185	3196	Perf_Background_Dpkg.Pcoptalt.Valid	
		» True	
3186	3197	Perf_Background_Dpkg.Pcoptalt.Data	
		» 19000.0	
3187	3198	Fmcs_Partition_Data_Pkg.Ops_Master_Status	
		» Master	
3188	3199	Ctp_Perf_bkgnd_put_bk_data.Boot_Status	Co
		» ld_Start	
3189	3200	Perf_Background_Dpkg.Preds_Output(Active)	
		» True	
3190	3201	Perf_Background_Dpkg.Psfinalalt	
		» 0.0	
3191	3202	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt	
		» 5000	
3192	3203	Perf_Background_Dpkg.Psfpolfnlful	
		» 0.0	
3193	3204	Perf_Background_Dpkg.Psfpolfnltme	
		» 0.0	
3194	3205	Perf_Background_Dpkg.Psfpolfnltg	
		» 0.0	
3195	3206	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel	
		» 40	
3196	3207	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	
		» 50	
3197	3208	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Percent	
		» 100.0	
3198	3209	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Upper_Limit	
		» 4.0	
3199	3210	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Lower_Limit	
		» 1.0	
3200	3211	Options_And_Data_Pkg:body.All_Options.Ats_Enable	
		» True	
3201	3212	Options_And_Data_Pkg:body.All_Options.Alt_n_Trip_In_Rsv_Enb	
		» True	
3202	3213	Options_And_Data_Pkg:body.All_Options.Cmp_Rsv_In_Flt_Enb	
		» True	
3203	3214	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	
		» 60	
3204	3215	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
3205	3216	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
3206	3217	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» True	
3207	3218	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
3208	3219	Perf_Background_Dpkg.Pcfltphase	P
		» reflight	
3209	3220	Perf_Background_Dpkg.Psfinaldes	
		» True	
3210	3221	Perf_Background_Dpkg.Vert_Auto_Mode	
		» True	
3211	3222	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	
3212	3223	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 55000.0	
3213	3224	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	
3214	3225	Perf_background_Dpkg.Maxalt.Num_Engout	
		» 0	
3215	3226	Perf_Background_Dpkg.Etp_Itin_Ran	
		» True	
3216	3227	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» False	
3217	3228	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» False	
3218	3229	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
3219	3230	Perf_Background_Dpkg.Pcitin.Flight_Plan	
		» Active	
3220	3231	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
3221	3232	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst	
		» False	
3222	3233	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress	
		» False	
3223	3234	Perf_Background_Dpkg.Pcgmtime.Gpc_Time	
		» 2	
3224	3235	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	
		» 0	
3225	3236	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq	
		» 0	
3226	3237	Perf_Background_Dpkg.Psprddataseq	
		» 3	
3227	3238	Perf_Dpkg.Pstopofcrzfl(Active).Valid	
		» False	
3228	3239	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» True	
3229	3240	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	
		» False	
3230	3241	Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest	
		» "P"	
3231	3242	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status	Perf_Int_Base_Tpkg.Dua
		» l_Master	
3232	3243	Perf_Background_Dpkg.Ats_Enable	
		» False	
3233	3244	Perf_Background_Dpkg.Psrsvaltn	
		» False	
3234	3245	Perf_Background_Dpkg.Psrsvinflt	
		» False	
3235	3246	Perf_Background_Dpkg.Psrtersvpctg	
		» 0.0	
3236	3247	Perf_Background_Dpkg.Psmaxrtersv	
		» 0.0	
3237	3248	Perf_Background_Dpkg.Psminrtersv	
		» 0.0	
3238	3249	Perf_Background_Dpkg.Ref_Flight_Plan	
		» 2	
3239	3250	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).PRDTAS	
		» 66.0	
3240	3251	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Prd_Wind_Mag	
		» 66.0	
3241	3252	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Prd_Wind_True_Brg	
		» 68.0	
3242	3253	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Prddataseq	
		» 5	
3243	3254	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Prdalt	
		» 1000.0	
3244	3255	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Prdgwttofix	
		» 69.0	
3245	3256	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Fixdistodest	
		» 70.0	
3246	3257	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Fixdtdbias	
		» 80.0	
3247	3258	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Fltphasefix	P
		» REFLIGHT	
3248	3259	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Prdterm	
		» TRUE	
3249	3260	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.CAPTURE_PATH_END).Firstpass	
		» FALSE	
3250	3261	Change	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3251	3262	» False			
		Change			
3252	3263	» False			
		Change			
3253	3264	» False			
3254	3265				
3255	3266	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
3256	3267	-----	-----	-----	-----
		» -----			
3257	3268	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.PRDTAS			
3258	3269		0.0	0.001	0.0
		» 0000E+00 P			
3259	3270	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Prd_Wind_Mag			
3260	3271		0.0	0.001	0.0
		» 0000E+00 P			
3261	3272	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Prd_Wind_True_Brg			
3262	3273		0.0	0.001	0.0
		» 0000E+00 P			
3263	3274	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Prddataseq			
3264	3275		0	(N/A)	
		» 0 P			
3265	3276	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Prdalt			
3266	3277		0.0	0.001	0.0
		» 0000E+00 P			
3267	3278	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Prdgwttofix			
3268	3279		0.0	0.001	0.0
		» 0000E+00 P			
3269	3280	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Fixdistodest			
3270	3281		0.0	0.001	0.0
		» 0000E+00 P			
3271	3282	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Fixdtdbias			
3272	3283		0.0	0.001	0.0
		» 0000E+00 P			
3273	3284	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Fltphasefix			
3274	3285		PREFLIGHT	(N/A)	P
		» REFLIGHT P			
3275	3286	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Prdterm			
3276	3287		False	(N/A)	
		» FALSE P			
3277	3288	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(2).Lateral_Offset.Capture_Path_End_Pt.Firstpass			
3278	3289		FALSE	(N/A)	
		» FALSE P			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

3279 3290
3280 3291
3281 3292 ===== All 11 Comparisons Passed =====
3282 3293
3283 3294
3284 3295 TESTID: 33
3285 3296
3286 3297 If the first legs match, then the Lateral Offset Data Point data shall be copied from Perf's working data to the appro
    » priate
3287 3298 Active or Secondary LGB header. This test case is written to cover the
3288 3299 sdd anchor PERF_SDD_3968_INT. Prf_Bkgnd_Pkg.Put_Bk_Data consist Store out the Lateral Offset Data Points,
3289 3300 In this test case Secondary Primary Flt Plan and RETURN_PATH_START Lateral Offset Data Points are considered
3290 3301 (PERF_SDD_3968_INT)
3291 3302
3292 3303
3293 3304 INPUT
3294 3305 -----
    » -----
3295 3306 Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx
    » 2
3296 3307 Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change
    » False
3297 3308 Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec
    » False
3298 3309 Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec
    » False
3299 3310 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec
    » False
3300 3311 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec
    » False
3301 3312 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec
    » False
3302 3313 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
    » False
3303 3314 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
    » 2
3304 3315 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
    » 0.0
3305 3316 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
    » 0.0
3306 3317 Perf_Etp_DPKG:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog
    » True
3307 3318 Perf_Background_DPKG.Opt_Step_Data.Distodest
    » 25.0

```



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3308	3319	Perf_Background_Dpkg.Opt_Step_Data.Timetogo
		» 5.0
3309	3320	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
		» 0.0
3310	3321	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
		» 0.0
3311	3322	Perf_Background_Dpkg.Pshmpreddata.Speed
		» 250.0
3312	3323	Perf_Background_Dpkg.Pshmpreddata.Fuel
		» 50.0
3313	3324	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
		» False
3314	3325	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
		» 0.0
3315	3326	Perf_Background_Dpkg.Pcoptalt.Valid
		» True
3316	3327	Perf_Background_Dpkg.Pcoptalt.Data
		» 19000.0
3317	3328	Fmcs_Partition_Data_Pkg.Ops_Master_Status
		» Master
3318	3329	Ctp_Perf_bkgnd_put_bk_data.Boot_Status
		» ld_Start
3319	3330	Perf_Background_Dpkg.Preds_Output(Active)
		» True
3320	3331	Perf_Background_Dpkg.Psfinalalt
		» 0.0
3321	3332	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt
		» 5000
3322	3333	Perf_Background_Dpkg.Psfpolfnlful
		» 0.0
3323	3334	Perf_Background_Dpkg.Psfpolfnltme
		» 0.0
3324	3335	Perf_Background_Dpkg.Psfpolfnltg
		» 0.0
3325	3336	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel
		» 40
3326	3337	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time
		» 50
3327	3338	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Percent
		» 100.0
3328	3339	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Upper_Limit
		» 4.0
3329	3340	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Lower_Limit
		» 1.0

Co

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3330	3341	Options_And_Data_Pkg:body.All_Options.Ats_Enable	
		» True	
3331	3342	Options_And_Data_Pkg:body.All_Options.Alt_Trip_In_Rsv_Enb	
		» True	
3332	3343	Options_And_Data_Pkg:body.All_Options.Cmp_Rsv_In_Flt_Enb	
		» True	
3333	3344	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	
		» 60	
3334	3345	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
3335	3346	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
3336	3347	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» True	
3337	3348	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
3338	3349	Perf_Background_Dpkg.Pcfltphase	P
		» reflight	
3339	3350	Perf_Background_Dpkg.Psfinaldes	
		» True	
3340	3351	Perf_Background_Dpkg.Vert_Auto_Mode	
		» True	
3341	3352	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	
3342	3353	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 55000.0	
3343	3354	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	
3344	3355	Perf_background_Dpkg.Maxalt.Num_Engout	
		» 0	
3345	3356	Perf_Background_Dpkg.Etp_Itin_Ran	
		» True	
3346	3357	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» False	
3347	3358	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» False	
3348	3359	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
3349	3360	Perf_Background_Dpkg.Pcitin.Flight_Plan	
		» Active	
3350	3361	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
3351	3362	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst	
		» False	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3352	3363	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress	
		» False	
3353	3364	Perf_Background_Dpkg.Pcgmtime.Gpc_Time	
		» 2	
3354	3365	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	
		» 0	
3355	3366	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq	
		» 0	
3356	3367	Perf_Background_Dpkg.Psprddataseq	
		» 3	
3357	3368	Perf_Dpkg.Pstopofcrzfl(Active).Valid	
		» False	
3358	3369	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc	
		» True	
3359	3370	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	
		» False	
3360	3371	Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest	
		» "P"	
3361	3372	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status	Perf_Int_Base_Tpkg.Dua
		» 1_Master	
3362	3373	Perf_Background_Dpkg.Ats_Enable	
		» False	
3363	3374	Perf_Background_Dpkg.Psrsvltn	
		» False	
3364	3375	Perf_Background_Dpkg.Psrsvinfl	
		» False	
3365	3376	Perf_Background_Dpkg.Psrtersvpctg	
		» 0.0	
3366	3377	Perf_Background_Dpkg.Psmxrtersv	
		» 0.0	
3367	3378	Perf_Background_Dpkg.Psminrtersv	
		» 0.0	
3368	3379	Perf_Background_Dpkg.Ref_Flight_Plan	
		» 3	
3369	3380	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).PRDTAS	
		» 67.0	
3370	3381	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Prd_Wind_Mag	
		» 66.0	
3371	3382	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Prd_Wind_True_Brg	
		» 68.0	
3372	3383	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Prddataseq	
		» 5	
3373	3384	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Prdalt	
		» 1000.0	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3374	3385	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Prdgwttofix			
		» 69.0			
3375	3386	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Fixdistodest			
		» 70.0			
3376	3387	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Fixdtdbias			
		» 80.0			
3377	3388	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Fltphasefix			P
		» REFLIGHT			
3378	3389	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Prdterm			
		» TRUE			
3379	3390	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_START).Firstpass			
		» FALSE			
3380	3391	Change			
		» False			
3381	3392	Change			
		» False			
3382	3393	Change			
		» False			
3383	3394				
3384	3395				
3385	3396	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
3386	3397	-----	-----	-----	-----
		» -----			
3387	3398	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.PRDTAS			
3388	3399		0.0	0.001	0.0
		» 0000E+00 P			
3389	3400	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Prd_Wind_Mag			
3390	3401		0.0	0.001	0.0
		» 0000E+00 P			
3391	3402	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Prd_Wind_True_Brg			
3392	3403		0.0	0.001	0.0
		» 0000E+00 P			
3393	3404	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Prddataseq			
3394	3405		0	(N/A)	
		» 0 P			
3395	3406	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Prdalt			
3396	3407		0.0	0.001	0.0
		» 0000E+00 P			
3397	3408	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Prdgwttofix			
3398	3409		0.0	0.001	0.0
		» 0000E+00 P			
3399	3410	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Fixdistodest			
3400	3411		0.0	0.001	0.0

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3401	3412	» 0000E+00 P			
3402	3413	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Fixdtdbias	0.0	0.001	0.0
3403	3414	» 0000E+00 P			
3404	3415	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Fltphasefix		(N/A)	P
3405	3416	» REFLIGHT P			
3406	3417	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Prdterm	False	(N/A)	
3407	3418	» FALSE P			
3408	3419	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(3).Lateral_Offset.Return_Path_Start_Pt.Firstpass	FALSE	(N/A)	
3409	3420	» FALSE P			
3410	3421				
3411	3422	====> All 11 Comparisons Passed <====			
3412	3423				
3413	3424				
3414	3425	TESTID: 34			
3415	3426				
3416	3427	If the first legs match, then the Lateral Offset Data Point data shall be copied from Perf's working data to the appro			
3417	3428	» priate			
3418	3429	Active or Secondary LGB header. This test case is written to cover the			
3419	3430	sdd anchor PERF_SDD_3968_INT. Prf_Bkgnd_Pkg.Put_Bk_Data consist Store out the Lateral Offset Data Points,			
3420	3431	In this test case Secondary Alternate Primary Flt Plan and RETURN_PATH_END Lateral Offset Data Points are considere			
3421	3432	» d			
3422	3433	(PERF_SDD_3968_INT)			
3423	3434	INPUT			VALUE
3424	3435	-----			
3425	3436	» -----			
3426	3437	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx			
3427	3438	» 2			
3428	3439	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change			
3429	3440	» False			
3430	3441	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec			
		» False			
		Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec			
		» False			
		Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec			
		» False			
		Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec			
		» False			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

3431 3442 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec
      » False
3432 3443 Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec
      » False
3433 3444 Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)
      » 2
3434 3445 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
      » 0.0
3435 3446 Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
      » 0.0
3436 3447 Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog
      » True
3437 3448 Perf_Background_DPkg.Opt_Step_Data.Distodest
      » 25.0
3438 3449 Perf_Background_DPkg.Opt_Step_Data.Timetogo
      » 5.0
3439 3450 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
      » 0.0
3440 3451 Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
      » 0.0
3441 3452 Perf_Background_Dpkg.Pshmpreddata.Speed
      » 250.0
3442 3453 Perf_Background_Dpkg.Pshmpreddata.Fuel
      » 50.0
3443 3454 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
      » False
3444 3455 Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
      » 0.0
3445 3456 Perf_Background_Dpkg.Pcoptalt.Valid
      » True
3446 3457 Perf_Background_Dpkg.Pcoptalt.Data
      » 19000.0
3447 3458 Fmcs_Partition_Data_Pkg.Ops_Master_Status
      » Master
3448 3459 Ctp_Perf_bkgnd_put_bk_data.Boot_Status
      » ld_Start
3449 3460 Perf_Background_Dpkg.Preds_Output(Active)
      » True
3450 3461 Perf_Background_Dpkg.Psfinalalt
      » 0.0
3451 3462 Options_And_Data_Pkg:body.Numeric_Data.Final_Alt
      » 5000
3452 3463 Perf_Background_Dpkg.Psfpolfnlful
      » 0.0

```

Co

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3453	3464	Perf_Background_Dpkg.Psfpolfnltme	
		» 0.0	
3454	3465	Perf_Background_Dpkg.Psfpolfnltg	
		» 0.0	
3455	3466	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel	
		» 40	
3456	3467	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	
		» 50	
3457	3468	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Percent	
		» 100.0	
3458	3469	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Upper_Limit	
		» 4.0	
3459	3470	Options_And_Data_Pkg:body.Numeric_Data.Route_Reserve_Lower_Limit	
		» 1.0	
3460	3471	Options_And_Data_Pkg:body.All_Options.Ats_Enable	
		» True	
3461	3472	Options_And_Data_Pkg:body.All_Options.Alt_Trip_In_Rsv_Enb	
		» True	
3462	3473	Options_And_Data_Pkg:body.All_Options.Cmp_Rsv_In_Flt_Enb	
		» True	
3463	3474	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plmg_Final_Time	
		» 60	
3464	3475	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
3465	3476	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
3466	3477	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» True	
3467	3478	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
3468	3479	Perf_Background_Dpkg.Pcfltphase	P
		» reflight	
3469	3480	Perf_Background_Dpkg.Psfinaldes	
		» True	
3470	3481	Perf_Background_Dpkg.Vert_Auto_Mode	
		» True	
3471	3482	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	
3472	3483	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 55000.0	
3473	3484	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	
3474	3485	Perf_background_Dpkg.Maxalt.Num_Engout	
		» 0	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3475	3486	Perf_Background_Dpkg.Etp_Itin_Ran	
		» True	
3476	3487	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» False	
3477	3488	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» False	
3478	3489	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
3479	3490	Perf_Background_Dpkg.Pcitin.Flight_Plan	
		» Active	
3480	3491	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
3481	3492	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfregst	
		» False	
3482	3493	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress	
		» False	
3483	3494	Perf_Background_Dpkg.Pcgmtime.Gpc_Time	
		» 2	
3484	3495	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	
		» 0	
3485	3496	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq	
		» 0	
3486	3497	Perf_Background_Dpkg.Psprddataseq	
		» 3	
3487	3498	Perf_Dpkg.Pstopofcrzfl(Active).Valid	
		» False	
3488	3499	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc	
		» True	
3489	3500	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	
		» False	
3490	3501	Options_And_Data_Pkg:body.Alpha_Data.Fuel_Pred_Final_Dest	
		» "p"	
3491	3502	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status	Perf_Int_Base_Tpkg.Dua
		» l_Master	
3492	3503	Perf_Background_Dpkg.Ats_Enable	
		» False	
3493	3504	Perf_Background_Dpkg.Psrsvaltn	
		» False	
3494	3505	Perf_Background_Dpkg.Psrsvinflt	
		» False	
3495	3506	Perf_Background_Dpkg.Psrtersvpctg	
		» 0.0	
3496	3507	Perf_Background_Dpkg.Psmaxrtersv	
		» 0.0	



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3497	3508	Perf_Background_Dpkg.Psminrtersv			
		» 0.0			
3498	3509	Perf_Background_Dpkg.Ref_Flight_Plan			
		» 4			
3499	3510	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).PRDTAS			
		» 68.0			
3500	3511	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Prd_Wind_Mag			
		» 66.0			
3501	3512	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Prd_Wind_True_Brg			
		» 68.0			
3502	3513	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Prddataseq			
		» 5			
3503	3514	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Prdalt			
		» 1000.0			
3504	3515	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Prdgwttofix			
		» 69.0			
3505	3516	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Fixdistodest			
		» 70.0			
3506	3517	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Fixdtdbias			
		» 80.0			
3507	3518	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Fltphasefix			P
		» REFLIGHT			
3508	3519	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Prdterm			
		» TRUE			
3509	3520	Perf_Background_Dpkg.Offset_Data_Pts(Lateral_Offset_Segment_Type_Tpkg.RETURN_PATH_END).Firstpass			
		» FALSE			
3510	3521	Change			
		» False			
3511	3522	Change			
		» False			
3512	3523	Change			
		» False			
3513	3524				
3514	3525				
3515	3526	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
3516	3527	-----	-----	-----	-----
		» -----			
3517	3528	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.PRDTAS			
3518	3529		0.0	0.001	0.0
		» 0000E+00 P			
3519	3530	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Prd_Wind_Mag			
3520	3531		0.0	0.001	0.0
		» 0000E+00 P			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3521	3532	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Prd_Wind_True_Brg			
3522	3533		0.0	0.001	0.0
		» 0000E+00 P			
3523	3534	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Prddataseq			
3524	3535		0	(N/A)	
		» 0 P			
3525	3536	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Prdalt			
3526	3537		0.0	0.001	0.0
		» 0000E+00 P			
3527	3538	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Prdgtwtofix			
3528	3539		0.0	0.001	0.0
		» 0000E+00 P			
3529	3540	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Fixdistodest			
3530	3541		0.0	0.001	0.0
		» 0000E+00 P			
3531	3542	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Fixdtdbias			
3532	3543		0.0	0.001	0.0
		» 0000E+00 P			
3533	3544	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Fltphasefix			
3534	3545		PREFLIGHT	(N/A)	P
		» REFLIGHT P			
3535	3546	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Prdterm			
3536	3547		False	(N/A)	
		» FALSE P			
3537	3548	CTP_PERF_BKGND_PUT_BK_DATA.Guidhdrarray(4).Lateral_Offset.Return_Path_End_Pt.Firstpass			
3538	3549		FALSE	(N/A)	
		» FALSE P			
3539	3550				
3540	3551				
3541	3552	====> All 11 Comparisons Passed <====			
3542	3553				
3543	3554				
3544	3555	TESTID: 35			
3545	3556				
3546	3557	Itin is active primary but Src_Idx equals the Chk_Idx and the perf request flag is set true so information is not outp			
		» uted.			
3547	3558	(PERF_SDD_2631_INT,PERF_SDD_4543_INT)			
3548	3559	The ETP predictions-in-progress flag will hold TRUE Value initialised in Input Since			
3549	3560	1)the current itinerary is the Active Primary Flight Plan Predictions			
3550	3561	2)the ETP-itinerary-has-run flag is True			
3551		<del>The ETP itinerary has run flag is then reset to false.</del>			
3552		<del>Here there is perf restart request hence the flags not reset.</del>			
3553	3562	(PERF_SDD_3155_INT)			
3554	3563				

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3555	3564	If the current itinerary is Active Primary Flight Plan Predictions, then the last Cruise flight level	
3556	3565	shall be sent to IO for output when the flight plan has been completely predicted.	
3557	3566	(PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))	
3558	3567		
3559	3568		
3560	3569	INPUT	VALUE
3561	3570	-----	-----
		» -----	
3562	3571	Perf_Background_Dpkg.Pcactorsec	Fprequestrec_Types.T
		» emporary	
3563	3572	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr	
		» 0	
3564	3573	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change	
		» False	
3565	3574	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	
		» False	
3566	3575	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	
		» False	
3567	3576	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	
		» False	
3568	3577	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	
		» False	
3569	3578	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	
		» False	
3570	3579	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	
		» False	
3571	3580	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)	
		» 0	
3572	3581	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest	
		» 0.0	
3573	3582	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo	
		» 0.0	
3574	3583	Perf_Etp_DPkg:body.Data.Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	
		» True	
3575	3584	Perf_Background_DPkg.Opt_Step_Data.Distodest	
		» 25.0	
3576	3585	Perf_Background_DPkg.Opt_Step_Data.Timetogo	
		» 5.0	
3577	3586	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed	
		» 0.0	
3578	3587	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel	
		» 0.0	
3579	3588	Perf_Background_Dpkg.Pshmpreddata.Speed	
		» 250.0	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3580	3589	Perf_Background_Dpkg.Pshmpreddata.Fuel	
		» 50.0	
3581	3590	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid	
		» False	
3582	3591	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data	
		» 0.0	
3583	3592	Perf_Background_Dpkg.Pcoptalt.Valid	
		» True	
3584	3593	Perf_Background_Dpkg.Pcoptalt.Data	
		» 19000.0	
3585	3594	Fmcs_Partition_Data_Pkg.Ops_Master_Status	
		» Master	
3586	3595	Ctp_Perf_bkgnd_put_bk_data.Boot_Status	Wa
		» rm_Start	
3587	3596	Perf_Background_Dpkg.Preds_Output(Active)	
		» True	
3588	3597	Perf_Background_Dpkg.Psfinalalt	
		» 0.0	
3589	3598	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt	
		» 5000	
3590	3599	Perf_Background_Dpkg.Psfpolfnlful	
		» 0.0	
3591	3600	Perf_Background_Dpkg.Psfpolfnltme	
		» 0.0	
3592	3601	Perf_Background_Dpkg.Psfpolfnltg	
		» 0.0	
3593	3602	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel	
		» 40	
3594	3603	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	
		» 50	
3595	3604	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	
		» 60	
3596	3605	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
3597	3606	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
3598	3607	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» False	
3599	3608	Perf_Background_Dpkg.Pcfpln	Scr
		» atchFpln	
3600	3609	Perf_Background_Dpkg.Pcfltphase	
		» Cruise	
3601	3610	Perf_Background_Dpkg.Psfinaldes	
		» True	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3602	3611	Perf_Background_Dpkg.Vert_Auto_Mode
		» True
3603	3612	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data
		» 50000.0
3604	3613	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data
		» 55000.0
3605	3614	Perf_background_Dpkg.Maxalt.Gwt
		» 150000.0
3606	3615	Perf_background_Dpkg.Maxalt.Num_Engout
		» 0
3607	3616	Perf_Background_Dpkg.Etp_Itin_Ran
		» True
3608	3617	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid
		» False
3609	3618	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid
		» False
3610	3619	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode
		» Single
3611	3620	Perf_Dpkg.Pstopofcrzfl(Active).Valid
		» False
3612	3621	Perf_Background_Dpkg.Pcitin.Flight_Plan
		» Active
3613	3622	Perf_Background_Dpkg.Pcitin.Itinerary
		» ln_Preds
3614	3623	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst
		» False
3615	3624	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress
		» False
3616	3625	Perf_Background_Dpkg.Pcgmtime.Gpc_Time
		» 2
3617	3626	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt
		» 0
3618	3627	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq
		» 0
3619	3628	Perf_Background_Dpkg.Psprddataseq
		» 3
3620	3629	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc
		» True
3621	3630	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid
		» False
3622	3631	Change
		» False
3623	3632	Chk_Idx
		» 0

Prim\_Fp

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3624	3633	Sys_Perf_Interface_Dpkg:body.Data.Storage.Psperfreqst			
		» True			
3625	3634				
3626	3635				
3627	3636	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
3628	3637	-----	-----	-----	-----
		» -----			
3629	3638	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr	0	(N/A)	
		» 0 P			
3630	3639	Perf_Etp_DPkg:body.Data.Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	True	(N/A)	
		» TRUE P			
	3640	Perf_Background_Dpkg.Etp_Itin_Ran	True	(N/A)	
		» TRUE P			
3631	3641	Perf_Background_Dpkg.Psffinalalt	0.0	0.001	0.0
		» 0000E+00 P			
3632	3642	Perf_Background_Dpkg.Psfpolfnlful	0.0	0.001	0.0
		» 0000E+00 P			
3633	3643	Perf_Background_Dpkg.Psfpolfnltme	0.0	0.001	0.0
		» 0000E+00 P			
3634	3644	Perf_Background_Dpkg.Psfpolfnltg	0.0	0.001	0.0
		» 0000E+00 P			
3635	3645	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)	
		» FALSE P			
3636	3646				
3637	3647				
3638		====> All 7 Comparisons Passed <====			
	3648	====> All 8 Comparisons Passed <====			
3639	3649				
3640	3650				
3641	3651	TESTID: 36			
3642	3652				
3643	3653	Itin is active primary and Src_Idx equals Chk_Idx and the perf request flag is set true so information is not outputed			
		» .			
3644	3654	LGB index of the dest leg of Scratch fpln is set equal Critical index destwpt.and Aircraft Level change Autocontrol			
3645	3655	Flag is set False.			
3646	3656	(PERF_SDD_2631_INT,PERF_SDD_4543_INT)			
3647	3657				
3648	3658	The ETP predictions-in-progress flag shall hold True since all of the following conditions are not met			
3649	3659	1)the current itinerary is the Active Primary Flight Plan Predictions			
3650	3660	2)the ETP-itinerary-has-run flag is TRUE			
3651	3661	The ETP-itinerary-has-run flag is then reset to false.			
3652	3662	Here there is perf restart request hence the flags not reset.			
3653	3663	(PERF_SDD_3155_INT)			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3654	3664		
3655	3665	If the current itinerary is Active Primary Flight Plan Predictions, then the last Cruise flight level	
3656	3666	shall be sent to IO for output when the flight plan has been completely predicted.	
3657	3667	(PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))	
3658	3668		
3659	3669		
3660	3670	INPUT	VALUE
3661	3671	-----	
		» -----	
3662	3672	Perf_Background_Dpkg.Pcactorsec	Fprequestrec_Types.T
		» temporary	
3663	3673	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr	
		» 0	
3664	3674	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change	
		» False	
3665	3675	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	
		» False	
3666	3676	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	
		» False	
3667	3677	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	
		» False	
3668	3678	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	
		» False	
3669	3679	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	
		» False	
3670	3680	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	
		» False	
3671	3681	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)	
		» 0	
3672	3682	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Destwpt)	
		» 0	
3673	3683	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest	
		» 0.0	
3674	3684	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo	
		» 0.0	
3675	3685	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	
		» True	
3676	3686	Perf_Background_DPkg.Opt_Step_Data.Distodest	
		» 25.0	
3677	3687	Perf_Background_DPkg.Opt_Step_Data.Timetogo	
		» 5.0	
3678	3688	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed	
		» 0.0	
3679	3689	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» 0.0	
3680	3690	Perf_Background_Dpkg.Pshmpreddata.Speed	
		» 250.0	
3681	3691	Perf_Background_Dpkg.Pshmpreddata.Fuel	
		» 50.0	
3682	3692	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid	
		» False	
3683	3693	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data	
		» 0.0	
3684	3694	Perf_Background_Dpkg.Pcoptalt.Valid	
		» True	
3685	3695	Perf_Background_Dpkg.Pcoptalt.Data	
		» 19000.0	
3686	3696	Fmcs_Partition_Data_Pkg.Ops_Master_Status	
		» Master	
3687	3697	Ctp_Perf_bkgnd_put_bk_data.Boot_Status	Wa
		» rm_Start	
3688	3698	Perf_Background_Dpkg.Preds_Output(Active)	
		» True	
3689	3699	Perf_Background_Dpkg.Psfinalalt	
		» 0.0	
3690	3700	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt	
		» 5000	
3691	3701	Perf_Background_Dpkg.Psfpolfnlful	
		» 0.0	
3692	3702	Perf_Background_Dpkg.Psfpolfnltme	
		» 0.0	
3693	3703	Perf_Background_Dpkg.Psfpolfnltg	
		» 0.0	
3694	3704	Perf_Background_Dpkg.Pslcautoctl	
		» False	
3695	3705	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel	
		» 40	
3696	3706	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	
		» 50	
3697	3707	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	
		» 60	
3698	3708	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
3699	3709	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
3700	3710	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» False	
3701	3711	Perf_Background_Dpkg.Pcfpln	



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» atchFpln	
3702	3712	Perf_Background_Dpkg.Pcfltphase	
		» Cruise	
3703	3713	Perf_Background_Dpkg.Psfinaldes	
		» True	
3704	3714	Perf_Background_Dpkg.Vert_Auto_Mode	
		» True	
3705	3715	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	
3706	3716	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 55000.0	
3707	3717	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	
3708	3718	Perf_background_Dpkg.Maxalt.Num_Engout	
		» 0	
3709	3719	Perf_Background_Dpkg.Etp_Itin_Ran	
		» False	
3710	3720	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» False	
3711	3721	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» False	
3712	3722	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
3713	3723	Perf_Dpkg.Pstopofcrzfl(Active).Valid	
		» False	
3714	3724	Perf_Background_Dpkg.Pcitin.Flight_Plan	
		» Active	
3715	3725	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
3716	3726	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst	
		» False	
3717	3727	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress	
		» False	
3718	3728	Perf_Background_Dpkg.Pcgmtime.Gpc_Time	
		» 2	
3719	3729	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	
		» 0	
3720	3730	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq	
		» 0	
3721	3731	Perf_Background_Dpkg.Psprddataseq	
		» 3	
3722	3732	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc	
		» True	
3723	3733	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3724	3734	» False			
		Change			
		» False			
3725	3735	Chk_Idx			
		» 0			
3726	3736	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst			
		» True			
3727	3737				
3728	3738				
3729	3739	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
3730	3740	-----	-----	-----	-----
		» -----			
3731	3741	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr	1	(N/A)	
		» 1 P			
3732	3742	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	True	(N/A)	
		» TRUE P			
3733	3743	Perf_Background_Dpkg.Psfinalalt	0.0	0.001	0.0
		» 0000E+00 P			
3734	3744	Perf_Background_Dpkg.Psfpolfnlful	0.0	0.001	0.0
		» 0000E+00 P			
3735	3745	Perf_Background_Dpkg.Psfpolfnltme	0.0	0.001	0.0
		» 0000E+00 P			
3736	3746	Perf_Background_Dpkg.Psfpolfnltg	0.0	0.001	0.0
		» 0000E+00 P			
3737	3747	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	True	(N/A)	
		» TRUE P			
3738	3748				
3739	3749				
3740	3750	====> All 7 Comparisons Passed <====			
3741	3751				
3742	3752				
3743	3753	TESTID: 37			
3744	3754				
3745	3755	Itin is active primary and Src_Idx equals Chk_Idx and perf request flag is set true so information is not outputed.			
3746	3756	(PERF_SDD_2631_INT)			
3747	3757	The ETP predictions-in-progress flag shall hold True since all of the following conditions are not met			
3748	3758	1)the current itinerary is the Active Primary Flight Plan Predictions			
3749	3759	2)the ETP-itinerary-has-run flag is TRUE			
3750	3760	The ETP-itinerary-has-run flag is then reset to false.			
3751	3761	Here the ETP-itinerary-has-run flag is false hence the flags not reset.			
3752	3762	(PERF_SDD_3155_INT)			
3753	3763				
3754	3764	If the current itinerary is Active Primary Flight Plan Predictions, then the last Cruise flight level			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3755	3765	shall be sent to IO for output when the flight plan has been completely predicted.	
3756	3766	(PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))	
3757	3767	Active flight plan predictions refresh timer is updated by calling Prf_Int_Utils.Update_Refresh_Timer.	
3758	3768	When Number of points are greater than Max refresh point	
3759	3769	(PERF_SDD_3511_INT)	
3760	3770		
3761	3771		
3762	3772	INPUT	VALUE
3763	3773	-----	-----
		» -----	
3764	3774	Perf_Background_Dpkg.Pcactorsec	Fprequestrec_Types.T
		» emporary	
3765	3775	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr	
		» 0	
3766	3776	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change	
		» False	
3767	3777	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	
		» False	
3768	3778	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	
		» False	
3769	3779	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	
		» False	
3770	3780	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	
		» False	
3771	3781	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	
		» False	
3772	3782	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	
		» False	
3773	3783	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)	
		» 0	
3774	3784	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Destwpt)	
		» 0	
3775	3785	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest	
		» 0.0	
3776	3786	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo	
		» 0.0	
3777	3787	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	
		» True	
3778	3788	Perf_Background_DPkg.Opt_Step_Data.Distodest	
		» 25.0	
3779	3789	Perf_Background_DPkg.Opt_Step_Data.Timetogo	
		» 5.0	
3780	3790	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed	
		» 0.0	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3781	3791	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
		» 0.0
3782	3792	Perf_Background_Dpkg.Pshmpreddata.Speed
		» 250.0
3783	3793	Perf_Background_Dpkg.Pshmpreddata.Fuel
		» 50.0
3784	3794	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
		» False
3785	3795	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
		» 0.0
3786	3796	Perf_Background_Dpkg.Pcoptalt.Valid
		» True
3787	3797	Perf_Background_Dpkg.Pcoptalt.Data
		» 19000.0
3788	3798	Fmcs_Partition_Data_Pkg.Ops_Master_Status
		» Master
3789	3799	Ctp_Perf_bkgnd_put_bk_data.Boot_Status
		» rm_Start
3790	3800	Perf_Background_Dpkg.Preds_Output(Active)
		» True
3791	3801	Perf_Background_Dpkg.Psfinalalt
		» 0.0
3792	3802	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt
		» 5000
3793	3803	Perf_Background_Dpkg.Psfpolfnlful
		» 0.0
3794	3804	Perf_Background_Dpkg.Psfpolfnltme
		» 0.0
3795	3805	Perf_Background_Dpkg.Psfpolfnltg
		» 0.0
3796	3806	Perf_Background_Dpkg.Pslcautoctl
		» True
3797	3807	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel
		» 40
3798	3808	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time
		» 50
3799	3809	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time
		» 60
3800	3810	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done
		» True
3801	3811	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid
		» True
3802	3812	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass
		» False

Wa

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3803	3813	Perf_Background_Dpkg.Pcfpln	Scr
		» atchFpln	
3804	3814	Perf_Background_Dpkg.Pcfltphase	
		» Cruise	
3805	3815	Perf_Background_Dpkg.Psfinaldes	
		» True	
3806	3816	Perf_Background_Dpkg.Vert_Auto_Mode	
		» True	
3807	3817	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	
3808	3818	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 55000.0	
3809	3819	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	
3810	3820	Perf_background_Dpkg.Maxalt.Num_Engout	
		» 0	
3811	3821	Perf_Background_Dpkg.Etp_Itin_Ran	
		» False	
3812	3822	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» False	
3813	3823	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» False	
3814	3824	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
3815	3825	Perf_Dpkg.Pstopofcrzfl(Active).Valid	
		» False	
3816	3826	Perf_Background_Dpkg.Pcitin.Flight_Plan	
		» Active	
3817	3827	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
3818	3828	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst	
		» False	
3819	3829	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress	
		» False	
3820	3830	Perf_Background_Dpkg.Pcgmtime.Gpc_Time	
		» 2	
3821	3831	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	
		» 0	
3822	3832	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq	
		» 0	
3823	3833	Perf_Background_Dpkg.Psprddataseq	
		» 3	
3824	3834	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc	
		» True	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3825	3835	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid			
		» False			
3826	3836	Change			
		» False			
3827	3837	Chk_Idx			
		» 0			
3828	3838	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst			
		» True			
3829	3839	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst			
		» False			
3830	3840	Timer.Number_Of_Points			
		» 6			
3831	3841	Timer.Refresh_Time			
		» 20.0			
3832	3842				
3833	3843				
3834	3844	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
3835	3845	-----	-----	-----	-----
		» -----			
3836	3846	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr		1	(N/A)
		» 1 P			
3837	3847	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	True		(N/A)
		» TRUE P			
3838	3848	Perf_Background_Dpkg.Psfinalalt	0.0	0.001	0.0
		» 0000E+00 P			
3839	3849	Perf_Background_Dpkg.Psfpolfnlful	0.0	0.001	0.0
		» 0000E+00 P			
3840	3850	Perf_Background_Dpkg.Psfpolfnltme	0.0	0.001	0.0
		» 0000E+00 P			
3841	3851	Perf_Background_Dpkg.Psfpolfnltg	0.0	0.001	0.0
		» 0000E+00 P			
3842	3852	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	True		(N/A)
		» TRUE P			
3843	3853				
3844	3854				
3845	3855	====> All 7 Comparisons Passed <====			
3846	3856				
3847	3857				
3848	3858	TESTID: 38			
3849	3859				
3850	3860	Itin is active primary and Src_Idx equals Chk_Idx and perf request flag is set True so information do not exist.			
3851	3861	(PERF_SDD_2631_INT)			
3852	3862	The ETP predictions-in-progress flag shall hold True since all of the following conditions are not met			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3853	3863	1)the current itinerary is the Active Primary Flight Plan Predictions	
3854	3864	2)the ETP-itinerary-has-run flag is TRUE	
3855	3865	Here the ETP-itinerary-has-run flag is false hence the flags not reset.	
3856	3866	(PERF_SDD_3155_INT)	
3857	3867		
3858	3868	If the current itinerary is Active Primary Flight Plan Predictions, then the last Cruise flight level	
3859	3869	shall be sent to IO for output when the flight plan has been completely predicted.	
3860	3870	(PERF_SDD_0421(PERF_SRD_2045, PERF_SRD_2051))	
3861	3871	ETT data have been transmitted from the slave FM to the Master when	
3862	3872	- Current Fm is not the master FM in the dual Configuration	
3863	3873	- A valid ETT has been computed on this pass of predictions.	
3864	3874	(PERF_SDD_3518_INT).	
3865	3875	ETT data output processing has been performed	
3866	3876	(PERF_SDD_3515_INT).	
3867	3877		
3868	3878		
3869	3879	INPUT	VALUE
3870	3880	-----	-----
		» -----	
3871	3881	Perf_Background_Dpkg.Pcactorsec	
		» Active	
3872	3882	Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr	
		» 0	
3873	3883	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change	
		» False	
3874	3884	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec	
		» False	
3875	3885	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec	
		» False	
3876	3886	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec	
		» False	
3877	3887	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec	
		» False	
3878	3888	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec	
		» False	
3879	3889	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec	
		» False	
3880	3890	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)	
		» 0	
3881	3891	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Destwpt)	
		» 0	
3882	3892	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest	
		» 0.0	
3883	3893	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» 0.0	
3884	3894	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog	
		» True	
3885	3895	Perf_Background_DPkg.Opt_Step_Data.Distodest	
		» 25.0	
3886	3896	Perf_Background_DPkg.Opt_Step_Data.Timetogo	
		» 5.0	
3887	3897	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed	
		» 0.0	
3888	3898	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel	
		» 0.0	
3889	3899	Perf_Background_Dpkg.Pshmpreddata.Speed	
		» 250.0	
3890	3900	Perf_Background_Dpkg.Pshmpreddata.Fuel	
		» 50.0	
3891	3901	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid	
		» False	
3892	3902	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data	
		» 0.0	
3893	3903	Perf_Background_Dpkg.Pcoptalt.Valid	
		» True	
3894	3904	Perf_Background_Dpkg.Pcoptalt.Data	
		» 19000.0	
3895	3905	Fmcs_Partition_Data_Pkg.Ops_Master_Status	
		» Master	
3896	3906	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status	Perf_Int_Base_Tpkg.Du
		» al_Slave	
3897	3907	Ctp_Perf_bkgnd_put_bk_data.Boot_Status	Wa
		» rm_Start	
3898	3908	Perf_Background_Dpkg.Preds_Output(Active)	
		» True	
3899	3909	Perf_Background_Dpkg.Psfinalalt	
		» 0.0	
3900	3910	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt	
		» 5000	
3901	3911	Perf_Background_Dpkg.Psfpolfnlful	
		» 0.0	
3902	3912	Perf_Background_Dpkg.Psfpolfnltme	
		» 0.0	
3903	3913	Perf_Background_Dpkg.Psfpolfnltg	
		» 0.0	
3904	3914	Perf_Background_Dpkg.Pctcstridx	
		» 1	
3905	3915	Perf_Background_Dpkg.Pslcautoctl	



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

	»	True
3906	3916	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel
	»	40
3907	3917	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time
	»	50
3908	3918	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time
	»	60
3909	3919	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done
	»	True
3910	3920	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid
	»	True
3911	3921	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass
	»	False
3912	3922	Perf_Background_Dpkg.Pcfpln
	»	cprimary
3913	3923	Perf_Background_Dpkg.Pcfltphase
	»	Cruise
3914	3924	Perf_Background_Dpkg.Psfinaldes
	»	True
3915	3925	Perf_Background_Dpkg.Pccompett(Active)
	»	True
3916	3926	Perf_Background_Dpkg.Vert_Auto_Mode
	»	True
3917	3927	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data
	»	50000.0
3918	3928	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data
	»	55000.0
3919	3929	Perf_background_Dpkg.Maxalt.Gwt
	»	150000.0
3920	3930	Perf_background_Dpkg.Maxalt.Num_Engout
	»	0
3921	3931	Perf_Background_Dpkg.Etp_Itin_Ran
	»	False
3922	3932	Perf_Background_Dpkg.Ett(Active).Data
	»	20.0
3923	3933	Perf_Background_Dpkg.Ett(Active).Status
	»	Valid
3924	3934	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid
	»	False
3925	3935	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid
	»	False
3926	3936	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode
	»	Single
3927	3937	Perf_Dpkg.Pstopofcrzfl(Active).Valid

Se



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3951	3961	» 0000E+00 P			
		Perf_Background_Dpkg.Psfpolfnltg	0.0	0.001	0.0
3952	3962	» 0000E+00 P			
		CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)	
3953	3963	» FALSE P			
		Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data	20.0	0.001	2.0
3954	3964	» 0000E+01 P			
		Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status	Valid	(N/A)	
3955	3965	» VALID P			
		Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh	True	(N/A)	
		» TRUE P			
3956	3966				
3957	3967				
3958	3968	====> All 10 Comparisons Passed <====			
3959	3969				
3960	3970				
3961	3971	TESTID: 39			
3962	3972				
3963	3973	Time Constraint Processing :			
3964	3974	Cost Index computation is for Active fpln TIME CSTR.			
3965	3975	Performance Cost index cannot be released to the system, the RTA working and control data have been output			
3966	3976	through the Perf RTA object manager.			
3967	3977	(PERF_SDD_3520_INT).			
3968	3978	Time Constraint Control data is stored out to the object manager after each pass of Predictions			
3969	3979	(PERF_SDD_3106_INT).			
3970	3980	This Test verifies for the output when the data is not transmitted to slave FM, Hence it stores the previous value.			
3971	3981				
3972	3982				
3973	3983	INPUT			VALUE
3974	3984	-----			-----
		» -----			
3975	3985	Perf_Background_Dpkg.Pcitin.Itinerary			Time_Constra
		» int_Eval			
3976	3986	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid			
		» False			
3977	3987	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Adjcostidx			
		» 10.0			
3978	3988	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Lastphase			
		» Descent			
3979	3989	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Glidx			
		» 100			
3980	3990	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Fpln			S
		» econdary			
3981	3991	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Valid			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

3982	3992	» False			
		Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Eval_Done			
		» False			
3983	3993	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Env_Limit			
		» False			
3984	3994	Perf_Background_Dpkg.Pctcstrctrl(Active).Adjcostidx			
		» 20.0			
3985	3995	Perf_Background_Dpkg.Pctcstrctrl(Active).Lastphase			
		» Cruise			
3986	3996	Perf_Background_Dpkg.Pctcstrctrl(Active).Glidx			
		» 2			
3987	3997	Perf_Background_Dpkg.Pcactorsec			
		» Active			
3988	3998	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid			
		» True			
3989	3999	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done			
		» True			
3990	4000	Perf_Background_Dpkg.Pctcstrctrl(Active).Envelope_Limit			
		» True			
3991	4001	Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit			
		» False			
3992	4002				
3993	4003				
3994	4004	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
3995	4005	-----	-----	-----	-----
		» -----			
3996	4006	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Adjcostidx	10.0	0.001	1.0
		» 0000E+01 P			
3997	4007	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Lastphase	Descent	(N/A)	
		» DESCENT P			
3998	4008	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Glidx	100	(N/A)	
		» 100 P			
3999	4009	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Fpln	Secondary	(N/A)	S
		» ECONDARY P			
4000	4010	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Valid	False	(N/A)	
		» FALSE P			
4001	4011	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Eval_Done	False	(N/A)	
		» FALSE P			
4002	4012	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Transfer.Env_Limit	False	(N/A)	
		» FALSE P			
4003	4013	Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit	False	(N/A)	
		» FALSE P			
4004	4014	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	True	(N/A)	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4005	4015	» TRUE P		
		CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg	False	(N/A)
		» FALSE P		
4006	4016			
4007	4017			
4008	4018	====> All 10 Comparisons Passed <====		
4009	4019			
4010	4020			
4011	4021	TESTID: 40		
4012	4022			
4013	4023	In the procedure Prf_Int_Utills.Update_Refresh_Timer updates the passed-in timer's record data. The passed in timer's r		
		» efresh		
4014	4024	time shall be set to the difference between the current FM time and the timer's reference start time, and the timer's		
		» reference		
4015	4025	start time set equal to the current FM time.		
4016	4026	(PERF_SDD_3500_INT)		
4017	4027	A running average of the most recent refresh time data points (up to five) shall be computed and stored in the passed-		
		» in timer's		
4018	4028	record data, along with the actual refresh time data points (up to five) used to compute the average.		
4019	4029	This Test also verifies for the output when the number of points are equal to the maximum refresh points.		
4020	4030	(PERF_SDD_3501_INT)		
4021	4031			
4022	4032			
4023	4033	INPUT		VALUE
4024	4034	-----		-----
		» -----		
4025	4035	Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx		
		» 2		
4026	4036	Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve.Pilot_Entered_Change		
		» False		
4027	4037	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec		
		» False		
4028	4038	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec		
		» False		
4029	4039	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec		
		» False		
4030	4040	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec		
		» False		
4031	4041	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec		
		» False		
4032	4042	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Route_Reserve_Exec		
		» False		
4033	4043	Ctp_Perf_bkgnd_put_bk_data.Guidhdr.Critidx(Firstleg)		
		» 2		

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4034	4044	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Distodest
		» 0.0
4035	4045	Ctp_Perf_bkgnd_put_bk_data.Opt_Step_Data.Timetogo
		» 0.0
4036	4046	Perf_Etp_DPkg:body.Data_Storage.Ckequidata.Data(1).Pack_Vals.Predinprog
		» True
4037	4047	Perf_Background_DPkg.Opt_Step_Data.Distodest
		» 25.0
4038	4048	Perf_Background_DPkg.Opt_Step_Data.Timetogo
		» 5.0
4039	4049	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Speed
		» 0.0
4040	4050	Ctp_Perf_bkgnd_put_bk_data.Pshmpreddata.Fuel
		» 0.0
4041	4051	Perf_Background_Dpkg.Pshmpreddata.Speed
		» 250.0
4042	4052	Perf_Background_Dpkg.Pshmpreddata.Fuel
		» 50.0
4043	4053	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.Valid
		» False
4044	4054	Ctp_Perf_bkgnd_put_bk_data.Pcoptalt.data
		» 0.0
4045	4055	Perf_Background_Dpkg.Pcoptalt.Valid
		» True
4046	4056	Perf_Background_Dpkg.Pcoptalt.Data
		» 19000.0
4047	4057	Fmcs_Partition_Data_Pkg.Ops_Master_Status
		» Master
4048	4058	Ctp_Perf_bkgnd_put_bk_data.Boot_Status
		» rm_Start
4049	4059	Perf_Background_Dpkg.Preds_Output(Active)
		» True
4050	4060	Perf_Background_Dpkg.Psfinalalt
		» 0.0
4051	4061	Options_And_Data_Pkg:body.Numeric_Data.Final_Alt
		» 5000
4052	4062	Perf_Background_Dpkg.Psfpolfnlful
		» 0.0
4053	4063	Perf_Background_Dpkg.Psfpolfnltme
		» 0.0
4054	4064	Perf_Background_Dpkg.Psfpolfnltg
		» 0.0
4055	4065	Options_And_Data_Pkg:body.Numeric_Data.Final_Fuel
		» 40

Wa

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4056	4066	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Pred_Final_Time	
		» 50	
4057	4067	Options_And_Data_Pkg:body.Numeric_Data.Fuel_Plng_Final_Time	
		» 60	
4058	4068	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» True	
4059	4069	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	
4060	4070	Perf_Background_Dpkg.Pctcstrctrl(Active).First_Pass	
		» False	
4061	4071	Perf_Background_Dpkg.Pcfpln	Ac
		» tprimary	
4062	4072	Perf_Background_Dpkg.Pcfltphase	
		» Cruise	
4063	4073	Perf_Background_Dpkg.Psfinaldes	
		» True	
4064	4074	Perf_Background_Dpkg.Vert_Auto_Mode	
		» True	
4065	4075	Perf_background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 50000.0	
4066	4076	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 55000.0	
4067	4077	Perf_background_Dpkg.Maxalt.Gwt	
		» 150000.0	
4068	4078	Perf_background_Dpkg.Maxalt.Num_Engout	
		» 0	
4069	4079	Perf_background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» False	
4070	4080	Perf_background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» False	
4071	4081	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Single	
4072	4082	Perf_Dpkg.Pstopofcrzfl(Active).Valid	
		» False	
4073	4083	Perf_Background_Dpkg.Pcitin.Flight_Plan	
		» Active	
4074	4084	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
4075	4085	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst	
		» False	
4076	4086	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress	
		» False	
4077	4087	Perf_Background_Dpkg.Pcgmtime.Gpc_Time	
		» 2	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4078	4088	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt			
		» 0			
4079	4089	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq			
		» 0			
4080	4090	Perf_Background_Dpkg.Psprddataseq			
		» 3			
4081	4091	Perf_Background_Dpkg.Etp_Itin_Ran			
		» False			
4082	4092	cdk_fuel_weight_dpkg:body.fpln_data(active).block_calc			
		» True			
4083	4093	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid			
		» False			
4084	4094	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Number_Of_Points			
		» 5			
4085	4095	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(1)			
		» 4.0			
4086	4096	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(2)			
		» 3.0			
4087	4097	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Avg_Refresh_Time_Data(3)			
		» 2.0			
4088	4098	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Average_Refresh_Time			
		» 0.0			
4089	4099	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Start_Time			
		» 0			
4090	4100	Fmcs_Partition_Data_Pkg.Ops_Time.Gpc_Time			
		» 20			
4091	4101	Ops_Timer_Pkg:body.Ops_time.Gpc_Time			
		» 30			
4092	4102	Change			
		» False			
4093	4103				
4094	4104				
4095	4105	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
4096	4106	-----	-----	-----	-----
		» -----			
4097	4107	Timer.Start_Time	30	(N/A)	
		» 30 P			
4098	4108	Timer.Refresh_Time	0.001	0.001	1.0
		» 0000E-03 P			
4099	4109	Perf_Dpkg.Refresh_Timers.Flight_Plan_Preds.Average_Refresh_Time	1.0002	0.001	1.0
		» 0020E+00 P			
4100	4110				
4101	4111				



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

4102 4112 ===== All 3 Comparisons Passed =====
4103 4113
4104 4114
4105 4115 TESTID: 41
4106 4116
4107 4117 All the inflection points stored in Flight Planning Working Layer shall be moved to
4108 4118 Flight Planning Active Layer as follows:
4109 4119     Get write point access to the Flight Plan being modified by calling routine
4110 4120     Perf_Lgb_Interface_Mgr_Pkg.Requestlgb
4111 4121     if the current executing itinerary is Primary Fpln Prediction, then
4112 4122     Activate Strategic Working Point List for the Flight Plan being modified by calling routine
4113 4123     Fpp_Wrap_Point_Pkg.Activate_Strategic_Working_Point_List.
4114 4124     if the current executing itinerary is Current_Mode_Preds or Current_Mode_Hi_Pri, then
4115 4125     Activate Tactical Working Point List for the Flight Plan being modified by calling routine
4116 4126     Fpp_Wrap_Point_Pkg.Activate_Tactical_Working_Point_List.
4117 4127     Release write point access to the Flight Plan being modified by calling routine
4118 4128     Perf_Lgb_Interface_Mgr_Pkg.Releaselgb
4119 4129 PERF_SDD_7018, PERF_SDD_07154
4120 4130
4121 4131 If the current itinerary is Active Primary Flight Plan Predictions,
4122 4132 then utility procedure Prf_Int_Utils.Align_Segments_At_Leg with inputs of active leg index
4123 4133 shall be called within the same LGB access for activating the strategic inflection points.
4124 4134 PERF_SDD_07527
4125 4135
4126 4136
4127 4137 INPUT VALUE
4128 4138 -----
4129 4139 Perf_Background_Dpkg.Pcitin.Itinerary Perf_Int_Base_Tpkg.Prim_Fp
4130 4140 » ln_Preds
4131 4141 Perf_Background_Dpkg.Psstepover
4132 4142 » False
4133 4143 Perf_Background_Dpkg.Pcitin.Flight_Plan
4134 4144 » Active
4135 4145 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst
4136 4146 » False
4137 4147
4138 4148 define Request_LGB_Called := FALSE
4139 4149 define Activate_Strategic_Working_Point_List_Called := FALSE
4140 4150 define Align_Segments_At_Leg_Exec := False
4141 4151 define Activate_Tactical_Working_Point_List_Called := FALSE
4142 4152 define Releaselgb_Called := FALSE
4143 4153 define Request_LGB_Called := TRUE

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

4141 4151 define Align_Segments_At_Leg_Exec := True
4142 4152 define Activate_Strategic_Working_Point_List_Called := TRUE
4143 4153 define Activate_Tactical_Working_Point_List_Called := TRUE
4144 4154 define ReleaseLgb_Called := TRUE
4145 4155 define Request_LGB_Called := TRUE
4146 4156 define Align_Segments_At_Leg_Exec := True
4147 4157 define Activate_Strategic_Working_Point_List_Called := TRUE
4148 4158 define ReleaseLgb_Called := TRUE
4149 4159
4150 4160
4151 4161 OUTPUT                                EXPECTED                                TOLERANCE                                ACTUAL
      »                P/F
4152 4162 -----
      » -----
4153 4163 Request_LGB_Called                                TRUE                                (N/A)
      »      TRUE  P
4154 4164 Activate_Strategic_Working_Point_List_Called    TRUE                                (N/A)
      »      TRUE  P
4155 4165 Activate_Tactical_Working_Point_List_Called    FALSE                               (N/A)
      »      FALSE P
4156 4166 ReleaseLgb_Called                                TRUE                                (N/A)
      »      TRUE  P
4157 4167 Align_Segments_At_Leg_Exec                      True                                (N/A)
      »      TRUE  P
4158 4168
4159 4169
4160 4170 =====> All 5 Comparisons Passed <=====
4161 4171
4162 4172
4163 4173 TESTID: 42
4164 4174
4165 4175 All the inflection points stored in Flight Planning Working Layer shall be moved to
4166 4176 Flight Planning Active Layer as follows:
4167 4177     Get write point access to the Flight Plan being modified by calling routine
4168 4178     Perf_Lgb_Interface_Mgr_Pkg.Requestlgb
4169 4179     if the current executing itinerary is Primary Fpln Prediction, then
4170 4180     Activate Strategic Working Point List for the Flight Plan being modified by calling routine
4171 4181     Fpp_Wrap_Point_Pkg.Activate_Strategic_Working_Point_List.
4172 4182     if the current executing itinerary is Current_Mode_Preds or Current_Mode_Hi_Pri, then
4173 4183     Activate Tactical Working Point List for the Flight Plan being modified by calling routine
4174 4184     Fpp_Wrap_Point_Pkg.Activate_Tactical_Working_Point_List.
4175 4185     Release write point access to the Flight Plan being modified by calling routine
4176 4186     Perf_Lgb_Interface_Mgr_Pkg.ReleaseLgb
4177 4187 PERF_SDD_7018, PERF_SDD_07154

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4178	4188				
4179	4189	If the current itinerary is not Active Primary Flight Plan Predictions, then utility procedure			
4180	4190	Prf_Int_Utills.Align_Segments_At_Leg shall not be called .			
4181	4191	PERF_SDD_07527			
4182	4192				
4183	4193				
4184	4194	INPUT			VALUE
4185	4195	-----			
		» -----			
4186	4196	Perf_Background_Dpkg.Pcitin.Itinerary		Perf_Int_Base_Tpkg.Current_Mod	
		» e_Hi_Pri			
4187	4197	Perf_Background_Dpkg.Psstepover			
		» False			
4188	4198	Perf_Background_Dpkg.Pcitin.Flight_Plan			S
		» econdary			
4189	4199	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfregst			
		» False			
4190	4200				
4191	4201				
4192	4202	define Request_LGB_Called := FALSE			
4193	4203	define Activate_Strategic_Working_Point_List_Called := FALSE			
4194	4204	define Align_Segments_At_Leg_Exec := False			
4195	4205	define Activate_Tactical_Working_Point_List_Called := FALSE			
4196	4206	define Releaselgb_Called := FALSE			
4197	4207	define Request_LGB_Called := TRUE			
4198	4208	define Align_Segments_At_Leg_Exec := True			
4199	4209	define Activate_Strategic_Working_Point_List_Called := TRUE			
4200	4210	define Activate_Tactical_Working_Point_List_Called := TRUE			
4201	4211	define Releaselgb_Called := TRUE			
4202	4212	define Request_LGB_Called := TRUE			
4203	4213	define Activate_Tactical_Working_Point_List_Called := TRUE			
4204	4214	define Releaselgb_Called := TRUE			
4205	4215				
4206	4216				
4207	4217	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
4208	4218	-----			
		» -----			
4209	4219	Request_LGB_Called	TRUE	(N/A)	
		» TRUE P			
4210	4220	Align_Segments_At_Leg_Exec	False	(N/A)	
		» FALSE P			
4211	4221	Activate_Strategic_Working_Point_List_Called	FALSE	(N/A)	
		» FALSE P			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4212	4222	Activate_Tactical_Working_Point_List_Called	TRUE	(N/A)
		» TRUE P		
4213	4223	Releaselgb_Called	TRUE	(N/A)
		» TRUE P		
4214	4224			
4215	4225			
4216	4226	====> All 5 Comparisons Passed <====		
4217	4227			
4218	4228			
4219	4229	TESTID: 43		
4220	4230			
4221	4231	All the inflection points stored in Flight Planning Working Layer shall be moved to		
4222	4232	Flight Planning Active Layer as follows:		
4223	4233	Get write point access to the Flight Plan being modified by calling routine		
4224	4234	Perf_Lgb_Interface_Mgr_Pkg.Requestlgb		
4225	4235	if the current executing itinerary is Primary Fpln Prediction, then		
4226	4236	Activate Strategic Working Point List for the Flight Plan being modified by calling routine		
4227	4237	Fpp_Wrap_Point_Pkg.Activate_Strategic_Working_Point_List.		
4228	4238	if the current executing itinerary is Current_Mode_Preds or Current_Mode_Hi_Pri, then		
4229	4239	Activate Tactical Working Point List for the Flight Plan being modified by calling routine		
4230	4240	Fpp_Wrap_Point_Pkg.Activate_Tactical_Working_Point_List.		
4231	4241	Release write point access to the Flight Plan being modified by calling routine		
4232	4242	Perf_Lgb_Interface_Mgr_Pkg.Releaselgb		
4233	4243	PERF_SDD_7018, PERF_SDD_07154		
4234	4244			
4235	4245			
4236	4246	INPUT		VALUE
4237	4247	-----		
		» -----		
4238	4248	Perf_Background_Dpkg.Pcitin.Itinerary		Perf_Int_Base_Tpkg.Current_Mo
		» de_Preds		
4239	4249	Perf_Background_Dpkg.Psstepover		
		» False		
4240	4250			
4241	4251			
4242	4252	define Request_LGB_Called := FALSE		
4243	4253	define Activate_Strategic_Working_Point_List_Called := FALSE		
4244	4254	define Activate_Tactical_Working_Point_List_Called := FALSE		
4245	4255	define Releaselgb_Called := FALSE		
4246	4256	define Request_LGB_Called := TRUE		
4247	4257	define Activate_Strategic_Working_Point_List_Called := TRUE		
4248	4258	define Activate_Tactical_Working_Point_List_Called := TRUE		
4249	4259	define Releaselgb_Called := TRUE		
4250	4260	define Request_LGB_Called := TRUE		

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4251	4261	define Activate_Tactical_Working_Point_List_Called := TRUE			
4252	4262	define Releaselgb_Called := TRUE			
4253	4263				
4254	4264				
4255	4265	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
4256	4266	-----	-----	-----	-----
		» -----			
4257	4267	Request_LGB_Called	TRUE	(N/A)	
		» TRUE P			
4258	4268	Activate_Strategic_Working_Point_List_Called	FALSE	(N/A)	
		» FALSE P			
4259	4269	Activate_Tactical_Working_Point_List_Called	TRUE	(N/A)	
		» TRUE P			
4260	4270	Releaselgb_Called	TRUE	(N/A)	
		» TRUE P			
4261	4271				
4262	4272				
4263	4273	====> All 4 Comparisons Passed <====			
4264	4274				
4265	4275				
4266	4276	TESTID: 44			
4267	4277				
4268	4278	All the inflection points stored in Flight Planning Working Layer shall be moved to			
4269	4279	Flight Planning Active Layer as follows:			
4270	4280	Get write point access to the Flight Plan being modified by calling routine			
4271	4281	Perf_Lgb_Interface_Mgr_Pkg.Requestlgb			
4272	4282	if the current executing itinerary is Primary Fpln Prediction, then			
4273	4283	Activate Strategic Working Point List for the Flight Plan being modified by calling routine			
4274	4284	Fpp_Wrap_Point_Pkg.Activate_Strategic_Working_Point_List.			
4275	4285	if the current executing itinerary is Current_Mode_Preds or Current_Mode_Hi_Pri, then			
4276	4286	Activate Tactical Working Point List for the Flight Plan being modified by calling routine			
4277	4287	Fpp_Wrap_Point_Pkg.Activate_Tactical_Working_Point_List.			
4278	4288	Release write point access to the Flight Plan being modified by calling routine			
4279	4289	Perf_Lgb_Interface_Mgr_Pkg.Releaselgb			
4280	4290	PERF_SDD_7018, PERF_SDD_07154			
4281	4291				
4282	4292				
4283	4293	INPUT			VALUE
4284	4294	-----	-----	-----	-----
		» -----			
4285	4295	Perf_Background_Dpkg.Pcitin.Itinerary		Perf_Int_Base_Tpkg.Fuel_Pla	
		» n_Stage2			
4286	4296	Perf_Background_Dpkg.Psstepover			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		»	TRUE				
4287	4297						
4288	4298						
4289	4299		define Request_LGB_Called := FALSE				
4290	4300		define Activate_Strategic_Working_Point_List_Called := FALSE				
4291	4301		define Activate_Tactical_Working_Point_List_Called := FALSE				
4292	4302		define ReleaseLgb_Called := FALSE				
4293	4303		define Request_LGB_Called := TRUE				
4294	4304		define Activate_Strategic_Working_Point_List_Called := TRUE				
4295	4305		define Activate_Tactical_Working_Point_List_Called := TRUE				
4296	4306		define ReleaseLgb_Called := TRUE				
4297	4307						
4298	4308						
4299	4309	OUTPUT		EXPECTED		TOLERANCE	ACTUAL
		»	P/F				
4300	4310	-----		-----		-----	-----
		»	-----				
4301	4311	Request_LGB_Called			FALSE	(N/A)	
		» FALSE P					
4302	4312	Activate_Strategic_Working_Point_List_Called			FALSE	(N/A)	
		» FALSE P					
4303	4313	Activate_Tactical_Working_Point_List_Called			FALSE	(N/A)	
		» FALSE P					
4304	4314	ReleaseLgb_Called			FALSE	(N/A)	
		» FALSE P					
4305	4315						
4306	4316						
4307	4317	====> All 4 Comparisons Passed <====					
4308	4318						
4309	4319						
4310	4320	TESTID: 45					
4311	4321						
4312	4322	The Flight Plan indicator LOCFP is set to Active for a temporary flight plan.					
4313	4323	(PERF_SDD_5617_INT)					
4314	4324	when a data save is initiated from the MRO page and it shall set the Perf_Data_Save_Initiated flag to true					
4315	4325	to prevent a subsequent Data Save from being initiated while a Data Save is already in progress.					
4316	4326	PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT)					
4317	4327						
4318	4328						
4319	4329	INPUT					VALUE
4320	4330	-----					-----
		»	-----				
4321	4331	Perf_Background_Dpkg.Pcactorsec					Fprequestrec_Types.T
		» emporary					

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4322	4332	CTP_PERF_BKGND_PUT_BK_DATA.Data			
		» 6			
4323	4333	Perf_Vdu_Dpkg.Data_Save			Perf_Vdu_T
		» pkg.None			
4324	4334	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated			
		» False			
4325	4335				
4326	4336				
4327	4337	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
4328	4338	-----	-----	-----	-----
		» -----			
4329	4339	Locfp	Active	(N/A)	
		» ACTIVE P			
4330	4340	Perf_Vdu_Dpkg.Data_Save	Perf_Vdu_Tpkg.Current_Mode	(N/A)	CURR
		» ENT_MODE P			
4331	4341	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated	True	(N/A)	
		» TRUE P			
4332	4342				
4333	4343				
4334	4344	====> All 3 Comparisons Passed <====			
4335	4345				
4336	4346				
4337	4347	TESTID: 46			
4338	4348				
4339	4349	The Flight Plan indicator LOCFP is set to Perf_Background_Dpkg.Pcactorsec for all flight plan other than temporary fli			
		» ght plan.			
4340	4350	(PERF_SDD_5617_INT)			
4341	4351	when a data save is initiated from the MRO page and it shall set the Perf_Data_Save_Initiated flag to true			
4342	4352	to prevent a subsequent Data Save from being initiated while a Data Save is already in progress.			
4343	4353	PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT)			
4344	4354				
4345	4355				
4346	4356	INPUT			VALUE
4347	4357	-----			-----
		» -----			
4348	4358	Perf_Background_Dpkg.Pcactorsec			
		» Active			
4349	4359	CTP_PERF_BKGND_PUT_BK_DATA.Data			
		» 5			
4350	4360	Perf_Vdu_Dpkg.Data_Save			Perf_Vdu_T
		» pkg.None			
4351	4361	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated			
		» False			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4352	4362				
4353	4363				
4354	4364	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
4355	4365	-----	-----	-----	-----
		» -----			
4356	4366	Locfp	Active	(N/A)	
		» ACTIVE P			
4357	4367	Perf_Vdu_Dpkg.Data_Save	Perf_Vdu_Tpkg.Secondary3	(N/A)	SE
		» CONDARY3 P			
4358	4368	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated	True	(N/A)	
		» TRUE P			
4359	4369				
4360	4370				
4361	4371	====> All 3 Comparisons Passed <====			
4362	4372				
4363	4373				
4364	4374	TESTID: 47			
4365	4375				
4366	4376	ETT data has not been transmitted from the slave FM to the Master			
4367	4377	(PERF_SDD_3518_INT).			
4368	4378	when a data save is initiated from the MRO page and it shall set the Perf_Data_Save_Initiated flag to true			
4369	4379	to prevent a subsequent Data Save from being initiated while a Data Save is already in progress.			
4370	4380	PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT)			
4371	4381				
4372	4382	If the data buffering process has not started based on the user request than following shall not be done			
4373	4383	(PERF_SDD_07467_INT)			
4374	4384	- Following procedure shall be called:			
4375	4385	Prf_Vdu_Utils.Save_Leg_Data - To buffer flight plan data.			
4376	4386	Prf_Vdu_Utils.Save_Pseudo_Data - To buffer psuedo waypoint data.			
4377	4387	Prf_Vdu_Utils.Save_Vga_Data - To buffer vertical guidance array data.			
4378	4388	Prf_Vdu_Utils.Save_Altitude_Data - To buffer important altitude values.			
4379	4389	(PERF_SDD_07468_INT)			
4380	4390	- After all the required data is buffered to VDU buffer the buffer validity shall be set to true			
4381	4391	and buffer prediction data sequence counter is set to current guidance header sequence counter.			
4382	4392	(PERF_SDD_07470_INT)			
4383	4393	- Flag indicating VDU Buffer save has been initiated for this pass of preds and			
4384	4394	the flag indicating the data buffering process has started shall be set to false			
4385	4395	(PERF_SDD_07471_INT)			
4386	4396				
4387	4397				
4388	4398	INPUT			VALUE
4389	4399	-----	-----	-----	-----
		» -----			



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4390	4400	CTP_PERF_BKGND_PUT_BK_DATA.Data	
		» 4	
4391	4401	Perf_Vdu_Dpkg.Data_Save	Perf_Vdu_T
		» pkg.None	
4392	4402	Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst	
		» False	
4393	4403	Fmcs_Partition_Data_Pkg.Ops_Master_Status	
		» Master	
4394	4404	Fmcs_Partition_Data_Pkg.Ops_Dual_Mode	
		» Dual	
4395	4405	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid	
		» False	
4396	4406	Perf_Time_Dpkg:body.Data_Storage(Active).Display_Asterisk	
		» False	
4397	4407	Perf_Background_Dpkg.Pcgmtime.Gpc_Time	
		» 2	
4398	4408	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt	
		» 0	
4399	4409	Perf_Background_Dpkg.Pcitin.Itinerary	Prim_Fp
		» ln_Preds	
4400	4410	Perf_Background_Dpkg.Pctcstridx	
		» 1	
4401	4411	Perf_Background_Dpkg.Pcdestglidx	
		» 0	
4402	4412	Perf_Background_Dpkg.Pctcstrctrl(Active).Timeonly	
		» True	
4403	4413	Perf_Background_Dpkg.Pctcstrctrl(Active).Eval_Done	
		» False	
4404	4414	Perf_Background_Dpkg.Pcfltphase	
		» Cruise	
4405	4415	Perf_Background_Dpkg.Rta.Missed	
		» False	
4406	4416	Perf_Background_Dpkg.Pcperflegs(18).Included	
		» True	
4407	4417	Perf_Background_Dpkg.Pcperflegs(18).Dist	
		» 600.0	
4408	4418	Perf_Background_Dpkg.Pcstartpt.Dist	
		» 600.0	
4409	4419	Perf_Background_Dpkg.Pccompett(Active)	
		» True	
4410	4420	Perf_Background_Dpkg.Rta.Eval_Done	
		» True	
4411	4421	Perf_Background_Dpkg.Pctcstrctrl(Active).Valid	
		» True	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4412	4422	Perf_Background_Dpkg.Ett(Active).Data			
		» 20.0			
4413	4423	Perf_Background_Dpkg.Ett(Active).Status			
		» Valid			
4414	4424	Perf_Background_Dpkg.Pctcstrctrl(Active).Transmit			
		» True			
4415	4425	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Data			
		» 5.0			
4416	4426	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Ett.Status			
		» Invalid			
4417	4427	Perf_Time_Dpkg:body.Data_Storage(Active).Ett_Transfer.Data_Fresh			
		» False			
4418	4428	CTP_PERF_BKGND_PUT_BK_DATA.Du_Status			Perf_Int_Base_Tpkg.Du
		» al_Slave			
4419	4429	Perf_Vdu_Dpkg.Vdu_Buffer.Buffer_Valid			
		» False			
4420	4430	Perf_Vdu_Dpkg.Vdu_Buffer.Prddataseq			
		» 0			
4421	4431	Perf_Background_Dpkg.Psprddataseq			
		» 1			
4422	4432	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated			
		» True			
4423	4433	CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec			
		» False			
4424	4434	CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec			
		» False			
4425	4435	CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec			
		» False			
4426	4436	CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec			
		» False			
4427	4437	Change			
		» False			
4428	4438	Change			
		» False			
4429	4439	Change			
		» False			
4430	4440				
4431	4441				
4432	4442	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
4433	4443	-----	-----	-----	-----
		» -----			
4434	4444	Ett_Sys.Data_Fresh	False	(N/A)	
		» FALSE P			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4435	4445	Send_Ett	False	(N/A)	
		» FALSE P			
4436	4446				
4437	4447				
4438	4448	INPUT			VALUE
4439	4449	-----			-----
		» -----			
4440	4450	Prf_Vdu_Utils:body.Data_Save_In_Progress			
		» False			
4441	4451	CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec			
		» True			
4442	4452	CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec			
		» True			
4443	4453	CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec			
		» True			
4444	4454	CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec			
		» True			
4445	4455				
4446	4456				
4447	4457	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
4448	4458	-----	-----	-----	-----
		» -----			
4449	4459	CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec	False	(N/A)	
		» FALSE P			
4450	4460	CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec	False	(N/A)	
		» FALSE P			
4451	4461	CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec	False	(N/A)	
		» FALSE P			
4452	4462	CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec	False	(N/A)	
		» FALSE P			
4453	4463	Perf_Vdu_Dpkg.Vdu_Buffer.Buffer_Valid	False	(N/A)	
		» FALSE P			
4454	4464	Perf_Vdu_Dpkg.Vdu_Buffer.Prddataseq	0	(N/A)	
		» 0 P			
4455	4465	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated	True	(N/A)	
		» TRUE P			
4456	4466				
4457	4467				
4458	4468	INPUT			VALUE
4459	4469	-----			-----
		» -----			
4460	4470	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated			
		» False			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4461	4471				
4462	4472				
4463	4473	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
4464	4474	-----	-----	-----	-----
		» -----			
4465	4475	Perf_Vdu_Dpkg.Data_Save	Perf_Vdu_Tpkg.Secondary2	(N/A)	SE
		» CONDARY2 P			
4466	4476	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated	True	(N/A)	
		» TRUE P			
4467	4477				
4468	4478				
4469	4479	====> All 11 Comparisons Passed <====			
4470	4480				
4471	4481				
4472	4482	TESTID: 48			
4473	4483				
4474	4484	when a data save is initiated from the MRO page and it shall set the Perf_Data_Save_Initiated flag to true			
4475	4485	to prevent a subsequent Data Save from being initiated while a Data Save is already in progress.			
4476	4486	PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT)			
4477	4487	- The distance to destination of point data buffered as a part of trajectory data shall be unbaised for all the points			
		» buffered.			
4478	4488	PERF_SDD_07469_INT			
4479	4489				
4480	4490	REQUIREMENTS UNDER EVALUATION : PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT),			
4481	4491	PERF_SDD_07469_INT			
4482	4492				
4483	4493	SUPPORTING REQUIREMENTS : N/A			
4484	4494				
4485	4495				
4486	4496	INPUT			VALUE
4487	4497	-----	-----	-----	-----
		» -----			
4488	4498	CTP_PERF_BKGND_PUT_BK_DATA.Data			
		» 3			
4489	4499	Perf_Vdu_Dpkg.Data_Save			Perf_Vdu_T
		» pkg.None			
4490	4500	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated			
		» False			
4491	4501	Perf_Background_Dpkg.Pcitin.Itinerary			
		» Goaround			
4492	4502	Perf_Background_Dpkg.Destination_Data.Efob.Data			
		» 20.0			
4493	4503	Perf_Background_Dpkg.Destination_Data.Efob.Valid			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4494	4504	» True			
		Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Data			
		» 0.0			
4495	4505	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Valid			
		» False			
4496	4506	Prf_Vdu_Utils:body.Data_Save_In_Progress			
		» False			
4497	4507	Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Number_Of_Points			
		» 2			
4498	4508	Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Point_Data(1).Aircraft_State.Distance_To_Destination			
		» 500.0			
4499	4509	Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Point_Data(2).Aircraft_State.Distance_To_Destination			
		» 100.0			
4500	4510	Perf_Dpkg.Psbias			
		» 400.0			
4501	4511				
4502	4512				
4503	4513	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
4504	4514	-----	-----	-----	-----
		» -----			
4505	4515	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Data	20.0	0.001	2.0
		» 0000E+01 P			
4506	4516	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Valid	True	(N/A)	
		» TRUE P			
4507	4517	Perf_Vdu_Dpkg.Data_Save	Perf_Vdu_Tpkg.Secondary1	(N/A)	SE
		» CONDARY1 P			
4508	4518	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated	True	(N/A)	
		» TRUE P			
4509	4519	Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Point_Data(1).Aircraft_State.Distance_To_Destination			
4510	4520		500.0	0.001	5.0
		» 0000E+02 P			
4511	4521	Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Point_Data(2).Aircraft_State.Distance_To_Destination			
4512	4522		100.0	0.001	1.0
		» 0000E+02 P			
4513	4523				
4514	4524				
4515	4525	====> All 6 Comparisons Passed <====			
4516	4526				
4517	4527				
4518	4528	TESTID: 49			
4519	4529				
4520	4530	when a data save is initiated from the MRO page and it shall set the Perf_Data_Save_Initiated flag to true			
4521	4531	to prevent a subsequent Data Save from being initiated while a Data Save is already in progress.			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4522	4532	PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT)			
4523	4533	- The distance to destination of point data buffered as a part of trajectory data shall be unbaised for all the points » buffered.			
4524	4534	PERF_SDD_07469_INT			
4525	4535				
4526	4536	REQUIREMENTS UNDER EVALUATION : PERF_SDD_07469_INT			
4527	4537				
4528	4538	SUPPORTING REQUIREMENTS : N/A			
4529	4539				
4530	4540				
4531	4541	INPUT			VALUE
4532	4542	-----			-----
		» -----			
4533	4543	CTP_PERF_BKGND_PUT_BK_DATA.Data			
		» 0			
4534	4544	Perf_Vdu_Dpkg.Data_Save			Perf_Vdu_Tpkg.Se
		» condaryl			
4535	4545	Perf_Background_Dpkg.Pcitin.Itinerary			
		» Goaround			
4536	4546	Perf_Background_Dpkg.Destination_Data.Efob.Data			
		» 20.0			
4537	4547	Perf_Background_Dpkg.Destination_Data.Efob.Valid			
		» True			
4538	4548	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Data			
		» 0.0			
4539	4549	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Valid			
		» False			
4540	4550	Prf_Vdu_Utils:body.Data_Save_In_Progress			
		» True			
4541	4551	Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Number_Of_Points			
		» 2			
4542	4552	Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Point_Data(1).Aircraft_State.Distance_To_Destination			
		» 500.0			
4543	4553	Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Point_Data(2).Aircraft_State.Distance_To_Destination			
		» 100.0			
4544	4554	Perf_Dpkg.Psbias			
		» 400.0			
4545	4555				
4546	4556				
4547	4557	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
4548	4558	-----	-----	-----	-----
		» -----			
4549	4559	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Data	20.0	0.001	2.0

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

4550 4560 » 0000E+01 P
4550 4560 Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Valid True (N/A)
4551 4561 » TRUE P
4551 4561 Perf_Vdu_Dpkg.Data_Save Perf_Vdu_Tpkg.None (N/A)
4552 4562 » NONE P
4552 4562 Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Point_Data(1).Aircraft_State.Distance_To_Destination
4553 4563 100.0 0.001 1.0
4554 4564 » 0000E+02 P
4554 4564 Perf_Vdu_Dpkg.Vdu_Buffer.Trajectory.Point_Data(2).Aircraft_State.Distance_To_Destination
4555 4565 -300.0 0.001 -3.0
4556 4566 » 0000E+02 P
4557 4567
4558 4568 ====> All 5 Comparisons Passed <====
4559 4569
4560 4570
4561 4571 TESTID: 50
4562 4572 If lateral segments are valid for the Active flight plan, then the following shall be performed to
4563 4573 align the lateral segments such that the DTD of the last segment of the input leg matches the DTD of the input leg:
4564 4574 1.The leg's last active segment is retrieved via Fpp_Wrap_Pkg.Get_Legs_Last_Active_Segment.
4565 4575 2.The leg corresponding to the input leg index is retrieved via Common_Lgb_Getlgbleg.
4566 4576 3.The adjustment factor (bias) is set to the leg's last segment DTD minus(the leg's DTD minus the leg's DTD bias).
4567 4577 4.All segments in the working layer are deleted by calling Fpp_Wrap_Segment_Pkg.Delete_All_Segments_From_Working_List.
4568 4578 5.The active layer segments are copied to the working layer by calling Fpp_Wrap_Segment_Pkg.Copy_Active_Segments_To_Wo
» rking.
4569 4579 6.The adjustment factor is removed from the working segments by calling
4570 4580 Fpp_Wrap_Segment_Pkg.Un_Bias_DTD_For_All_Working_Segments with the input computed bias.
4571 4581 when a data save is initiated from the MRO page and it shall set the Perf_Data_Save_Initiated flag to true
4572 4582 to prevent a subsequent Data Save from being initiated while a Data Save is already in progress.
4573 4583 PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT)
4574 4584 If the data buffering process has not started based on the user request than following shall not be done
4575 4585 (PERF_SDD_07467_INT)
4576 4586 - Following procedure shall be called:
4577 4587 Prf_Vdu_Utils.Save_Leg_Data - To buffer flight plan data.
4578 4588 Prf_Vdu_Utils.Save_Pseudo_Data - To buffer psuedo waypoint data.
4579 4589 Prf_Vdu_Utils.Save_Vga_Data - To buffer vertical guidance array data.
4580 4590 Prf_Vdu_Utils.Save_Altitude_Data - To buffer important altitude values.
4581 4591 (PERF_SDD_07468_INT)
4582 4592 - After all the required data is buffered to VDU buffer the buffer validity shall be set to true
4583 4593 and buffer prediction data sequence counter is set to current guidance header sequence counter.
4584 4594 (PERF_SDD_07470_INT)
4585 4595 - Flag indicating VDU Buffer save has been initiated for this pass of preds and
4586 4596 the flag indicating the data buffering process has started shall be set to false
4587 4597 (PERF_SDD_07471_INT)

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

4588 4598 This function(Get_Data_Save_State) shall return the flag Perf_Data_Save_Initiated that is used to prevent a subsequent
      » Data Save
4589 4599 from being initiated while a Data Save is already in progress. While the flag is true, a new data save cannot be initi
      » ated.
4590 4600 PERF_SDD_07481(PERF_SRD_23173_INT)
4591 4601 This function(Int_To_Str) shall always return a string of two characters; the characters are always the digits.
4592 4602 The first digit of the string is the result of integer division of input number by 10.
4593 4603 The second digit of the string is the result of following equation: input number - first digit * 10.
4594 4604 PERF_SDD_07480_INT
4595 4605
4596 4606 REQUIREMENTS UNDER EVALUATION : PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT), PERF_SDD_07481(PERF_SRD_23173_
      » INT),
4597 4607 PERF_SDD_07467_INT, PERF_SDD_07468_INT, PERF_SDD_07470_INT, PERF_SDD_07471_INT, PERF_S
      » DD_07480_INT
4598 4608
4599 4609 SUPPORTING REQUIREMENTS : N/A
4600 4610
4601 4611
4602 4612 INPUT VALUE
4603 4613 -----
      » -----
4604 4614 Sys_Perf_Interface_Dpkg:body.Data_Storage.Psperfreqst
      » False
4605 4615 CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec
      » False
4606 4616 CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec
      » False
4607 4617 CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec
      » False
4608 4618 CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec
      » False
4609 4619 Perf_Background_Dpkg.Psstepover
      » False
4610 4620 Perf_Background_Dpkg.Pcitin.Itinerary Prim_Fp
      » ln_Preds
4611 4621 Perf_Background_Dpkg.Pcitin.Flight_Plan
      » Active
4612 4622 Perf_Vdu_Dpkg.Vdu_Buffer.Buffer_Valid
      » False
4613 4623 Perf_Vdu_Dpkg.Vdu_Buffer.Prddataseq
      » 0
4614 4624 Perf_Background_Dpkg.Psprddataseq
      » 1
4615 4625 Perf_Vdu_Dpkg.Perf_Data_Save_Initiated

```



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4616	4626	» True			
		Prf_Vdu_Utils:body.Data_Save_In_Progress			
		» False			
4617	4627	CTP_PERF_BKGND_PUT_BK_DATA.Data			
		» 1			
4618	4628	Perf_Vdu_Dpkg.Data_Save			Perf_Vdu_T
		» pkg.None			
4619	4629	CTP_PERF_BKGND_PUT_BK_DATA.Num			
		» 99			
4620	4630	CTP_PERF_BKGND_PUT_BK_DATA.Int_To_Str_Exec			
		» "01"			
4621	4631	CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec			
		» True			
4622	4632	CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec			
		» True			
4623	4633	CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec			
		» True			
4624	4634	CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec			
		» True			
4625	4635				
4626	4636				
4627	4637	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
4628	4638	-----	-----	-----	-----
		» -----			
4629	4639	Perf_Vdu_Dpkg.Vdu_Buffer.Buffer_Valid	False	(N/A)	
		» FALSE P			
4630	4640	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated	True	(N/A)	
		» TRUE P			
4631	4641	Perf_Vdu_Dpkg.Vdu_Buffer.Prddataseq	0	(N/A)	
		» 0 P			
4632	4642	CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec	False	(N/A)	
		» FALSE P			
4633	4643	CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec	False	(N/A)	
		» FALSE P			
4634	4644	CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec	False	(N/A)	
		» FALSE P			
4635	4645	CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec	False	(N/A)	
		» FALSE P			
4636	4646				
4637	4647				
4638	4648	INPUT			VALUE
4639	4649	-----	-----	-----	-----
		» -----			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4640	4650	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated			
		» False			
4641	4651				
4642	4652				
4643	4653	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
4644	4654	-----	-----	-----	-----
		» -----			
4645	4655	CTP_PERF_BKGND_PUT_BK_DATA.Int_To_Str_Exec	"99"	(N/A)	
		» "99" P			
4646	4656	CTP_PERF_BKGND_PUT_BK_DATA.Get_Data_Save_State_Exec	True	(N/A)	
		» TRUE P			
4647	4657	Perf_Vdu_Dpkg.Data_Save	Perf_Vdu_Tpkg.Active	(N/A)	
		» ACTIVE P			
4648	4658	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated	True	(N/A)	
		» TRUE P			
4649	4659				
4650	4660				
4651	4661	====> All 11 Comparisons Passed <====			
4652	4662				
4653	4663				
4654	4664	TESTID: 51			
4655	4665	when a data save is initiated from the MRO page and it shall set the Perf_Data_Save_Initiated flag to true			
4656	4666	to prevent a subsequent Data Save from being initiated while a Data Save is already in progress.			
4657	4667	PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT)			
4658	4668				
4659	4669	If the data buffering process has started based on the user request than following shall be done			
4660	4670	(PERF_SDD_07467_INT)			
4661	4671	- Following procedure shall be called:			
4662	4672	Prf_Vdu_Utils.Save_Leg_Data - To buffer flight plan data.			
4663	4673	Prf_Vdu_Utils.Save_Pseudo_Data - To buffer psuedo waypoint data.			
4664	4674	Prf_Vdu_Utils.Save_Vga_Data - To buffer vertical guidance array data.			
4665	4675	Prf_Vdu_Utils.Save_Altitude_Data - To buffer important altitude values.			
4666	4676	(PERF_SDD_07468_INT)			
4667	4677	- After all the required data is buffered to VDU buffer the buffer validity shall be set to true			
4668	4678	and buffer prediction data sequence counter is set to current guidance header sequence counter.			
4669	4679	(PERF_SDD_07470_INT)			
4670	4680	- Flag indicating VDU Buffer save has been initiated for this pass of preds and			
4671	4681	the flag indicating the data buffering process has started shall be set to false			
4672	4682	(PERF_SDD_07471_INT)			
4673	4683	This function(Get_Data_Save_State) shall return the flag Perf_Data_Save_Initiated that is used to prevent a subsequent			
		» Data Save			
4674	4684	from being initiated while a Data Save is already in progress. While the flag is true, a new data save cannot be initi			
		» ated.			

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

4675 4685 PERF_SDD_07481(PERF_SRD_23173_INT)
4676 4686 This function(Int_To_Str) shall always return a string of two characters; the characters are always the digits.
4677 4687 The first digit of the string is the result of integer division of input number by 10.
4678 4688 The second digit of the string is the result of following equation: input number - first digit * 10.
4679 4689 PERF_SDD_07480_INT
4680 4690 Access to LGB is requested using the utility Perf_Lgb_Interface_Mgr_Pkg.Requestlgb and
4681 4691 first leg data in the flight plan shall be obtained using the utility Common_Lgb.Getlgbleg.
4682 4692 PERF_SDD_07473_INT
4683 4693 Flight plan data required to draw the trajectory shall be buffered to VDU buffer for the all the legs in the flight pl
    » an.
4684 4694 PERF_SDD_07474_INT
4685 4695 The distance to destination data for the first leg shall be set to the current aircraft distance to destination only
4686 4696 if the current flight phase is preflight.
4687 4697 PERF_SDD_07475_INT
4688 4698 On completion of buffering of the data leg data access obtained to LGB shall be released by calling the utility
4689 4699 Perf_Lgb_Interface_Mgr_Pkg.Releaselgb and number of flight plan legs buffered into VDU buffer is updated.
4690 4700 PERF_SDD_07476_INT
4691 4701 Pseudo waypoint data shall be buffered to VDU buffer (Perf_Vdu_Dpkg.Vdu_Buffer.Pseudos) from
4692 4702 Perf background (Perf_Background_Dpkg.Pcperflgls).
4693 4703 PERF_SDD_07477_INT
4694 4704 Descent path data shall be buffered to VDU buffer (Perf_Vdu_Dpkg.Vdu_Buffer.Despath) from
4695 4705 Perf background (Perf_Despath_Dpkg.Pcdespath).
4696 4706 PERF_SDD_07479_INT
4697 4707 Following altitude value and validity shall be copied from background variables to VDU buffer:
4698 4708 - Cruise altitude.
4699 4709 - Maximum Certified altitude.
4700 4710 - Recommended Maximum altitude.
4701 4711 - Computed Optimum altitude.
4702 4712 - Clearance altitude.
4703 4713 - Tropopause altitude.
4704 4714 PERF_SDD_07472_INT
4705 4715
4706 4716 REQUIREMENTS UNDER EVALUATION : PERF_SDD_07482(PERF_SRD_23172_INT, PERF_SRD_23173_INT), PERF_SDD_07481(PERF_SRD_23173_
    » INT),
4707 4717 PERF_SDD_07467_INT, PERF_SDD_07468_INT, PERF_SDD_07470_INT, PERF_SDD_07471_INT, PERF_S
    » DD_07480_INT,
4708 4718 PERF_SDD_07473_INT, PERF_SDD_07474_INT, PERF_SDD_07475_INT, PERF_SDD_07476_INT, PERF_S
    » DD_07477_INT,
4709 4719 PERF_SDD_07479_INT, PERF_SDD_07472_INT
4710 4720
4711 4721 SUPPORTING REQUIREMENTS : N/A
4712 4722
4713 4723
4714 4724 INPUT

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4715	4725	-----	
		» -----	
4716	4726	CTP_PERF_BKGND_PUT_BK_DATA.Data	
		» 2	
4717	4727	CTP_PERF_BKGND_PUT_BK_DATA.Num	
		» 10	
4718	4728	Perf_Background_Dpkg.Pcitin.Itinerary	Perf_Int_Base_Tpkg.Current_Mo
		» de_Preds	
4719	4729	Prf_Vdu_Utils:body.Data_Save_In_Progress	
		» True	
4720	4730	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.FixIdent	"
		» aB19 fg"	
4721	4731	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Altaacstr	
		» 123.00	
4722	4732	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Altabcstr	
		» 12345.6	
4723	4733	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Cnstraintspd	
		» 12345.6	
4724	4734	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Targetalt	
		» 12345.6	
4725	4735	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Cstraltlim	
		» 12345.6	
4726	4736	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Spcspd	
		» 12345.6	
4727	4737	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Fpa	
		» 1234.56	
4728	4738	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.FpaVal	
		» True	
4729	4739	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.PathTerm	
		» FA	
4730	4740	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Clbordescstr	De
		» scentseg	
4731	4741	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Altaacstrval	
		» True	
4732	4742	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Altabcstrval	
		» True	
4733	4743	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Spcspdval	
		» True	
4734	4744	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Toosteppath	
		» Tsptop	
4735	4745	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Fixdistodest	1
		» 23456.00	
4736	4746	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Incouse	
		» 12300.0	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4737	4747	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.ISADev	
		» 1000.00	
4738	4748	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.LegDistance	
		» 1000.00	
4739	4749	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Outcourse	
		» 1000.00	
4740	4750	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Prdairstpd	(123
		» 4.5,CAS)	
4741	4751	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Prdalt	
		» 123.00	
4742	4752	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Prdetatofix	
		» 12	
4743	4753	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Prdgndspd	
		» 1000.2	
4744	4754	Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg.Nextfpn	
		» 2	
4745	4755	Perf_Background_Dpkg.Pcfltphase	P
		» reflight	
4746	4756	Perf_Background_Dpkg.Psldistodest	
		» 20006.00	
4747	4757	Perf_Background_Dpkg.Pcfirstlegidx	
		» 1	
4748	4758	Perf_Background_Dpkg.Pcdestglidx	
		» 2	
4749	4759	Perf_Background_Dpkg.Psprddataseq	
		» 1	
4750	4760	Perf_Background_Dpkg.Pscrzalt.Data	
		» 10000.00	
4751	4761	Perf_Background_Dpkg.Pscrzalt.Valid	
		» True	
4752	4762	Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Data	
		» 50000.00	
4753	4763	Perf_Background_Dpkg.Maxalt.Maximum_Maximum_Alt.Valid	
		» True	
4754	4764	Perf_Background_Dpkg.Maxalt.Maximum_Alt.Data	
		» 55000.00	
4755	4765	Perf_Background_Dpkg.Maxalt.Maximum_Alt.Valid	
		» True	
4756	4766	Perf_Background_Dpkg.Pcoptalt.Data	
		» 1000.00	
4757	4767	Perf_Background_Dpkg.Pcoptalt.Valid	
		» True	
4758	4768	Perf_Background_Dpkg.Pstropoalt	
		» 20000.00	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4759	4769	CTP_PERF_BKGND_PUT_BK_DATA.Clr.Data	
		» 5000.00	
4760	4770	CTP_PERF_BKGND_PUT_BK_DATA.Clr.Valid	
		» True	
4761	4771	Perf_Background_Dpkg.Pcperflegs(33).Included	
		» True	
4762	4772	Perf_Background_Dpkg.Pcperflegs(33).Dist	
		» 10002.0	
4763	4773	Perf_Despath_Dpkg.Pcdespath.vga(74).PACK.DISCON	
		» True	
4764	4774	Perf_Despath_Dpkg.Pcdespath.VGAINDXLAST	
		» 74	
4765	4775	Perf_Despath_Dpkg.Pcdespath.VGAVALID	
		» True	
4766	4776	Perf_Vdu_Dpkg.Data_Save	Perf_Vdu_T
		» pkg.None	
4767	4777	CTP_PERF_BKGND_PUT_BK_DATA.Int_To_Str_Exec	
		» "99"	
4768	4778	CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec	
		» False	
4769	4779	CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec	
		» False	
4770	4780	CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec	
		» False	
4771	4781	CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec	
		» False	
4772	4782	CTP_PERF_BKGND_PUT_BK_DATA.Requestlgb_Exec	
		» False	
4773	4783	CTP_PERF_BKGND_PUT_BK_DATA.Releaselgb_Exec	
		» FALSE	
4774	4784	CTP_PERF_BKGND_PUT_BK_DATA.Getlgbleg_Exec	
		» False	
4775	4785	Perf_Vdu_Dpkg.Vdu_Buffer.Buffer_Valid	
		» False	
4776	4786	Perf_Vdu_Dpkg.Vdu_Buffer.Prddataseq	
		» 0	
4777	4787	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated	
		» True	
4778	4788	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).FixIdent	"
		» gfedcba"	
4779	4789	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Altaacstr	
		» 321.00	
4780	4790	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Altabcstr	
		» 32145.6	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4781	4791	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Cstrspdlim	
		» 32145.6	
4782	4792	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Targetalt	
		» 32145.6	
4783	4793	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Cstraltilim	
		» 32145.6	
4784	4794	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Spcspd	
		» 32145.6	
4785	4795	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).SpCFpa	
		» 3214.56	
4786	4796	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).FpaVal	
		» False	
4787	4797	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).PathTerm	
		» AF	
4788	4798	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Clbordescstr	
		» CLIMBSEG	
4789	4799	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Altaacstrval	
		» False	
4790	4800	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Altabcstrval	
		» False	
4791	4801	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Spcspdval	
		» False	
4792	4802	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Toosteppath	
		» TSPNULL	
4793	4803	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Fixdistodest	3
		» 21456.00	
4794	4804	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Incouse	
		» 32100.0	
4795	4805	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).ISADev	
		» 3000.00	
4796	4806	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).LegDistance	
		» 3000.00	
4797	4807	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Outcourse	
		» 3000.00	
4798	4808	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Prdairspd	( 3214
		» .5,Mach)	
4799	4809	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Prdalt	
		» 321.00	
4800	4810	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Prdtime	
		» 21	
4801	4811	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Prdgndspd	
		» 3000.2	
4802	4812	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Num_GLlegs	
		» 123	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4803	4813	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Crz.Data			
		» 1.0			
4804	4814	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Crz.Valid			
		» False			
4805	4815	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Max.Data			
		» 11000.0			
4806	4816	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Max.Valid			
		» False			
4807	4817	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Rec.Data			
		» 12345.6			
4808	4818	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Rec.Valid			
		» False			
4809	4819	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Opt.Data			
		» 12345.6			
4810	4820	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Opt.Valid			
		» False			
4811	4821	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Clr.Data			
		» 65432.1			
4812	4822	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Clr.Valid			
		» False			
4813	4823	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Tropo.Data			
		» 11.11			
4814	4824	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Tropo.Valid			
		» False			
4815	4825	Perf_Vdu_Dpkg.Vdu_Buffer.Pseudos(33).Included			
		» False			
4816	4826	Perf_Vdu_Dpkg.Vdu_Buffer.Pseudos(33).Dist			
		» 20001.0			
4817	4827	Perf_Vdu_Dpkg.Vdu_Buffer.Despath.vga(74).PACK.DISCON			
		» False			
4818	4828	Perf_Vdu_Dpkg.Vdu_Buffer.Despath.VGAINDXLAST			
		» 37			
4819	4829	Perf_Vdu_Dpkg.Vdu_Buffer.Despath.VGAVALID			
		» False			
4820	4830	CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec			
		» True			
4821	4831				
4822	4832				
4823	4833	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
4824	4834	-----	-----	-----	-----
		» -----			
4825	4835	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Fixdistodest	123456.00	0.001	1.2
		» 3456E+05 P			



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4826	4836	CTP_PERF_BKGND_PUT_BK_DATA.Requestlgb_Exec	True	(N/A)	
		» TRUE P			
4827	4837	CTP_PERF_BKGND_PUT_BK_DATA.Getlgbleg_Exec	True	(N/A)	
		» TRUE P			
4828	4838	CTP_PERF_BKGND_PUT_BK_DATA.Releaselgb_Exec	True	(N/A)	
		» TRUE P			
4829	4839				
4830	4840				
4831	4841	INPUT			VALUE
4832	4842	-----			-----
		» -----			
4833	4843	CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec			
		» True			
4834	4844	CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec			
		» True			
4835	4845	CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec			
		» True			
4836	4846	Common_Lgb:BODY.Header_Control.Clralt.Data			
		» 5000.00			
4837	4847	Common_Lgb:BODY.Header_Control.Clralt.Valid			
		» True			
4838	4848				
4839	4849				
4840	4850	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
4841	4851	-----	-----	-----	-----
		» -----			
4842	4852	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Crz.Data	10000.00	0.001	1.0
		» 0000E+04 P			
4843	4853	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Crz.Valid	True	(N/A)	
		» TRUE P			
4844	4854	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Max.Data	50000.00	0.001	5.0
		» 0000E+04 P			
4845	4855	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Max.Valid	True	(N/A)	
		» TRUE P			
4846	4856	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Rec.Data	55000.00	0.001	5.5
		» 0000E+04 P			
4847	4857	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Rec.Valid	True	(N/A)	
		» TRUE P			
4848	4858	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Opt.Data	1000.00	0.001	1.0
		» 0000E+03 P			
4849	4859	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Opt.Valid	True	(N/A)	
		» TRUE P			
4850	4860	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Clr.Data	5000.00	0.001	5.0

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» 0000E+03 P			
4851	4861	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Clr.Valid	True	(N/A)	
		» TRUE P			
4852	4862	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Tropo.Data	20000.00	0.001	2.0
		» 0000E+04 P			
4853	4863	Perf_Vdu_Dpkg.Vdu_Buffer.Altitudes.Tropo.Valid	True	(N/A)	
		» TRUE P			
4854	4864	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated	False	(N/A)	
		» FALSE P			
4855	4865	Prf_Vdu_Utils:body.Data_Save_In_Progress	False	(N/A)	
		» FALSE P			
4856	4866	Perf_Vdu_Dpkg.Vdu_Buffer.Buffer_Valid	True	(N/A)	
		» TRUE P			
4857	4867	Perf_Vdu_Dpkg.Vdu_Buffer.Prddataseq	1	(N/A)	
		» 1 P			
4858	4868	CTP_PERF_BKGND_PUT_BK_DATA.Save_Leg_Data_Exec	True	(N/A)	
		» TRUE P			
4859	4869	CTP_PERF_BKGND_PUT_BK_DATA.Save_Pseudo_Data_Exec	True	(N/A)	
		» TRUE P			
4860	4870	CTP_PERF_BKGND_PUT_BK_DATA.Save_Vga_Data_Exec	True	(N/A)	
		» TRUE P			
4861	4871	CTP_PERF_BKGND_PUT_BK_DATA.Save_Altitude_Data_Exec	True	(N/A)	
		» TRUE P			
4862	4872	Perf_Vdu_Dpkg.Vdu_Buffer.Pseudos(33).Included	True	(N/A)	
		» TRUE P			
4863	4873	Perf_Vdu_Dpkg.Vdu_Buffer.Pseudos(33).Dist	10002.0	0.001	1.0
		» 0020E+04 P			
4864	4874	Perf_Vdu_Dpkg.Vdu_Buffer.Despath.vga(74).PACK.DISCON	True	(N/A)	
		» TRUE P			
4865	4875	Perf_Vdu_Dpkg.Vdu_Buffer.Despath.VGAINDXLAST	74	(N/A)	
		» 74 P			
4866	4876	Perf_Vdu_Dpkg.Vdu_Buffer.Despath.VGAVALID	True	(N/A)	
		» TRUE P			
4867	4877	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).FixIdent	"aB19 fg"	(N/A)	"
		» aB19 fg" P			
4868	4878	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Altaacstr	123.00	0.001	1.2
		» 3000E+02 P			
4869	4879	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Altabcstr	12345.6	0.001	1.2
		» 3456E+04 P			
4870	4880	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Cstrspdlim	12345.6	0.001	1.2
		» 3456E+04 P			
4871	4881	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Targetalt	12345.6	0.001	1.2
		» 3456E+04 P			
4872	4882	Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Cstraltilim	12345.6	0.001	1.2

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4873	4883	» 3456E+04 P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Spcspd	12345.6	0.001	1.2
4874	4884	» 3456E+04 P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).SpcFpa	1234.56	0.001	1.2
4875	4885	» 3456E+03 P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).FpaVal	True	(N/A)	
4876	4886	» TRUE P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).PathTerm	FA	(N/A)	
4877	4887	» FA P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Clbordescstr	Descentseg	(N/A)	DE
4878	4888	» SCENTSEG P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Altaacstrval	True	(N/A)	
4879	4889	» TRUE P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Altabcstrval	True	(N/A)	
4880	4890	» TRUE P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Spcspdval	True	(N/A)	
4881	4891	» TRUE P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Toosteppath	Tsptop	(N/A)	
4882	4892	» TSPTOP P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Fixdistodest	20006.00	0.001	2.0
4883	4893	» 0060E+04 P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Incouse	12300.0	0.001	1.2
4884	4894	» 3000E+04 P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).ISADev	1000.00	0.001	1.0
4885	4895	» 0000E+03 P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).LegDistance	1000.00	0.001	1.0
4886	4896	» 0000E+03 P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Outcourse	1000.00	0.001	1.0
4887	4897	» 0000E+03 P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Prdairspd.VALUE	1234.5	0.001	1.2
4888	4898	» 3450E+03 P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Prdairspd.SPEED_TYPE	CAS	(N/A)	
4889	4899	» CAS P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Prdalt	123.00	0.001	1.2
4890	4900	» 3000E+02 P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Prdtime	12	(N/A)	
4891	4901	» 12 P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Data(1).Prdgndspd	1000.2	0.001	1.0
4892	4902	» 0020E+03 P			
		Perf_Vdu_Dpkg.Vdu_Buffer.Fpln.Num_GLlegs	2	(N/A)	
4893	4903	» 2 P			
		CTP_PERF_BKGND_PUT_BK_DATA.Int_To_Str_Exec	"10"	(N/A)	
4894	4904	» "10" P			
		CTP_PERF_BKGND_PUT_BK_DATA.Get_Data_Save_State_Exec	False	(N/A)	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4895	4905	» FALSE P				
		Perf_Vdu_Dpkg.Data_Save	Perf_Vdu_Tpkg.Temporary	(N/A)		T
		» EMPORARY P				
4896	4906	Perf_Vdu_Dpkg.Perf_Data_Save_Initiated	True	(N/A)		
		» TRUE P				
4897	4907					
4898	4908					
4899	4909	====> All 59 Comparisons Passed <====				
4900	4910					
4901	4911					
4902	4912	TESTID: 52				
4903	4913	If the scratch flight plan is not being used, the predictions-output indication shall be set				
4904	4914	according to Table 11.14-4.				
4905	4915					
4906	4916	Scratchfpln Change_Occurred Psperfregst Predictions_Output				
4907	4917	FALSE TRUE TRUE FALSE				
4908	4918	FALSE TRUE FALSE FALSE				
4909	4919	FALSE FALSE TRUE FALSE				
4910	4920	FALSE FALSE FALSE TRUE				
4911	4921					
4912	4922	PERF_SDD_4544_INT				
4913	4923					
4914	4924	Perf copy of CDA Enabled shall be initialized to OPC option				
4915	4925	Options_And_Data_Pkg.CDA_Enable				
4916	4926					
4917	4927	PERF_SDD_09025				
4918	4928	REQUIREMENTS UNDER EVALUATION : PERF_SDD_4544_INT, PERF_SDD_09025				
4919	4929	SUPPORTING REQUIREMENTS : N/A				
4920	4930					
4921	4931					
4922	4932	INPUT				VALUE
4923	4933	-----				
		» -----				
4924	4934	Perf_Background_Dpkg.Pcitin.Itinerary				Prim_Fp
		» ln_Preds				
4925	4935	Perf_Background_Dpkg.Pcgmtime.Gpc_Time				
		» 2				
4926	4936	Perf_Time_Dpkg:body.Data_Storage(Active).Gmt				
		» 0				
4927	4937	Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq				
		» 0				
4928	4938	Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid				
		» False				
4929	4939	Perf_Background_Dpkg.Destination_Data.Efob.Data				

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4930	4940	» 20.0			
		Perf_Background_Dpkg.Destination_Data.Efob.Valid			
		» True			
4931	4941	Perf_Background_Dpkg.Destination_Data.Ete.Data			
		» 50.0			
4932	4942	Perf_Background_Dpkg.Destination_Data.Ete.Valid			
		» True			
4933	4943	Perf_Background_Dpkg.Destination_Data.Firstpass			
		» True			
4934	4944	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Data			
		» 0.0			
4935	4945	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Valid			
		» False			
4936	4946	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Ete.Data			
		» 0.0			
4937	4947	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Ete.Valid			
		» False			
4938	4948	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Firstpass			
		» False			
4939	4949	Perf_Background_Dpkg.Pcfpln			Ac
		» tprimary			
4940	4950	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress			
		» False			
4941	4951	Perf_Background_Dpkg.Preds_Output(Active)			
		» False			
4942	4952	Perf_Dpkg.CDA_Enabled			
		» true			
4943	4953	Options_And_Data_Pkg:body.All_Options.Cda_Enable			
		» false			
4944	4954	Change			
		» False			
4945	4955	Change			
		» False			
4946	4956	Change			
		» False			
4947	4957				
4948	4958				
4949	4959	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
4950	4960	-----	-----	-----	-----
		» -----			
4951	4961	Perf_Background_Dpkg.Preds_Output(Active)	True	(N/A)	
		» TRUE P			
4952	4962	Perf_Dpkg.CDA_Enabled	false	(N/A)	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

```

4953 4963 » FALSE P
4954 4964
4955 4965 ===== All 2 Comparisons Passed =====
4956 4966
4957 4967
4958 4968 TESTID: 53
4959 4969 If the scratch flight plan is not being used, the predictions-output indication shall be set
4960 4970 according to Table 11.14-4.
4961 4971
4962 4972 Scratchfpln      Change_Occurred  Psperfreqst      Predictions_Output
4963 4973 FALSE            TRUE              TRUE              FALSE
4964 4974 FALSE            TRUE              FALSE             FALSE
4965 4975 FALSE            FALSE             TRUE              FALSE
4966 4976 FALSE            FALSE             FALSE             TRUE
4967 4977
4968 4978 PERF_SDD_4544_INT
4969 4979
4970 4980 Perf copy of CDA Enabled shall be initialized to OPC option
4971 4981 Options_And_Data_Pkg.CDA_Enable
4972 4982
4973 4983 PERF_SDD_09025
4974 4984 REQUIREMENTS UNDER EVALUATION : PERF_SDD_4544_INT, PERF_SDD_09025
4975 4985 SUPPORTING REQUIREMENTS : N/A
4976 4986
4977 4987
4978 4988 INPUT                                                     VALUE
4979 4989 -----
4980 4990 Perf_Background_Dpkg.Pcitin.Itinerary                      Prim_Fp
4981 4991 » ln_Preds
4982 4992 Perf_Background_Dpkg.Pcgmtime.Gpc_Time
4983 4993 » 2
4984 4994 Perf_Time_Dpkg:body.Data_Storage(Active).Gmt
4985 4995 » 0
4986 4996 Perf_Time_Dpkg:body.Data_Storage(Active).Prddataseq
4987 4997 » 0
4988 4998 Perf_Time_Dpkg:body.Data_Storage(Active).Rta_Control.Valid
4989 4999 » False
4990 5000 Perf_Background_Dpkg.Destination_Data.Efob.Data
4991 5001 » 20.0
4992 5002 Perf_Background_Dpkg.Destination_Data.Efob.Valid
4993 5003 » True
4994 5004 Perf_Background_Dpkg.Destination_Data.Ete.Data

```

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

4988	4998	» 50.0			
		Perf_Background_Dpkg.Destination_Data.Ete.Valid			
		» True			
4989	4999	Perf_Background_Dpkg.Destination_Data.Firstpass			
		» True			
4990	5000	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Data			
		» 0.0			
4991	5001	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Efob.Valid			
		» False			
4992	5002	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Ete.Data			
		» 0.0			
4993	5003	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Ete.Valid			
		» False			
4994	5004	Perf_Interface_Dpkg:body.Data_Storage.Pgdestdata(Active).Firstpass			
		» False			
4995	5005	Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress			
		» TRUE			
4996	5006	Perf_Background_Dpkg.Preds_Output(Active)			
		» TRUE			
4997	5007	Perf_Background_Dpkg.Pcfpln			Ac
		» tprimary			
4998	5008	Perf_Dpkg.CDA_Enabled			
		» false			
4999	5009	Options_And_Data_Pkg:body.All_Options.Cda_Enable			
		» true			
5000	5010				
5001	5011				
5002	5012	define Verify_SDD_07059_Invalid := False			
5003	5013				
5004	5014				
5005	5015	INPUT			VALUE
5006	5016	-----			-----
		» -----			
5007	5017	Change			
		» False			
5008	5018				
5009	5019				
5010	5020	OUTPUT	EXPECTED	TOLERANCE	ACTUAL
		» P/F			
5011	5021	-----			-----
		» -----			
5012	5022	Perf_Background_Dpkg.Preds_Output(Active)	False	(N/A)	
		» FALSE P			
5013	5023	Perf_Dpkg.CDA_Enabled	true	(N/A)	

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

		» TRUE P
5014	5024	
5015	5025	
5016	5026	====> All 2 Comparisons Passed <====
5017	5027	
5018	5028	
5019		Test End Time: Aug 26 11:16:37 2014
	5029	Test End Time: Oct 21 09:14:56 2014
5020	5030	Test Generation System (TGS) Version v4.5.2, ps4082887-103
5021	5031	Current Program Library
5022		<del>C:\a340\Builds\st2050\BLD_ST2050\Libraries\A29_cert_system.alb (root)</del>
5023		<del>C:\A340\Builds\ST2050\BLD_ST2050\Libraries\CSW_ABPEG_006.ALB</del>
5024		<del>C:\A340\Builds\ST2050\BLD_ST2050\Libraries\mtyp.ALB</del>
5025		<del>C:\A340\Builds\ST2050\BLD_ST2050\Libraries\Iotbx.alb</del>
5026		<del>C:\A340\Builds\ST2050\BLD_ST2050\Libraries\mcdu.alb</del>
5027		<del>C:\A340\Builds\ST2050\BLD_ST2050\Libraries\Tou.alb</del>
5028		<del>C:\A340\Builds\ST2050\BLD_ST2050\Libraries\Nam.alb</del>
5029		<del>C:\A340\Builds\ST2050\BLD_ST2050\Libraries\Ops.alb</del>
5030		<del>C:\A340\Builds\ST2050\BLD_ST2050\Libraries\Bsvc.alb</del>
5031		<del>C:\A340\Builds\ST2050\BLD_ST2050\Libraries\Opc.alb</del>
5032		<del>C:\A340\Builds\ST2050\BLD_ST2050\Libraries\Io.alb</del>
5033		<del>C:\A340\Builds\ST2050\BLD_ST2050\Libraries\Isb.alb</del>
5034		<del>C:\A340\Builds\ST2050\BLD_ST2050\Libraries\prnt.alb</del>
5035		<del>C:\A340\Builds\ST2050\BLD_ST2050\Libraries\w429.alb</del>
5036		<del>C:\A340\Builds\ST2050\BLD_ST2050\Libraries\Com.alb</del>
5037		<del>C:\A340\Builds\ST2050\BLD_ST2050\Libraries\Fm.alb</del>
5038		<del>C:\a340\Builds\st2050\BLD_st2050\Libraries\fm2.alb</del>
5039		<del>C:\workspace\A340\ST2050\CTP_A340S1A_PERF_BKGND_PUT_BK_DATA\new\fm2_p.alb</del>
5040		<del>C:\workspace\A340\ST2050\CTP_A340S1A_PERF_BKGND_PUT_BK_DATA\new\my_fm2.alb</del>
	5032	C:\a340\Builds\st2099\BLD_st2099\Libraries\A29_cert_system.alb (root)
	5033	C:\A340\Builds\ST2099\BLD_ST2099\Libraries\CSW_ABPEG_006.ALB
	5034	C:\A340\Builds\ST2099\BLD_ST2099\Libraries\mtyp.ALB
	5035	C:\A340\Builds\ST2099\BLD_ST2099\Libraries\Iotbx.alb
	5036	C:\A340\Builds\ST2099\BLD_ST2099\Libraries\mcdu.alb
	5037	C:\A340\Builds\ST2099\BLD_ST2099\Libraries\Tou.alb
	5038	C:\A340\Builds\ST2099\BLD_ST2099\Libraries\Nam.alb
	5039	C:\A340\Builds\ST2099\BLD_ST2099\Libraries\Ops.alb
	5040	C:\A340\Builds\ST2099\BLD_ST2099\Libraries\Bsvc.alb
	5041	C:\A340\Builds\ST2099\BLD_ST2099\Libraries\Opc.alb
	5042	C:\A340\Builds\ST2099\BLD_ST2099\Libraries\Io.alb
	5043	C:\A340\Builds\ST2099\BLD_ST2099\Libraries\Isb.alb
	5044	C:\A340\Builds\ST2099\BLD_ST2099\Libraries\prnt.alb
	5045	C:\A340\Builds\ST2099\BLD_ST2099\Libraries\w429.alb
	5046	C:\A340\Builds\ST2099\BLD_ST2099\Libraries\Com.alb



File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.rst (continued)

5047	C:\A340\Builds\ST2099\BLD_ST2099\Libraries\Fm.alb
5048	C:\a340\Builds\st2099\BLD_st2099\Libraries\fm2.alb
5049	C:\Workspace\A340\ST2050\CTP_A340S1A_PERF_BKGND_PUT_BK_DATA\new\fm2_p.alb
5050	C:\Workspace\A340\ST2050\CTP_A340S1A_PERF_BKGND_PUT_BK_DATA\new\my_fm2.alb

Mode: All Lines

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.dsp

1	1	#####
		>> **
2	2	## DSP Generator Tool Version 1.0
3	3	#####
		>> **
4	4	##
5	5	## CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.DSP
6	6	##
7	7	## NOTE:
8	8	## A. "Any" SCR that is mentioned in this DSP file must contain the prefix "SCR_disposed#: "
9	9	## B. Template of this DSP file is created by tool and it should not be modified/deleted.
10	10	## C. If any information is not applicable then mark the corresponding field as N/A instead of deleting it.
11	11	## D. If more than one SCR has to be used for one issue, make separate entry. SCRs should not be captured
12	12	## in the same line using comma or any other separators.
13	13	##
14	14	##
15	15	
16	16	-----
		>> --
17	17	1. REASON_FOR_FAILURES_OF_TEST_CASE(S):
18	18	## The below mentioned group of lines need to be repeated for each Test case ID, which is having test failures in it.
19	19	-----
		>> --
20	20	
21		<del>Test_case_Id: 12</del>
22		<del>#_of_Failures: 1</del>
23		<del>Failed_Requirements: PERF_SDD_3155_INT</del>
24		<del>SCR_disposed#: 49180.00</del>
25		<del>SCR_PROJECT: FMS2000</del>
26		<del>SCR_SUB_PROJECT: A3XX</del>
27		<del>Disposition: The build is not matching the SDD requirement and the failures will get removed in the future.</del>
28		
29		<del>Test_case_Id: 13</del>
30		<del>#_of_Failures: 1</del>
31		<del>Failed_Requirements: PERF_SDD_3155_INT</del>
32		<del>SCR_disposed#: 49180.00</del>
33		<del>SCR_PROJECT: FMS2000</del>
34		<del>SCR_SUB_PROJECT: A3XX</del>
35		<del>Disposition: The build is not matching the SDD requirement and the failures will get removed in the future.</del>
36		
37		<del>Test_case_Id: 14</del>
38		<del>#_of_Failures: 1</del>

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.dsp (continued)

39		<del>Failed_Requirements: PERF_SDD_3155_INT</del>
40		<del>SCR_disposed#: 49180.00</del>
41		<del>SCR_PROJECT: FMS2000</del>
42		<del>SCR_SUB_PROJECT: A3XX</del>
43		<del>Disposition: The build is not matching the SDD requirement and the failures will get removed in the future.</del>
44		
45		Test_case_Id: 17
46		#_of_Failures: 1
47		Failed_Requirements: PERF_SDD_3155_INT
48		SCR_disposed#: 49180.00
49		SCR_PROJECT: FMS2000
50		SCR_SUB_PROJECT: A3XX
51		Disposition: The build is not matching the SDD requirement and the failures will get removed in the future.
	21	Test_case_Id: N/A
	22	#_of_Failures: N/A
	23	Failed_Requirements: N/A
	24	SCR_disposed#: N/A
	25	SCR_PROJECT: N/A
	26	SCR_SUB_PROJECT: N/A
	27	Disposition: N/A
52	28	
53	29	-----
		» --
54	30	2. COVERAGE_PROBLEM(S):
55	31	## Standard excuse and SCR related details need to be mentioned for each and every sub unit separately.
56	32	-----
		» --
57	33	Compilation_Unit_Name: PRF_BKGND_PKG.PUT_BK_DATA.PUT_BK_DATA
58	34	Uncovered_Code:
59	35	
60	36	
61		<del>TCH(Test_Coverage_Hole)_Excuse: N/A</del>
	37	TCH(Test_Coverage_Hole)_Excuse: N/A
62	38	N/A
63	39	SCR_disposed#: N/A
64	40	SCR_PROJECT: N/A
65	41	SCR_SUB_PROJECT: N/A
66	42	
67	43	-----
		» --
68	44	3. ANY_OTHER_ISSUE(S):
69	45	## A. Every entry in Any_Other_Issue should be followed by a SCR_number, its corresponding CM 21 project and subprojec
		» t.
70	46	## B. If SCR is not applicable then mention N/A.

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.dsp (continued)

71	47	## C. If more than one SCR has to be used for one issue, make separate entry. SCRs should not be captured
72	48	## in the same line using comma or any other separators.
73	49	----- » --
74		<del>(1) Perf_SRD_12094 has been partially tested here. It is tested completely in following</del>
75		<del>CTPs: CTP_A340S1A_PERF_FPLN_FIND_DES and CTP_A340S1A_PERF_FPLN_CONTROL_INTEG.</del>
	50	
	51	(i) Perf_SRD_12094 has been partially tested here. It is tested completely in following
	52	CTPs:- CTP_A340S1A_PERF_FPLN_FIND_DES and CTP_A340S1A_PERF_FPLN_CONTROL_INTEG.
76	53	SCR_disposed#: N/A
77	54	SCR_PROJECT: N/A
78	55	SCR_SUB_PROJECT: N/A
79	56	
80		<del>(2) PERF_SRD_2051 is partially tested in this CTP and is tested in following CTPs:-</del>
81		<del>CTP_A340S1A_PERF_FPLN_PKG.</del>
	57	(ii) PERF_SRD_2051 is partially tested in this CTP and is tested in following CTPs:
	58	CTP_A340S1A_PERF_FPLN_PKG.
82	59	SCR_disposed#: N/A
83	60	SCR_PROJECT: N/A
84	61	SCR_SUB_PROJECT: N/A
85	62	
86		<del>(3) PERF_SRD_2020 is partially tested in this CTP and is tested in CTP_A340S1A_PERF_MAXALT.</del>
	63	(iii) PERF_SRD_2020 is partially tested in this CTP and is tested in CTP_A340S1A_PERF_MAXALT.
87	64	SCR_disposed#: N/A
88	65	SCR_PROJECT: N/A
89	66	SCR_SUB_PROJECT: N/A
90	67	
91		<del>(4) PERF_SRD_2087_INT is partially tested in this CTP and is tested in CTP_A340S1A_PERF_HM_PKG.</del>
	68	(iv) PERF_SRD_2087_INT is partially tested in this CTP and is tested in CTP_A340S1A_PERF_HM_PKG.
92	69	SCR_disposed#: N/A
93	70	SCR_PROJECT: N/A
94	71	SCR_SUB_PROJECT: N/A
95	72	
96		<del>(5) PERF_SRD_10869 is partially tested here and is tested in following CTPs:-</del>
97		<del>CTP_A340S1A_PERF_BKGND_INVAL_PREDS, CTP_A340S1A_PERF_PUT_DEST_ETA,</del>
98		<del>CTP_A340S1A_PERF_FPLN_PROCESS_GUIDTERM, CTP_A340S1A_PERF_CHGPROC_PRIM_FPLN_CHG.</del>
	73	(v) PERF_SRD_10869 is partially tested here and is tested in following CTPs:
	74	CTP_A340S1A_PERF_BKGND_INVAL_PREDS, CTP_A340S1A_PERF_PUT_DEST_ETA,
	75	CTP_A340S1A_PERF_FPLN_PROCESS_GUIDTERM, CTP_A340S1A_PERF_CHGPROC_PRIM_FPLN_CHG.
99	76	SCR_disposed#: N/A
100	77	SCR_PROJECT: N/A
101	78	SCR_SUB_PROJECT: N/A
102	79	
103	80	-----

File: CTP\_A340S1A\_PERF\_BKGND\_PUT\_BK\_DATA.dsp (continued)

```
104      81  »  --
105      81  4. SPECIAL_EXECUTION_INSTRUCTION(S):
106      82  ## Capture all additional information and/or supporting file(s) required for this CTP execution.
107      83  ## For example:
108      84  ## (i) "nav_db23.o" is required for execution.
109      85  ## (ii) "apex_traps.o"/gen=xx and "common file"/gen=xx are required for execution.
110      86  ## Database_Details:
111      87  ## 1. <Enter the database name>
112      88  -----
113      89  »  --
114      90  "apex_traps.o"/gen=3 and "CTP_A340S1A_PERF_COMMON_OBJECTS.C"/gen=1 are required for execution.
115      91
116      92  Database_Details:
117      93  1. N/A
118      94
119      95  ***** End of Report *****
120      96  »  **
```

Mode: All Lines

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB

1	1	--
2	2	-- STUB FILE
3	3	--
4	4	-- CTP_A340S1A_PERF_BND_PUT_BK_DAT_CDK_FUEL.STB
5	5	--
6	6	-- REASON FOR STUBBING : The following procedures Put_Block_Fuel,Put_Final_Fuel,Put_Route_Reserve
7	7	-- and function Route_Reserve in the package body cdk_fuel_weight_dpkg are stubbed out to
8	8	-- aid for CTP testing.
9	9	--
	10	-- Original File Name: Cdk_Fuel_Weight_Dpkg.ada
10	11	--*****
11	12	with Conversion_Const_Pkg;
12	13	with Fmcs_Base_Types;
13	14	with Io_Fmf_Out_Dpkg;
14	15	with Io_Interface_Tpkg;
15	16	with Fprequestrec_Types;
16	17	with Ops_Data_Retained_Pkg;
17	18	with Options_And_Data_Pkg;
18	19	with Portable_Types_Pkg;
19	20	with Cdk_Fpln_Tpkg;
20	21	with Cdk_Int_Saved_Dpkg;
21	22	with Cdk_Fuel_Pred_Page_Dpkg;
22	23	with Sys_Perf_Interface_Dpkg;
23	24	with Cdk_Fuel_Utility_Pkg;
24	25	with Base_Domain_Services_Tpkg;
25	26	with Fpln_Ext_Dpkg;
26	27	with Cdk_Vert_Dpkg;
	28	with Altn_And_Fuels_Tpkg;
	29	with Shared_Const_Pkg;
27	30	
	31	
28	32	use Io_Interface_Tpkg;
29	33	use Portable_Types_Pkg;
30	34	use Fprequestrec_Types;
31	35	use Cdk_Fpln_Tpkg;
32	36	use Base_Domain_Services_Tpkg;
	37	use Shared_Const_Pkg;
	38	
33	39	with Ctp_Perf_bkgnd_Put_Bk_Data;
34	40	
35	41	package body Cdk_Fuel_Weight_Dpkg is
36		---

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

37		
38		<del>---  @DESCRIPTION: This package contains the fuel weight data.</del>
39		<del>---</del>
40		<del>---</del>
41		<del>---</del>
42		<del>---</del>
43		<del>--- </del>
	42	
44	43	
45	44	
46	45	type Fpln_Dependent_Data_T is record
47		<del>Altn_Fuel : Fmcs_Base_Types.Float_32_Entry_Stat.State;</del>
	46	Altn_Fuel : Cdk_Alternate_Tpkg.Altn_Fuel_Array_T;
48	47	Min_Fuel_At_Dest : Fmcs_Base_Types.Float_32_Entry_Stat.State;
49	48	Block_Fuel : Fmcs_Base_Types.Float_32_Entry_Stat.State;
50	49	Taxi_Fuel : Fmcs_Base_Types.Float_32_Entry_Stat.State;
51	50	Zero_Fuel_Weight : Fmcs_Base_Types.Float_32_Entry_Stat.State;
52	51	Takeoff_Gross_Weight : Fmcs_Base_Types.Float_32_Entry_Stat.State;
53	52	Zero_Fuel_Weight_Cg : Fmcs_Base_Types.Float_32_Entry_Stat.State;
54	53	Fuel_Planning_Mode : Cdk_Fuel_Weight_Tpkg.Fuel_Plan_State_T;
55	54	Block_Calc : Boolean;
56	55	Final_Fuel : Cdk_Fuel_Weight_Tpkg.Final_Fuel_Time_T;
57	56	Route_Reserve : Cdk_Fuel_Weight_Tpkg.Reserve_Record_T;
58	57	end record;
59	58	
60	59	type Fpln_Dependent_Array_T is array ( Fprequestrec_Types.Major_Actorsec_Type ) of Fpln_Dependent_Data_T;
61	60	
62	61	Fpln_Data : Fpln_Dependent_Array_T;
63	62	
64	63	type Singular_Data_T is record
65	64	Gross_Weight : Fmcs_Base_Types.Float_32_Entry_Stat.State;
66	65	Jettison_Gross_Weight : Io_Interface_Tpkg.Float_32_Valid.Normal;
67	66	Center_Of_Gravity : Fmcs_Base_Types.Float_32_Entry_Stat.State;
68	67	Fuel_On_Board : Fmcs_Base_Types.Float_32_Entry_Stat.State;
69	68	Ff_Sensor_Selected : Boolean;
70	69	Fq_Sensor_Selected : Boolean;
71	70	end record;
72	71	
73	72	Single_Data : Singular_Data_T;
74	73	
75	74	
76	75	procedure Put_Block_Fuel
77	76	(
78	77	Fpln : in Fprequestrec_Types.Major_Actorsec_Type;

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

79	78	Block_Fuel : in Fmcs_Base_Types.Float_32_Entry_Stat.State
80	79	) is
81		<del>-----+-----</del>
82		<del>-----+----- @DESCRIPTION: Store the Block Fuel.</del>
83		<del>-----+-----</del>
84		<del>-----+-----</del>
85		<del>-----+-----</del>
86		<del>-----+----- @UNITS: Fpln Active, Secondary</del>
87		<del>-----+----- Block_Fuel Metric Tonnes</del>
88		<del>-----+-----</del>
89		<del>-----+----- @SPECIAL CONSIDERATIONS: Block Fuel can be entered in either thousands of pounds</del>
90		<del>-----+----- or thousand of kilograms. However, it will always be</del>
91		<del>-----+----- stored in Metric Tonnes.</del>
92		<del>-----+-----</del>
	80	
93	81	begin
94	82	Fpln_Data( Fpln ).Block_Fuel := Block_Fuel;
95	83	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Block_Fuel_Exec := True;
96	84	end Put_Block_Fuel;
97	85	
98	86	
99	87	function Block_Fuel
100	88	(
101	89	Fpln : Fprequestrec_Types.Major_Actorsec_Type
102	90	) return Fmcs_Base_Types.Float_32_Entry_Stat.State is
		»
103		<del>-----+-----</del>
104		<del>-----+----- @DESCRIPTION: Retrieve the Block Fuel.</del>
105		<del>-----+-----</del>
106		<del>-----+-----</del>
107		<del>-----+-----</del>
108		<del>-----+----- @UNITS: Fpln Active, Secondary</del>
109		<del>-----+----- Return Value Metric Tonnes</del>
110		<del>-----+-----</del>
111		<del>-----+----- @SPECIAL CONSIDERATIONS: N/A</del>
112		<del>-----+-----</del>
	91	
113	92	begin
114	93	return Fpln_Data( Fpln ).Block_Fuel;
115	94	end Block_Fuel;
116	95	
117	96	
118	97	procedure Put_Taxi_Fuel
119	98	(



File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

120	99	Fpln : in Fprequestrec_Types.Major_Actorsec_Type;
121	100	Taxi_Fuel : in Fmcs_Base_Types.Float_32_Entry_Stat.State
122	101	) is
123		<del>-----+     @DESCRIPTION: Store the Taxi Fuel.               @UNITS: Fpln - Active, Secondary   Taxi_Fuel Metric Tonnes       @SPECIAL CONSIDERATIONS: Taxi Fuel can be entered in either thousands of pounds   or thousand of kilograms. However, it will always be   stored in Metric Tonnes    -----+</del>
	102	
135	103	begin
136	104	Fpln_Data( Fpln ).Taxi_Fuel := Taxi_Fuel;
137	105	end Put_Taxi_Fuel;
138	106	
139	107	
140	108	function Taxi_Fuel
141	109	(
142	110	Fpln : Fprequestrec_Types.Major_Actorsec_Type
143	111	) return Fmcs_Base_Types.Float_32_Entry_Stat.State is
		»
144		<del>-----+     @DESCRIPTION: Retrieve the Taxi Fuel.               @UNITS: Fpln - Active, Secondary   Return Value Metric Tonnes       @SPECIAL CONSIDERATIONS: N/A    -----+</del>
	112	
154	113	begin
155	114	return Fpln_Data( Fpln ).Taxi_Fuel;
156	115	end Taxi_Fuel;
157	116	
158	117	
159	118	procedure Put_Zero_Fuel_Weight
160	119	(

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

161	120	Fpln : in Fprequestrec_Types.Major_Actorsec_Type;
162	121	Zero_Fuel_Weight : in Fmcs_Base_Types.Float_32_Entry_Stat.State
163	122	) is
164		<del>-----+     @DESCRIPTION: Store the Zero Fuel Weight.             @UNITS: Fpln - Active, Secondary   Zero_Fuel_Weight Metric Tonnes       @SPECIAL CONSIDERATIONS: Zero Fuel Weight can be entered in either thousands of   pounds or thousand of kilograms. However, it will always   be stored in Metric Tonnes.    -----+</del>
	123	
176	124	begin
177	125	Fpln_Data( Fpln ).Zero_Fuel_Weight := Zero_Fuel_Weight;
178	126	end Put_Zero_Fuel_Weight;
179	127	
180	128	
181	129	function Zero_Fuel_Weight
182	130	(
183	131	Fpln : Fprequestrec_Types.Major_Actorsec_Type
184	132	) return Fmcs_Base_Types.Float_32_Entry_Stat.State is
		»
185		<del>-----+     @DESCRIPTION: Retrieve the Zero Fuel Weight.             @UNITS: Fpln - Active, Secondary   Return Value Metric Tonnes       @SPECIAL CONSIDERATIONS: N/A    -----+</del>
	133	
195	134	begin
196	135	return Fpln_Data( Fpln ).Zero_Fuel_Weight;
197	136	end Zero_Fuel_Weight;
198	137	
199	138	
200	139	function Takeoff_Gross_Weight
201	140	(

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

202	141	Fpln : Fprequestrec_Types.Major_Actorsec_Type
203	142	) return Fmcs_Base_Types.Float_32_Entry_Stat.State is
		»
204		<del>_____+_____</del>
205		<del>_____+_____</del> @DESCRIPTION: Returns the computed Takeoff Gross Weight based on Engine State
206		<del>_____+_____</del> and Flight plan type.
207		<del>_____+_____</del>
208		<del>_____+_____</del>
209		<del>_____+_____</del>
210		<del>_____+_____</del> @UNITS: Fpln Active, Secondary
211		<del>_____+_____</del> Return Value Metric Tonnes
212		<del>_____+_____</del>
213		<del>_____+_____</del> @SPECIAL CONSIDERATIONS: N/A
214		<del>_____+_____</del>
	143	
215	144	Takeoff_Gross_Weight : Fmcs_Base_Types.Float_32_Entry_Stat.State := (Data => 0.0, Status => Io_Interface_Tpkg.Inva
		» lid);
216	145	
		»
217	146	Flight_Phase : Base_Domain_Services_Tpkg.Flight_Phase_Type;
218	147	
219	148	begin
220	149	
221	150	if ( Fpln = Fprequestrec_Types.Active or else ( Fpln = Fprequestrec_Types.Secondary and then
222	151	Cdk_Int_Saved_Dpkg.Cksecorigin = Cdk_Fpln_Tpkg.Copiedact ) ) then
223	152	
224	153	-- Get Flight Phase
225	154	Fpln_Ext_Dpkg.Get_Flight_Phase( Flight_Phase );
226	155	
227	156	-- IF FLIGHT PHASE IS PREFLIGHT
228	157	if ( Flight_Phase = Base_Domain_Services_Tpkg.Preflight ) then
229	158	
230	159	-- ENGINES ARE OFF
231	160	if ( not Cdk_Vert_Dpkg.Engines_On ) then
232	161	-- SET TOW
233	162	if ( ( Cdk_Fuel_Weight_Dpkg.Zero_Fuel_Weight( Fpln ).Status /= Io_Interface_Tpkg.Invalid ) and then
234	163	( Cdk_Fuel_Weight_Dpkg.Block_Fuel( Fpln ).Status /= Io_Interface_Tpkg.Invalid ) ) then
235	164	
236	165	Takeoff_Gross_Weight.Data :=
237	166	Cdk_Fuel_Weight_Dpkg.Zero_Fuel_Weight( Fpln ).Data + Cdk_Fuel_Weight_Dpkg.Block_Fuel( Fpln ).Data -
238	167	Cdk_Fuel_Weight_Dpkg.Taxi_Fuel( Fpln ).Data;
239	168	
240	169	Takeoff_Gross_Weight.Status := Io_Interface_Tpkg.Valid;
241	170	

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

```

242 171         end if;
243 172
244 173         -- ENGINES ARE ON
245 174     else
246 175         -- SET TOW
247 176         -- IF EITHER FQMS GW OR WBBS GW IS VALID
248 177         if ( Cdk_Fuel_Pred_Page_Dpkg.Gross_Weight.Status /= Io_Interface_Tpkg.Invalid ) then
249 178
250 179             Takeoff_Gross_Weight.Status := Io_Interface_Tpkg.Valid;
251 180
252 181             Takeoff_Gross_Weight.Data := Cdk_Fuel_Pred_Page_Dpkg.Gross_Weight.Data - Cdk_Fuel_Weight_Dpkg.Taxi_Fuel( F
» pln ).Data;
253 182
254 183         end if;
255 184     end if;
256 185
257 186     elsif ( Flight_Phase > Base_Domain_Services_Tpkg.Preflight ) then
258 187
259 188         Takeoff_Gross_Weight.Status := Io_Interface_Tpkg.Invalid;
260 189
261 190     end if;
262 191
263 192     else
264 193
265 194         -- SET TOW FOR SPECIFIED SECONDARY FLIGHT PLAN IF ZFW AND BLOCK FUEL STATUS IS VALID
266 195         if ( ( Cdk_Fuel_Weight_Dpkg.Zero_Fuel_Weight( Fpln ).Status /= Io_Interface_Tpkg.Invalid ) and then
267 196             ( Cdk_Fuel_Weight_Dpkg.Block_Fuel( Fpln ).Status /= Io_Interface_Tpkg.Invalid ) ) then
268 197
269 198             Takeoff_Gross_Weight.Data :=
270 199                 Cdk_Fuel_Weight_Dpkg.Zero_Fuel_Weight( Fpln ).Data + Cdk_Fuel_Weight_Dpkg.Block_Fuel( Fpln ).Data -
271 200                 Cdk_Fuel_Weight_Dpkg.Taxi_Fuel( Fpln ).Data;
272 201
273 202             Takeoff_Gross_Weight.Status := Io_Interface_Tpkg.Valid;
274 203
275 204         end if;
276 205
277 206     end if;
278 207
279 208     return Takeoff_Gross_Weight;
280 209
281 210 end Takeoff_Gross_Weight;
282 211
283 212
284 213 procedure Put_Gross_Weight

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

285	214	(
286	215	Gross_Weight : in Fmcs_Base_Types.Float_32_Entry_Stat.State
287	216	) is
288		<del>-----+ -----+ @DESCRIPTION: Store the Gross Weight. -----+ -----+ -----+ -----+ -----+ @UNITS: Gross_Weight -- Metric Tonnes -----+ -----+ @SPECIAL CONSIDERATIONS: Gross Weight can be entered in either thousands of -----+ pounds or thousand of kilograms. However, it will -----+ always be stored in Metric Tonnes. -----+</del>
	217	
299	218	begin
300	219	-- Stubbed body
301	220	null;
302	221	end Put_Gross_Weight;
303	222	
304	223	
305	224	function Gross_Weight return Fmcs_Base_Types.Float_32_Entry_Stat.State is
306		<del>-----+ -----+ @DESCRIPTION: Retrieve the Gross Weight. -----+ -----+ -----+ -----+ -----+ @UNITS: Return Value -- Metric Tonnes -----+ -----+ @SPECIAL CONSIDERATIONS:N/A -----+</del>
	225	
315	226	begin
316	227	-- Stubbed body, return invalid data
317	228	return (Data => 0.0, Status => Io_Interface_Tpkg.Invalid);
318	229	end Gross_Weight;
319	230	
320	231	
321	232	procedure Put_Jettison_Gross_Weight
322	233	(
323	234	Jettison_Gross_Weight : in Io_Interface_Tpkg.Float_32_Valid.Normal
324	235	) is
325		<del>-----+ -----+ @DESCRIPTION: Store the Jettison Gross Weight. -----+</del>
326		

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

327		<del>-----+-----</del>
328		<del>-----+-----</del>
329		<del>-----+-----</del>
330		<del>-----+-----@UNITS: Jettison_Gross_Weight Metric Tonnes</del>
331		<del>-----+-----</del>
332		<del>-----+-----@SPECIAL CONSIDERATIONS: Jettison_Gross_Weight can be entered in either thousands-</del>
333		<del>-----+-----of pounds or thousand of kilograms. However, it will-</del>
334		<del>-----+-----always be stored in Metric Tonnes.</del>
335		<del>-----+-----</del>
<hr/>		
	236	
336	237	begin
337	238	Single_Data.Jettison_Gross_Weight := Jettison_Gross_Weight;
338	239	end Put_Jettison_Gross_Weight;
339	240	
340	241	
341	242	function Jettison_Gross_Weight return Io_Interface_Tpkg.Float_32_Valid.Normal is
342		<del>-----+-----</del>
343		<del>-----+-----@DESCRIPTION: Retrieve the Jettison_Gross_Weight.</del>
344		<del>-----+-----</del>
345		<del>-----+-----</del>
346		<del>-----+-----</del>
347		<del>-----+-----@UNITS: Return Value Metric Tonnes</del>
348		<del>-----+-----</del>
349		<del>-----+-----@SPECIAL CONSIDERATIONS:N/A</del>
350		<del>-----+-----</del>
<hr/>		
	243	
351	244	begin
352	245	return Single_Data.Jettison_Gross_Weight;
353	246	end Jettison_Gross_Weight;
354	247	
355	248	
356	249	procedure Put_Zero_Fuel_Weight_Cg
357	250	(
358	251	Fpln                  : in Fprequestrec_Types.Major_Actorsec_Type;
359	252	Zero_Fuel_Weight_Cg : in Fmcs_Base_Types.Float_32_Entry_Stat.State
360	253	) is
361		<del>-----+-----</del>
362		<del>-----+-----@DESCRIPTION: Store the Zero Fuel Weight CG.</del>
363		<del>-----+-----</del>
364		<del>-----+-----</del>
365		<del>-----+-----</del>
366		<del>-----+-----@UNITS: Fpln Active, Secondary</del>
367		<del>-----+-----Zero_Fuel_Weight_Cg % MAC</del>
368		<del>-----+-----</del>

369		<del>-----+ @SPECIAL CONSIDERATIONS:N/A -----+</del>
370		
	254	
371	255	begin
372	256	Fpln_Data( Fpln ).Zero_Fuel_Weight_Cg := Zero_Fuel_Weight_Cg;
373	257	end Put_Zero_Fuel_Weight_Cg;
374	258	
375	259	
376	260	function Zero_Fuel_Weight_Cg
377	261	(
378	262	Fpln : Fprequestrec_Types.Major_Actorsec_Type
379	263	) return Fmcs_Base_Types.Float_32_Entry_Stat.State is
		»
380		<del>-----+   @DESCRIPTION: Retrieve the Zero Fuel Weight CG.           @UNITS: Fpln - Active, Secondary   Return Value % MAC       @SPECIAL CONSIDERATIONS:N/A    -----+</del>
381		
382		
383		
384		
385		
386		
387		
388		
389		
	264	
390	265	begin
391	266	return Fpln_Data( Fpln ).Zero_Fuel_Weight_Cg;
392	267	end Zero_Fuel_Weight_Cg;
393	268	
394	269	
395	270	procedure Put_Center_Of_Gravity
396	271	(
397	272	Center_Of_Gravity : in Fmcs_Base_Types.Float_32_Entry_Stat.State
398	273	) is
399		<del>-----+   @DESCRIPTION: Store the CG.           @UNITS: Center_Of_Gravity - % MAC       @SPECIAL CONSIDERATIONS:N/A    -----+</del>
400		
401		
402		
403		
404		
405		
406		
407		
	274	
408	275	begin

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

409	276	-- Stubbed body
410	277	null;
411	278	end Put_Center_Of_Gravity;
412	279	
413	280	
414	281	function Center_Of_Gravity return Fmcs_Base_Types.Float_32_Entry_Stat.State is
415		<del>-----+ @DESCRIPTION: Retrieve the CG. -----+ -----+ -----+ -----+ @UNITS: Return Value -- % MAC -----+ @SPECIAL CONSIDERATIONS: N/A -----+</del>
	282	
424	283	begin
425	284	
426	285	-- Stubbed body, return invalid data
427	286	return (Data => 0.0, Status => Io_Interface_Tpkg.Invalid);
428	287	end Center_Of_Gravity;
429	288	
430	289	
431	290	procedure Put_Fuel_Planning_Mode
432	291	(
433	292	Fpln                    : in Fprequestrec_Types.Major_Actorsec_Type;
434	293	Fuel_Planning_Mode : in Cdk_Fuel_Weight_Tpkg.Fuel_Plan_State_T
435	294	) is
436		<del>-----+ @DESCRIPTION: Store the Fuel Planning Mode. -----+ -----+ -----+ @UNITS: Fpln -- Active, Secondary -----+           Fuel_Planning_Mode -- Fuelinit, Validzfw, Fuelplncalc, Waitoncnfrm, Fuelpredict -----+ @SPECIAL CONSIDERATIONS: N/A -----+</del>
	295	
446	296	begin
447	297	Fpln_Data( Fpln ).Fuel_Planning_Mode := Fuel_Planning_Mode;
448	298	end Put_Fuel_Planning_Mode;
449	299	
450	300	



File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

451	301	function Fuel_Planning_Mode
452	302	(
453	303	Fpln : Fprequestrec_Types.Major_Actorsec_Type
454	304	) return Cdk_Fuel_Weight_Tpkg.Fuel_Plan_State_T is
		»
455		<del>-----+-----</del>
456		<del>----- ----- @DESCRIPTION: Retrieve the Fuel Planning Mode.</del>
457		<del>----- -----</del>
458		<del>----- -----</del>
459		<del>----- -----</del>
460		<del>----- ----- @UNITS: Fpln Active, Secondary</del>
461		<del>----- ----- Return Value Fuelinit, Validzfw, Fuelplncalc, Waitonenfrm, Fuelpredict</del>
462		<del>----- -----</del>
463		<del>----- ----- @SPECIAL CONSIDERATIONS: N/A</del>
464		<del>-----+-----</del>
	305	
465	306	begin
466	307	return Fpln_Data( Fpln ).Fuel_Planning_Mode;
467	308	end Fuel_Planning_Mode;
468	309	
469	310	
470	311	procedure Put_Block_Calc
471	312	(
472	313	Fpln : in Fprequestrec_Types.Major_Actorsec_Type;
473	314	Block_Calc : in Boolean
474	315	) is
475		<del>-----+-----</del>
476		<del>----- ----- @DESCRIPTION: Store the Block Calc.</del>
477		<del>----- -----</del>
478		<del>----- -----</del>
479		<del>----- -----</del>
480		<del>----- ----- @UNITS: Fpln Active, Secondary</del>
481		<del>----- ----- Block_Calc True/False</del>
482		<del>----- -----</del>
483		<del>----- ----- @SPECIAL CONSIDERATIONS: N/A</del>
484		<del>-----+-----</del>
	316	
485	317	begin
486	318	Fpln_Data( Fpln ).Block_Calc := Block_Calc;
487	319	end Put_Block_Calc;
488	320	
489	321	
490	322	function Block_Calc
491	323	(

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

492	324	Fpln : Fprequestrec_Types.Major_Actorsec_Type
493	325	) return Boolean is
494		<del>-----+-----</del>
495		<del>-----+ @DESCRIPTION: Retrieve the Block Calc.</del>
496		<del>-----+-----</del>
497		<del>-----+-----</del>
498		<del>-----+-----</del>
499		<del>-----+ @UNITS: Fpln Active, Secondary</del>
500		<del>-----+ Return Value True/False</del>
501		<del>-----+-----</del>
502		<del>-----+ @SPECIAL CONSIDERATIONS:N/A</del>
503		<del>-----+-----</del>
	326	
504	327	begin
505	328	return Fpln_Data( Fpln ).Block_Calc;
506	329	end Block_Calc;
507	330	
508	331	
509	332	procedure Put_Final_Fuel
510	333	(
511	334	Fpln : in Fprequestrec_Types.Major_Actorsec_Type;
512	335	Final_Fuel : in Cdk_Fuel_Weight_Tpkg.Final_Fuel_Time_T
513	336	) is
514		<del>-----+-----</del>
515		<del>-----+ @DESCRIPTION: Store the Final Fuel.</del>
516		<del>-----+-----</del>
517		<del>-----+-----</del>
518		<del>-----+-----</del>
519		<del>-----+ @UNITS: Fpln Active, Secondary</del>
520		<del>-----+ Final_Fuel Record Type, see type definition</del>
521		<del>-----+-----</del>
522		<del>-----+ @SPECIAL CONSIDERATIONS:N/A</del>
523		<del>-----+-----</del>
	337	
524	338	begin
525	339	Fpln_Data( Fpln ).Final_Fuel := Final_Fuel;
526	340	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Final_Fuel_Exec := True;
527	341	end Put_Final_Fuel;
528	342	
529	343	
530	344	function Final_Fuel
531	345	(
532	346	Fpln : Fprequestrec_Types.Major_Actorsec_Type
533	347	) return Cdk_Fuel_Weight_Tpkg.Final_Fuel_Time_T is

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

534		<del>-----+-----</del>
535		<del>----- ----- @DESCRIPTION: Retrieve the Final Fuel.</del>
536		<del>-----+-----</del>
537		<del>-----+-----</del>
538		<del>-----+-----</del>
539		<del>----- ----- @UNITS: Fpln Active, Secondary</del>
540		<del>----- ----- Return Value Record Type, see type definition</del>
541		<del>-----+-----</del>
542		<del>----- ----- @SPECIAL CONSIDERATIONS:N/A</del>
543		<del>-----+-----</del>
	348	
544	349	begin
545	350	return Fpln_Data( Fpln ).Final_Fuel;
546	351	end Final_Fuel;
547	352	
548	353	
549	354	procedure Put_Route_Reserve
550	355	(
551	356	Fpln              : in Fprequestrec_Types.Major_Actorsec_Type;
552	357	Route_Reserve : in Cdk_Fuel_Weight_Tpkg.Reserve_Record_T
553	358	) is
554		<del>-----+-----</del>
555		<del>----- ----- @DESCRIPTION: Store the Route Reserve.</del>
556		<del>-----+-----</del>
557		<del>-----+-----</del>
558		<del>-----+-----</del>
559		<del>----- ----- @UNITS: Fpln Active, Secondary</del>
560		<del>----- ----- Route_Reserve Record Type, see type definition</del>
561		<del>-----+-----</del>
562		<del>----- ----- @SPECIAL CONSIDERATIONS:N/A</del>
563		<del>-----+-----</del>
	359	
564	360	begin
565	361	Fpln_Data( Fpln ).Route_Reserve := Route_Reserve;
566	362	Ctp_Perf_Bkgnd_Put_Bk_data.Put_Route_Reserve_Exec := True;
567	363	end Put_Route_Reserve;
568	364	
569	365	
570	366	function Route_Reserve
571	367	(
572	368	Fpln : Fprequestrec_Types.Major_Actorsec_Type
573	369	) return Cdk_Fuel_Weight_Tpkg.Reserve_Record_T is
		»
574		<del>-----+-----</del>

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

575		<del>-----+-----</del>	<del>@DESCRIPTION: Retrieve the Route Reserve.</del>
576		<del>-----+-----</del>	
577		<del>-----+-----</del>	
578		<del>-----+-----</del>	
579		<del>-----+-----</del>	<del>@UNITS: Fpln Active, Secondary</del>
580		<del>-----+-----</del>	<del>Return Value Record Type, see type definition</del>
581		<del>-----+-----</del>	
582		<del>-----+-----</del>	<del>@SPECIAL CONSIDERATIONS: N/A</del>
583		<del>-----+-----</del>	
	370		
584	371		begin
585	372		return Ctp_Perf_Bkgnd_Put_Bk_Data.Route_Reserve;
586	373		end Route_Reserve;
587	374		
588	375		
589	376		procedure Put_Fuel_On_Board
590	377		(
591	378		Fuel_On_Board : in Fmcs_Base_Types.Float_32_Entry_Stat.State
592	379		) is
593		<del>-----+-----</del>	
594		<del>-----+-----</del>	<del>@DESCRIPTION: Store the Fuel On Board. This is the FOB that is displayed on the</del>
595		<del>-----+-----</del>	<del>Fuel Pred page. The value can either be pilot entered (status field of</del>
596		<del>-----+-----</del>	<del>Pilot_Entered), computed by PERF (status field of Valid), or not valid</del>
597		<del>-----+-----</del>	<del>(status field of Invalid).</del>
598		<del>-----+-----</del>	
599		<del>-----+-----</del>	
600		<del>-----+-----</del>	
601		<del>-----+-----</del>	<del>@UNITS: Fuel_On_Board Metric Tonnes</del>
602		<del>-----+-----</del>	
603		<del>-----+-----</del>	<del>@SPECIAL CONSIDERATIONS: This procedure only applies to A340S1A. It should not be used</del>
604		<del>-----+-----</del>	<del>by A340.</del>
605		<del>-----+-----</del>	
	380		
606	381		begin
607	382		-- Stubbed body
608	383		null;
609	384		end Put_Fuel_On_Board;
610	385		
611	386		
612	387		function Fuel_On_Board return Fmcs_Base_Types.Float_32_Entry_Stat.State is
613		<del>-----+-----</del>	
614		<del>-----+-----</del>	<del>@DESCRIPTION: Retrieve the Fuel On Board. This is the FOB that is displayed on the</del>
615		<del>-----+-----</del>	<del>Fuel Pred page. The value can either be pilot entered (status field of</del>
616		<del>-----+-----</del>	<del>Pilot_Entered), computed by PERF (status field of Valid), or not valid</del>

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

617		<del>(status field of Invalid).</del>
618		<del>-----</del>
619		<del>-----</del>
620		<del>-----</del>
621		<del>@UNITS: Return Value Metric Tonnes</del>
622		<del>-----</del>
623		<del>@SPECIAL CONSIDERATIONS: This function only applies to A340S1A. It should not be used</del>
624		<del>by A340.</del>
625		<del>-----</del>
<hr/>		
	388	
626	389	begin
627	390	-- Stubbed body, return invalid data
628	391	return (Data => 0.0, Status => Io_Interface_Tpkg.Invalid);
629	392	end Fuel_On_Board;
630	393	
631	394	
632	395	procedure Put_Ff_Sensor_Selected
633	396	(
634	397	Ff_Sensor_Selected : in Boolean
635	398	) is
636		<del>-----</del>
637		<del>@DESCRIPTION: Store the FF Sensor Selected flag. A True value indicates that</del>
638		<del>the FF sensor has been selected on the Fuel Pred page. A False value</del>
639		<del>indicates that the FF sensor is not selected on the Fuel Pred page.</del>
640		<del>-----</del>
641		<del>-----</del>
642		<del>-----</del>
643		<del>@UNITS: ff_sensor_selected True/False</del>
644		<del>-----</del>
645		<del>@SPECIAL CONSIDERATIONS: This procedure only applies to A340S1A. It should not be used</del>
646		<del>by A340.</del>
647		<del>-----</del>
<hr/>		
	399	
648	400	begin
649	401	-- Stubbed body
650	402	null;
651	403	end Put_Ff_Sensor_Selected;
652	404	
653	405	
654	406	function Ff_Sensor_Selected return Boolean is
655		<del>-----</del>
656		<del>@DESCRIPTION: Retrieve the FF Sensor Selected flag. A True value indicates that</del>
657		<del>the FF sensor has been selected on the Fuel Pred page. A False value</del>
658		<del>indicates that the FF sensor is not selected on the Fuel Pred page.</del>

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

659		<del>-----+-----</del>
660		<del>-----+-----</del>
661		<del>-----+-----</del>
662		<del>-----+-----@UNITS: Return Value True/False</del>
663		<del>-----+-----</del>
664		<del>-----+-----@SPECIAL CONSIDERATIONS: This function only applies to A340S1A. It should not be used</del>
665		<del>-----+-----by A340.</del>
666		<del>-----+-----</del>
<hr/>		
	407	
667	408	begin
668	409	-- Stubbed body
669	410	return False;
670	411	end Ff_Sensor_Selected;
671	412	
672	413	
673	414	procedure Put_Fq_Sensor_Selected
674	415	(
675	416	Fq_Sensor_Selected : in Boolean
676	417	) is
677		<del>-----+-----</del>
678		<del>-----+-----@DESCRIPTION: Store the FQ Sensor Selected flag. A True value indicates that</del>
679		<del>-----+-----the FQ sensor has been selected on the Fuel Pred page. A False value</del>
680		<del>-----+-----indicates that the FQ sensor is not selected on the Fuel Pred page.</del>
681		<del>-----+-----</del>
682		<del>-----+-----</del>
683		<del>-----+-----</del>
684		<del>-----+-----@UNITS: ff_sensor_selected True/False</del>
685		<del>-----+-----</del>
686		<del>-----+-----@SPECIAL CONSIDERATIONS: This procedure only applies to A340S1A. It should not be used</del>
687		<del>-----+-----by A340.</del>
688		<del>-----+-----</del>
<hr/>		
	418	
689	419	begin
690	420	
691	421	-- Stubbed body
692	422	null;
693	423	end Put_Fq_Sensor_Selected;
694	424	
695	425	
696	426	function Fq_Sensor_Selected return Boolean is
697		<del>-----+-----</del>
698		<del>-----+-----@DESCRIPTION: Retrieve the FQ Sensor Selected flag. A True value indicates that</del>
699		<del>-----+-----the FQ sensor has been selected on the Fuel Pred page. A False value</del>
700		<del>-----+-----indicates that the FQ sensor is not selected on the Fuel Pred page.</del>

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

701		<del>-----+-----</del>
702		<del>-----+-----</del>
703		<del>-----+-----</del>
704		<del>-----+ @UNITS: Return Value True/False</del>
705		<del>-----+-----</del>
706		<del>-----+ @SPECIAL CONSIDERATIONS: This function only applies to A340S1A. It should not be used</del>
707		<del>-----+----- by A340.</del>
708		<del>-----+-----</del>
	427	
709	428	begin
710	429	
711	430	-- Stubbed body
712	431	return False;
713	432	end Fq_Sensor_Selected;
714	433	
715		
	434	
	435	
716	436	procedure Put_Min_Fuel_At_Dest
717	437	(
718	438	Fpln : in Fprequestrec_Types.Major_Actorsec_Type;
719	439	Min_Fuel_At_Dest : in Fmcs_Base_Types.Float_32_Entry_Stat.State
720	440	) is
721		<del>-----+-----</del>
722		<del>-----+ @DESCRIPTION: Store the Min_Fuel_At_Dest</del>
723		<del>-----+-----</del>
724		<del>-----+-----</del>
725		<del>-----+-----</del>
726		<del>-----+ @UNITS: Fpln Active and Secondary</del>
727		<del>-----+ Min_Fuel_At_Dest Record Type, see type definition</del>
728		<del>-----+-----</del>
729		<del>-----+ @SPECIAL CONSIDERATIONS: N/A</del>
730		<del>-----+-----</del>
	441	
731	442	begin
732	443	--stubbed body
733	444	
734	445	null;
735	446	
736	447	end Put_Min_Fuel_At_Dest;
737	448	function Min_Fuel_At_Dest
738	449	(
739	450	Fpln : Fprequestrec_Types.Major_Actorsec_Type
740	451	) return Fmcs_Base_Types.Float_32_Entry_Stat.State is

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

741		»
742		<del>_____</del>
743		<del>_____ @DESCRIPTION: Retrieve the in Fuel at dest.</del>
744		<del>_____ CURRENTLY STUBBED OUT. INCLUDED FOR PERF ROLLOVER.</del>
745		<del>_____</del>
746		<del>_____</del>
747		<del>_____ @UNITS: Fpln Active, Secondary,</del>
748		<del>_____ Return Value - Metric Tonnes</del>
749		<del>_____</del>
750		<del>_____ @SPECIAL CONSIDERATIONS: N/A</del>
751		<del>_____</del>
752		<del>_____ Min_Fuel_At_Dest : Fmcs_Base_Types.Float_32_Entry_Stat.State := (Data =&gt; 0.0, Status =&gt; Io_Interface_Tpkg.Invalid</del>
		<del>» );</del>
	452	
	453	Final_Fuel_Record : Fmcs_Base_Types.Float_32_Entry_Stat.State := (Data => 0.0, Status => Io_Interface_Tpkg.Invalid
		» d);
753	454	»
	455	Altn_Fuel_Record : Fmcs_Base_Types.Float_32_Entry_Stat.State := (Data => 0.0, Status => Io_Interface_Tpkg.Invalid
		» );
	456	
754	457	begin
755		-
756		<del>_____ Currently stubbed until</del>
757		<del>_____ return Min_Fuel_At_Dest;</del>
	458	Final_Fuel_Record := Cdk_Fuel_Weight_Dpkg.Final_Fuel( Fpln ).Fuel;
	459	Altn_Fuel_Record := Cdk_Fuel_Weight_Dpkg.Altn_Fuel( Fpln );
	460	if ( Fpln_Data( Fpln ).Min_Fuel_At_Dest.Status /= Io_Interface_Tpkg.Pilot_Entered ) then
	461	
	462	if ( Final_Fuel_Record.Status /= Io_Interface_Tpkg.Invalid ) and then
	463	( Altn_Fuel_Record.Status /= Io_Interface_Tpkg.Invalid ) then
	464	
	465	Fpln_Data( Fpln ).Min_Fuel_At_Dest.Data := Final_Fuel_Record.Data + Altn_Fuel_Record.Data;
		»
	466	
	467	Fpln_Data( Fpln ).Min_Fuel_At_Dest.Status := Io_Interface_Tpkg.Valid;
	468	
	469	else
	470	
	471	Fpln_Data( Fpln ).Min_Fuel_At_Dest.Data := 0.0;
		»
	472	
	473	Fpln_Data( Fpln ).Min_Fuel_At_Dest.Status := Io_Interface_Tpkg.Invalid;



File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

474		
475		end if;
476		
477		end if;
478		
479		return Fpln_Data( Fpln ).Min_Fuel_At_Dest;
758	480	
759		<del>end Min_Fuel_At_Dest;</del>
	481	end Min_Fuel_At_Dest;
	482	
760	483	
761	484	
762		<del>procedure Put_Altn_Fuel</del>
763		<del>(</del>
764		<del>Fpln : in Fprequestrec_Types.Major_Fp_Type;</del>
765		<del>Altn_Fuel : in Fmcs_Base_Types.Float_32_Entry_Stat.State</del>
766		<del>) is</del>
767		<del>+</del>
768		<del>-- @DESCRIPTION: Store the Alternate Fuel At Destination.</del>
769		<del>CURRENTLY STUBBED OUT UNTIL DCR 104_2.</del>
770		<del> </del>
771		<del>-- </del>
772		<del> </del>
773		<del>-- @UNITS: Fpln - Active, Secondary, Temporary</del>
774		<del>Altn_Fuel Metric Tonnes</del>
775		<del> </del>
776		<del>-- @SPECIAL CONSIDERATIONS: Alternate Fuel can be entered in either thousands of pounds</del>
777		<del>or thousand of kilograms. However, it will always be</del>
778		<del>-- stored in Metric Tonnes.</del>
779		<del>+</del>
780		<del>begin</del>
781		<del>-</del>
782		<del>null;</del>
783		<del>_____</del>
784		<del>end Put_Altn_Fuel;</del>
	485	procedure Put_Altn_Fuel
	486	(
	487	Fpln : in Fprequestrec_Types.Major_Actorsec_Type;
	488	Altn_Fuel : in Fmcs_Base_Types.Float_32_Entry_Stat.State;
	489	Index : in Portable_Types_Pkg.Integer_32 := 1000
	490	) is
	491	
	492	
	493	Local_Index : Portable_Types_Pkg.Integer_32;

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

```

494     Pcaltnpreds : Altn_And_Fuels_Tpkg.Altnperfrec := Sys_Perf_Interface_Dpkg.Pcaltnpreds( Fpln );
495     begin
496
497         -- If index is the default value, this procedure is being called without an index
498         -- which indicates that the user wants to store fuel for selected alternate.
499         if Index = 1000 then
500             Local_Index := Cdk_Vert_Dpkg.Selected_Alternate( Fpln ).Selectedaltn;
501         else
502             Local_Index := Index;
503         end if;
504
505         Fpln_Data( Fpln ).Altn_Fuel( Local_Index ) := Altn_Fuel;
506
507         -- Update the Perf fuel
508         Pcaltnpreds.Altnfuel( Local_Index ) := Altn_Fuel;
509
510         -- Perf needs to recompute only if fuel is neither pilot-entered nor invalid
511         -- If fuel is invalid, the perf validity flag should be set to false anyway.
512         if Altn_Fuel.Status /= Io_Interface_Tpkg.Pilot_Entered then
513             Pcaltnpreds.Altnfuelval( Local_Index ) := False;
514         else
515             Pcaltnpreds.Altnfuelval( Local_Index ) := True;
516         end if;
517
518         Sys_Perf_Interface_Dpkg.Put_Pcaltnpreds( Fpln, Pcaltnpreds );
519
520     end Put_Altn_Fuel;

```

785 521  
786 522

```

787  function Altn_Fuel
788  (
789  Fpln : Fprequestrec.Types.Major_Fp_Type
790  ) return Fmes_Base_Types.Float_32_Entry_State is
791  »
792  +
793    @DESCRIPTION: Retrieve the Alternate Fuel. CURRENTLY STUBBED
794    OUT UNTIL DCR 104_2 updates.
795    |
796    |
797    @UNITS: Fpln Active, Secondary, Temporary
798    Return Value Metric Tonnes
799    |
800    @SPECIAL CONSIDERATIONS: N/A

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

801	<del>_____+</del>
802	<del>_____</del>
803	<del>Altn_Fuel : Fmcs_Base_Types.Float_32_Entry_Stat.State := (Data =&gt; 0.0, Status =&gt; Io_Interface_Tpkg.Invalid);</del>
804	<del>_____</del>
805	<del>begin</del>
806	<del>_____</del>
807	<del>Currently just a stub here until DCR 104_2 is implemented.</del>
808	<del>return Altn_Fuel;</del>

  

523	function Altn_Fuel
524	(
525	Fpln : Fprequestrec_Types.Major_Actorsec_Type;
526	Index : Portable_Types_Pkg.Integer_32 := 1000
527	) return Fmcs_Base_Types.Float_32_Entry_Stat.State is
528	»
529	
530	Local_Index : Portable_Types_Pkg.Integer_32;
531	
532	begin
533	
534	-- If index is the default value, this procedure is being called without an index
535	-- which indicates that the user wants to store fuel for selected alternate.
536	if Index = 1000 then
537	Local_Index := Cdk_Vert_Dpkg.Selected_Alternate( Fpln ).Selectedaltn;
538	else
539	Local_Index := Index;
540	end if;
541	
542	if ( Fpln_Data( Fpln ).Altn_Fuel( Local_Index ).Status /= Io_Interface_Tpkg.Pilot_Entered ) then
543	
544	if ( Sys_Perf_Interface_Dpkg.Pcaltnpreds( Fpln ).Altnfuelval( Local_Index ) ) then
545	
546	-- Alternate Fuel is not Pilot Entered and Computed Alternate Fuel Exists.
547	Fpln_Data( Fpln ).Altn_Fuel( Local_Index ).Data := Sys_Perf_Interface_Dpkg.Pcaltnpreds( Fpln ).Altnfuel( Local
548	» _Index ).Data;
549	Fpln_Data( Fpln ).Altn_Fuel( Local_Index ).Status := Io_Interface_Tpkg.Valid;
550	
551	else
552	
553	-- Alternate Fuel is not Pilot Entered and Computed Alternate Fuel not Exists.
554	Fpln_Data( Fpln ).Altn_Fuel( Local_Index ).Data := 0.0;
555	
556	Fpln_Data( Fpln ).Altn_Fuel( Local_Index ).Status := Io_Interface_Tpkg.Invalid;

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

	557	
	558	end if;
	559	
	560	end if;
	561	
	562	return Fpln_Data( Fpln ).Altn_Fuel( Local_Index );
	563	
	564	end Altn_Fuel;
	565	
	566	
	567	procedure Put_All_Altn_Fuel
	568	(
	569	Fpln : in Fprequestrec_Types.Major_Actorsec_Type;
	570	Altn_Fuel : in Cdk_Alternate_Tpkg.Altn_Fuel_Array_T
	571	) is
	572	
	573	
	574	
	575	Pcaltnpreds : Altn_And_Fuels_Tpkg.Altnperfrec := Sys_Perf_Interface_Dpkg.Pcaltnpreds( Fpln );
	576	
	577	begin
	578	
	579	for I in Portable_Types_Pkg.Integer_32 range Shared_Const_Pkg.No_Alternates..Shared_Const_Pkg.Last_Alternate loop
	580	»
	581	Fpln_Data( Fpln ).Altn_Fuel( I ) := Altn_Fuel( I );
	582	
	583	Pcaltnpreds.Altnfuel( I ) := Altn_Fuel( I );
	584	
	585	end loop;
809	586	
810		<del>end Altn_Fuel;</del>
	587	Sys_Perf_Interface_Dpkg.Put_Pcaltnpreds( Fpln, Pcaltnpreds );
	588	
	589	
	590	end Put_All_Altn_Fuel;
811	591	
812	592	
813		<del>procedure Initialize is</del>
814		<del>+</del>
815		<del>— @DESCRIPTION: Initialize all data internal to this package.</del>
816		<del>—</del>
817		<del>—</del>
818		<del>—</del>

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

```

819 | ----- | @UNITS: N/A
820 | ----- |
821 | ----- | @SPECIAL CONSIDERATIONS: N/A
822 | ----- |

```

---

```

593 | function All_Altfn_Fuel
594 | (
595 |   Fpln : Fprequestrec_Types.Major_Actorsec_Type
596 | ) return Cdk_Alternate_Tpkg.Altfn_Fuel_Array_T is
597 |
598 |
599 | begin
600 |
601 |   for I in Portable_Types_Pkg.Integer_32 range Shared_Const_Pkg.No_Alternates..Shared_Const_Pkg.Last_Alternate loop
602 |   »
603 |     if ( Fpln_Data( Fpln ).Altfn_Fuel( I ).Status /= Io_Interface_Tpkg.Pilot_Entered ) then
604 |
605 |       if ( Sys_Perf_Interface_Dpkg.Pcaltnpreds( Fpln ).Altfnfuelval( I ) ) then
606 |
607 |         -- Alternate Fuel is not Pilot Entered and Computed Alternate Fuel Exists.
608 |
609 |         Fpln_Data( Fpln ).Altfn_Fuel( I ).Data := Sys_Perf_Interface_Dpkg.Pcaltnpreds( Fpln ).Altfnfuel( I ).Data;
610 |
611 |         Fpln_Data( Fpln ).Altfn_Fuel( I ).Status := Io_Interface_Tpkg.Valid;
612 |
613 |       else
614 |
615 |         -- Alternate Fuel is not Pilot Entered and Computed Alternate Fuel not Exists.
616 |         Fpln_Data( Fpln ).Altfn_Fuel( I ).Data := 0.0;
617 |
618 |         Fpln_Data( Fpln ).Altfn_Fuel( I ).Status := Io_Interface_Tpkg.Invalid;
619 |
620 |       end if;
621 |
622 |     end if;
623 |
624 |   end loop;
625 |
626 |   return Fpln_Data( Fpln ).Altfn_Fuel;
627 |
628 | end All_Altfn_Fuel;
629 |
630 |
631 | procedure Initialize is

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

	632	
823	633	begin
824	634	if not Ops_Data_Retained_Pkg.Ops_Sram_Valid then
825	635	
826	636	-- Initialize memory that should be retained over cold starts
827	637	
828		<del>Fpln_Data( Fprequestrec_Types.Active ).Altn_Fuel := ( Data =&gt; 0.0, Status =&gt; Io_Interface_Tpkg.Invalid );</del>
	638	<del>Fpln_Data( Fprequestrec_Types.Active ).Altn_Fuel := ( others =&gt; ( Data =&gt; 0.0, Status =&gt; Io_Interface_Tpkg.Invalid</del>
		<del>» id ) );</del>
829	639	Fpln_Data( Fprequestrec_Types.Active ).Min_Fuel_At_Dest := ( Data => 0.0, Status => Io_Interface_Tpkg.Invalid );
830	640	Fpln_Data( Fprequestrec_Types.Active ).Block_Fuel := (Data => 0.0, Status => Io_Interface_Tpkg.Invalid);
831	641	Fpln_Data( Fprequestrec_Types.Active ).Taxi_Fuel :=
832	642	(Data => Options_And_Data_Pkg.Taxi_Fuel * Conversion_Const_Pkg.Kg_To_Tons, Status => Io_Interface_Tpkg.Valid
		» );
833	643	Fpln_Data( Fprequestrec_Types.Active ).Zero_Fuel_Weight := (Data => 0.0, Status => Io_Interface_Tpkg.Invalid);
834	644	Fpln_Data( Fprequestrec_Types.Active ).Takeoff_Gross_Weight := (Data => 0.0, Status => Io_Interface_Tpkg.Invalid
		» );
835	645	Fpln_Data( Fprequestrec_Types.Active ).Zero_Fuel_Weight_Cg := (Data => 0.0, Status => Io_Interface_Tpkg.Invalid)
		» ;
836	646	Fpln_Data( Fprequestrec_Types.Active ).Fuel_Planning_Mode := Cdk_Fuel_Weight_Tpkg.Fuelinit;
837	647	Fpln_Data( Fprequestrec_Types.Active ).Block_Calc := False;
838	648	Fpln_Data( Fprequestrec_Types.Active ).Final_Fuel := (Fuel => (Data => 0.0, Status => Io_Interface_Tpkg.Invalid)
		» ,
839	649	Time => (Data => 0.0, Status => Io_Interface_Tpkg.Invalid)
		» ,
840	650	Default_Data_Status => Cdk_Fuel_Weight_Tpkg.Ftnotvalid);
841	651	Fpln_Data( Fprequestrec_Types.Active ).Route_Reserve := (Fuel => (Data => 0.0, Status => Io_Interface_Tpkg.Invalid
		» id),
842		<del>Percent =&gt; (Data =&gt; Options_And_Data_Pkg.Route_Reserve_</del>
		<del>» Percent,</del>
843		<del>Status =&gt; Io_Interface_Tpkg.Pilot_Entered),</del>
	652	Percent => (Data => Options_And_Data_Pkg.Route_Reserve_
		» Percent, Status => Io_Interface_Tpkg.Pilot_Entered),
844	653	Default => Options_And_Data_Pkg.Route_Reserve_Percent,
845	654	Default_Data_Status => Cdk_Fuel_Weight_Tpkg.Dbpercent,
846	655	Pilot_Entered_Change => False);
	656	
847	657	
848	658	Fpln_Data( Fprequestrec_Types.Secondary ) := Fpln_Data( Fprequestrec_Types.Active );
849	659	
850	660	Single_Data := (Gross_Weight => (Data => 0.0, Status => Io_Interface_Tpkg.Invalid),
851	661	Jettison_Gross_Weight => (Data => 0.0, Valid => False),
852	662	Center_Of_Gravity => (Data => 0.0, Status => Io_Interface_Tpkg.Invalid),
853	663	Fuel_On_Board => (Data => 0.0, Status => Io_Interface_Tpkg.Invalid),

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

```

854 664          Ff_Sensor_Selected => True,
855 665          Fq_Sensor_Selected => True);
856 666
857 667      end if;
858 668
859 669      -- Initialize memory that should not be retained over cold starts
860 670
861 671      -- Reset Taxi Fuel to the default value if it does not currently have an entered value
862 672
863 673      if ( Fpln_Data( Fprequestrec_Types.Active ).Taxi_Fuel.Status /= Io_Interface_Tpkg.Pilot_Entered ) then
864 674          Fpln_Data( Fprequestrec_Types.Active ).Taxi_Fuel :=
865 675          (Data => Options_And_Data_Pkg.Taxi_Fuel * Conversion_Const_Pkg.Kg_To_Tons, Status => Io_Interface_Tpkg.Valid
866 676      » );
867 677
868 678      if ( Fpln_Data( Fprequestrec_Types.Secondary ).Taxi_Fuel.Status /= Io_Interface_Tpkg.Pilot_Entered ) then
869 679          Fpln_Data( Fprequestrec_Types.Secondary ).Taxi_Fuel :=
870 680          (Data => Options_And_Data_Pkg.Taxi_Fuel * Conversion_Const_Pkg.Kg_To_Tons, Status => Io_Interface_Tpkg.Valid
871 681      » );
872 682
873 683      -- Invalidate Jettison Gross Weight if the Jettison option is not enabled
874 684      if not Options_And_Data_Pkg.Jettison_Enable then
875 685          Single_Data.Jettison_Gross_Weight := (Data => 0.0, Valid => False);
876 686      end if;
877 687
878 688
879 689      -- Initialize all the reinitialization flags (bit 11 flags) to False after a cold start.
880 690      Io_Fmf_Out_Dpkg.Zfw_Requires_Reinitialization.Put(Data => False, Is_Valid => True);
881 691      Io_Fmf_Out_Dpkg.Zfcg_Requires_Reinitialization.Put(Data => False, Is_Valid => True);
882 692
883 693
884 694      end Initialize;
885 695
886 696
887 697      function Togw_For_Minspeed_Check return Fmcs_Base_Types.Float_32_Entry_Stat.State is
888 698      »

```

```

888 698      +
889 699      -- @DESCRIPTION: Retrieve the Takeoff_Gross_Weight for minimum speed limitation checks corresponding
890 700      -- to active flight plan since the checks are applicable for active flight plan only in
891 701      -- preflight flight phase
892 702
893 703
894 704      -- @ANCHOR: CDCK_CODE_08494

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

895		
896		<del>@UNITS: Return Value - Metric Tonnes</del>
897		
898		<del>@SPECIAL CONSIDERATIONS: N/A</del>
899		
	698	
900	699	
901	700	Togw_For_Minspeed_Check : Fmcs_Base_Types.Float_32_Entry_Stat.State := (Data => 0.0, Status => Io_Interface_Tpkg.I
		» nvalid);
902	701	
		»
903	702	Temp_Zfw_Record : Fmcs_Base_Types.Float_32_Entry_Stat.State :=
904	703	Cdk_Fuel_Weight_Dpkg.Zero_Fuel_Weight( Fprequestrec_Types.Active );
905	704	
906	705	Temp_Block_Fuel_Record : Fmcs_Base_Types.Float_32_Entry_Stat.State :=
907	706	Cdk_Fuel_Weight_Dpkg.Block_Fuel( Fprequestrec_Types.Active );
908	707	
909	708	Temp_Taxi_Fuel_Record : Fmcs_Base_Types.Float_32_Entry_Stat.State :=
910	709	Cdk_Fuel_Weight_Dpkg.Taxi_Fuel( Fprequestrec_Types.Active );
911	710	
912	711	Temp_Fob_Record : Fmcs_Base_Types.Float_32_Entry_Stat.State := Cdk_Fuel_Pred_Page_Dpkg.Fuel_On_Board;
913	712	
914	713	Flight_Phase : Base_Domain_Services_Tpkg.Flight_Phase_Type;
915	714	
916	715	begin
917	716	
918	717	-- Get Flight Phase
919	718	Fpln_Ext_Dpkg.Get_Flight_Phase( Flight_Phase );
920	719	
921	720	-- IF FLIGHT PHASE IS PREFLIGHT
922	721	if ( Flight_Phase = Base_Domain_Services_Tpkg.Preflight ) then
923	722	-- ENGINES ARE OFF
924	723	if ( not Cdk_Vert_DpkgEngines_On ) then
925	724	-- SET TOW as ZFW+BLOCK-TAXI
926	725	if ( ( Temp_Zfw_Record.Status /= Io_Interface_Tpkg.Invalid ) and then
927	726	( Temp_Block_Fuel_Record.Status /= Io_Interface_Tpkg.Invalid ) ) then
928	727	
929	728	Togw_For_Minspeed_Check.Data := ( Temp_Zfw_Record.Data + Temp_Block_Fuel_Record.Data - Temp_Taxi_Fuel_Record
		» .Data );
930	729	
931	730	Togw_For_Minspeed_Check.Status := Io_Interface_Tpkg.Valid;
932	731	
933	732	end if;
934	733	



File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_CDK\_FUEL.STB (continued)

935	734	-- ENGINES ARE ON
936	735	else
937	736	-- SET TOW as ZFW+FOB-TAXI
938	737	if ( ( Temp_Zfw_Record.Status /= Io_Interface_Tpkg.Invalid ) and then
939	738	( Temp_Fob_Record.Status /= Io_Interface_Tpkg.Invalid ) ) then
		»
940	739	
941	740	Togw_For_Minspeed_Check.Status := Io_Interface_Tpkg.Valid;
942	741	
943	742	Togw_For_Minspeed_Check.Data := ( Temp_Zfw_Record.Data + Temp_Fob_Record.Data - Temp_Taxi_Fuel_Record.Data )
		» ;
944	743	
945	744	end if;
946	745	end if;
947	746	
948	747	end if;
949	748	
950	749	
951	750	return Togw_For_Minspeed_Check;
952	751	
953	752	end Togw_For_Minspeed_Check;
954	753	
955	754	end Cdk_Fuel_Weight_Dpkg;
956	755	□

Mode: All Lines

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB

1	1	--
2	2	-- STUB FILE
3	3	--
4	4	-- CTP_A340S1A_PERF_BND_PUT_BK_DAT_COM_LGB.STB
5	5	--
6	6	-- REASON FOR STUBBING: The procedures Get_Lgb_Ctrl_Data, update_legindex and put_perf_capture_return_path_record
7	7	-- in the package body common_lgb have been stubbed out to aid for CTP testing.
8	8	--
9		<del>DATA RIGHTS: HONEYWELL CONFIDENTIAL &amp; PROPRIETARY</del>
10		<del>THIS WORK CONTAINS VALUABLE CONFIDENTIAL AND PROPRIETARY</del>
11		<del>INFORMATION. DISCLOSURE, USE OR REPRODUCTION OUTSIDE OF</del>
12		<del>HONEYWELL INTERNATIONAL, INC. IS PROHIBITED EXCEPT AS</del>
13		<del>AUTHORIZED IN WRITING. THIS UNPUBLISHED WORK IS PROTECTED BY</del>
14		<del>THE LAWS OF THE UNITED STATES AND OTHER COUNTRIES. IN THE</del>
15		<del>EVENT OF PUBLICATION, THE FOLLOWING NOTICE SHALL APPLY:</del>
16		<del>COPR. 2002 HONEYWELL INTERNATIONAL, INC. ALL RIGHTS RESERVED.</del>
17		<del></del>
18		<del></del>
	9	-- Original File Name: Common_Lgb.ada
	10	
19	11	with Airbus_Lgbm;
20	12	with Apex_Partition_Pkg;
21	13	with Apex_Types_Pkg;
22	14	with Bite_Fault_Recovery_Tpkg;
23	15	with Bite_Recover_Gpkg;
24	16	with Common_Lgb_Int_Nonresync_Dpkg;
25	17	with Flight_Pln_Hdr_Types;
26	18	with Fmcs_Partition_Data_Pkg;
27	19	with Lgb_Error_Code_Dpkg;
28	20	with Portable_Types_Pkg;
29	21	with Standard_Angle_Pkg;
30	22	with System;
31	23	with Unchecked_Conversion;
32	24	with Lateral_Offset_Segment_Type_Tpkg;
33	25	use Flight_Pln_Hdr_Types;
34	26	use Portable_Types_Pkg;
35	27	use Standard_Angle_Pkg;
36	28	use Lateral_Offset_Segment_Type_Tpkg;
37	29	
38	30	package body Common_Lgb is
39		<del></del>
40		<del>@DESCRIPTION: This package defines the interface routines between a user and the</del>

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

41		<del>data structures of the lateral guidance buffer. It contains basic</del>
42		<del>user interface routines and custom interface routines. It also</del>
43		<del>contains maintenance routines on the buffer.</del>
44		<del>-----</del>
45		<del>-----</del>
	31	
46	32	
47	33	
48	34	-----
49	35	-- Hidden lateral guidance buffer constants (sizes in 32 bit words)
50	36	-----
51	37	Leg_Size : constant Portable_Types_Pkg.Unsigned_32 := Flight_Pln_Leg_Types.Leg_Rec'Size / 32;
52	38	
53	39	Hdr_Size : constant Portable_Types_Pkg.Unsigned_32 := Flight_Pln_Hdr_Types.Flight_Pln_Hdr_Rec'Size / 32;
54	40	
55	41	-----
56	42	-- Hidden lateral guidance buffer types
57	43	-----
58	44	type Total_Leg_Type is
59	45	array ( Portable_Types_Pkg.Integer_32 range 1..Fmcs_Fp_Guid_Btypes.Max_Total_Routes ) of Portable_Types_Pkg.Integer_32;
60	46	» ger_32;
61	47	type Lgb_Boolean_Arr is array ( Flight_Pln_Leg_Types.Leg_Index_Type range 1..Fmcs_Fp_Guid_Btypes.Max_Total_Legs ) of Boolean;
62	48	» Boolean;
63	49	type Leg_Set_Rec is record
64	50	Legs_In_Use : Lgb_Boolean_Arr;
65	51	Total_Used_Legs : Total_Leg_Type;
66	52	end record;
67	53	
68	54	type Fp_Range_Type is record
69	55	Min, Max : Flight_Pln_Leg_Types.Leg_Index_Type;
70	56	Size : Portable_Types_Pkg.Integer_32;
71	57	end record;
72	58	type Fp_Range_Array is
73	59	array ( Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type range 1..Fmcs_Fp_Guid_Btypes.Max_Total_Routes ) of Fp_Range_Type
74	60	» ;
75	61	Fp_Range : Fp_Range_Array;
76	62	Last_Process : Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
77	63	Pending : Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
78	64	Max_Legs : Portable_Types_Pkg.Integer_32;
79	65	
80	66	subtype Leg_Pointer is System.Address;

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

81      67      subtype Hdr_Pointer is System.Address;
82      68
83      69      Legs_List : Leg_Set_Rec;
84      70
85      71
86      72      -- State Monitor
87      73      Min_Counter_Value : constant Portable_Types_Pkg.Unsigned_32:= 1;
88      74      Max_Counter_Value : constant Portable_Types_Pkg.Unsigned_32:= 10000;
89      75
90      76      type State_Monitor_Record is record
91      77          Change_In_Progress : Boolean := false;
92      78          Change_Counter : Portable_Types_Pkg.Unsigned_32 := Min_Counter_Value;
93      79      end record;
94      80
95      81      type State_Monitor_Array is
96      82          array ( Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type range 1..Fmcs_Fp_Guid_Btypes.Max_Total_Routes ) of State_Monitor
          » _Record;
97      83      State_Monitor : State_Monitor_Array;
98      84
99      85      -----
100     86      -- Hidden lateral guidance buffer objects
101     87      -----
102     88      type Reader_Array is array ( Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type ) of Boolean;
103     89      type Lgb_Access_Header_Rec is record
104     90          Writer           : Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
105     91          Writing          : Boolean;
106     92          Operation       : Flight_Pln_Hdr_Types.Type_Of_Access;
107     93          Total_Write_Access : Boolean;
108     94          Reader          : Reader_Array;
109     95      end record;
110     96      type Lgb_Access_Header_Array is
111     97          array ( Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type range 1..Fmcs_Fp_Guid_Btypes.Max_Total_Routes ) of Lgb_Access_He
          » ader_Rec;
112     98      Lgb_Access_Header : Lgb_Access_Header_Array;
113     99
114    100      type Route_Array is array ( Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type range 1..Fmcs_Fp_Guid_Btypes.Max_Total_Routes )
          » of Boolean;
115    101      type Operation_Array_Rec is record
116    102          Route : Route_Array;
117    103          In_Use : Route_Array;
118    104      end record;
119    105      type Operation_Array is array ( Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type ) of Operation_Array_Rec;
120    106      Lgb_Referee : Operation_Array;
121    107

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

122 108 Init_Bite_Data : constant Common_Lgb_Int_Nonresync_Dpkg.Bite_Recover_Rec :=
123 109     ( False, Fmcs_Fp_Guid_Btypes.Flight_Planning, False, 0, False, 0 );
124 110
125 111 type Writers_Semaphore_Array is
126 112     array ( Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type range 1..
127 113         Fmcs_Fp_Guid_Btypes.Max_Total_Routes ) of Apex_Semaphore_Pkg.Semaphore_Id_Type;
128 114 Writer_Semaphores : Writers_Semaphore_Array;
129 115
130 116 -- The Header
131 117 Header_Control      : Header_Control_Rec;
132 118 Header_Control_Ptr  : Header_Control_Access;
133 119
134 120
135 121 -- The access and privilege table matrix of locked routes
136 122 Read_Write_Matrix : Read_Write_Access_Arr := (others => (others => Lo));
137 123
138 124 Starting_Active : Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
139 125
140 126 -----
141 127 -- function renames
142 128 -----
143 129 function "="
144 130     (
145 131         Left, Right : Apex_Types_Pkg.Status_Code_Type
146 132     ) return Boolean renames Apex_Types_Pkg."=";
147 133
148 134 function "-"
149 135     (
150 136         Left, Right : Standard_Angle_Pkg.Saf_32
151 137     ) return Standard_Angle_Pkg.Saf_32 renames Standard_Angle_Pkg."-";
152 138
153 139 »
154 140 function "<="
155 141     (
156 142         Left, Right : Standard_Angle_Pkg.Saf_32
157 143     ) return Boolean renames Standard_Angle_Pkg."<=";
158 144
159 145 function "="
160 146     (
161 147         Left, Right : Apex_Partition_Pkg.Operating_Mode_Type
162 148     ) return Boolean renames Apex_Partition_Pkg."=";
163 149
164 150 -- -----*
```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

165 151 -- -- Hidden lateral guidance buffer routines
166 152 -- -----*
167 153 function Gleg_Ptr_To_Access is new Unchecked_Conversion(Source => Leg_Pointer, Target => Flight_Pln_Leg_Types.Leg_Re
    » c_Ptr);
168 154 function Ghdr_Control_To_Access is new Unchecked_Conversion(Source => Hdr_Pointer, Target => Header_Control_Access);
169 155
170 156 -- Bite History Storage Routine
171 157 package Log_Assert_Pkg is new Bite_Recover_Gpkg(Data_Type => Common_Lgb_Int_Nonresync_Dpkg.Bite_Recover_Rec);
172 158
173 159 -- -----
174 160 -- -- LGBM Utilities
175 161 -- -----
176 162
177 163 -- -----*
178 164 -- Local routines --
179 165 -- -----*
180 166 package Checksum_Utils is
181 167     procedure Checksum_Leg
182 168     (
183 169         Leg_Index : Flight_Pln_Leg_Types.Leg_Index_Type
184 170     );
185 171     procedure Checksum_Header
186 172     (
187 173         Route : Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type
188 174     );
189 175     procedure Init_All_Checksums;
190 176 end;
191 177 package body Checksum_Utils is
192 178     procedure Checksum_Leg
193 179     (
194 180         Leg_Index : Flight_Pln_Leg_Types.Leg_Index_Type
195 181     ) is
196 182     begin
197 183         null;
198 184     end Checksum_Leg;
199 185
200 186     procedure Checksum_Header
201 187     (
202 188         Route : Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type
203 189     ) is
204 190     begin
205 191         null;
206 192     end Checksum_Header;
207 193     procedure Init_All_Checksums is

```

-- checksum a single leg

-- checksum a single header

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

208 194     begin
209 195         null;
210 196     end Init_All_Checksums;
211 197 end Checksum_Utils;
212 198
213 199
214 200 procedure Call_Bite_Recover
215 201 (
216 202     Event_Code : Lgb_Error_Code_Dpkg.Error_Subcode_Type
217 203 ) is
218 204     Fp_Bite_Code : constant Portable_Types_Pkg.Unsigned_8 := 22;
219 205 begin
220 206     Log_Assert_Pkg.Recover(Common_Lgb_Int_Nonresync_Dpkg.Bite_Data,
221 207                         (Code => Fp_Bite_Code, Subcode => Portable_Types_Pkg.Byte_Type (Event_Code)),
222 208                         Bite_Fault_Recovery_Tpkg.Record_And_Raise_Exception);
223 209 end Call_Bite_Recover;
224 210
225 211 procedure Verify_Route
226 212 (
227 213     Route : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type
228 214 ) is
229 215 begin
230 216     if not ( Route in 1..Last_Flight_Plan ) then
231 217         Common_Lgb_Int_Nonresync_Dpkg.Bite_Data.Route_Id_Valid := True;
232 218         Common_Lgb_Int_Nonresync_Dpkg.Bite_Data.Route_Id := Route;
233 219
234 220         Call_Bite_Recover( Lgb_Error_Code_Dpkg.Gb_Route_Out_Of_Range );
235 221     end if;
236 222 end Verify_Route;
237 223 pragma Inline( Verify_Route );
238 224
239 225 procedure Set_Bite_Process_Data
240 226 (
241 227     Process : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type
242 228 ) is
243 229 begin
244 230     Common_Lgb_Int_Nonresync_Dpkg.Bite_Data := Init_Bite_Data;
245 231     Common_Lgb_Int_Nonresync_Dpkg.Bite_Data.Process_Id_Valid := True;
246 232     Common_Lgb_Int_Nonresync_Dpkg.Bite_Data.Process_Id := Process;
247 233 end Set_Bite_Process_Data;
248 234
249 235 function Writer
250 236 (
251 237     Route : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

252 238 Process : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type
253 239 ) return Boolean is
254 240 begin
255 241 Verify_Route( Route );
256 242 return Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Writer( Route ) = Process;
257 243 end Writer;
258 244
259 245
260 246 function Reader
261 247 (
262 248 Route : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
263 249 Process : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type
264 250 ) return Boolean is
265 251 Have_Access_To_Any_Fpln : Boolean;
266 252 begin
267 253 Verify_Route( Route );
268 254 if Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Reader_Array( Process, Route ) or
269 255 ( Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Writer( Route ) = Process ) then
270 256 return True;
271 257 else
272 258 Have_Access_To_Any_Fpln := False;
273 259 for Rte in 1..Last_Flight_Plan loop
274 260 if Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Reader_Array( Process, Rte ) or
275 261 ( Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Writer( Rte ) = Process ) then
276 262 Have_Access_To_Any_Fpln := True;
277 263 exit;
278 264 end if;
279 265 end loop;
280 266 return Have_Access_To_Any_Fpln;
281 267 end if;
282 268 end Reader;
283 269
284 270 procedure Verify_Leg
285 271 (
286 272 Leg : in Flight_Pln_Leg_Types.Leg_Index_Type
287 273 ) is
288 274 begin
289 275 if not ( Leg in 1..Max_Legs ) then
290 276 Common_Lgb_Int_Nonresync_Dpkg.Bite_Data.Leg_Index_Valid := True;
291 277 Common_Lgb_Int_Nonresync_Dpkg.Bite_Data.Leg_Index := Leg;
292 278
293 279 Call_Bite_Recover( Lgb_Error_Code_Dpkg.Gb_Leg_Out_Of_Range );
294 280 end if;
295 281 end Verify_Leg;

```



File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

296      282      pragma Inline( Verify_Leg );
297      283
298      284      procedure Leg_To_Route_Leg
299      285          (
300      286              Leg_Index : in Flight_Pln_Leg_Types.Leg_Index_Type;
301      287              Route      : out Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
302      288              Out_Index  : out Flight_Pln_Leg_Types.Leg_Index_Type
303      289          ) is
304      290
305      291              Numeric_Route : Flight_Pln_Leg_Types.Leg_Index_Type;
306      292              I              : Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
307      293
308      294      begin
309      295          Verify_Leg( Leg_Index );
310      296          I := 1;
311      297          loop
312      298              exit when ( Fp_Range( I ).Min <= Leg_Index ) and ( Fp_Range( I ).Max >= Leg_Index );
313      299              I := I + 1;
314      300          end loop;
315      301          Route := I;
316      302          Out_Index := Flight_Pln_Leg_Types.Leg_Index_Type( ( Leg_Index - Fp_Range( I ).Min ) + 1 );
317      303      end Leg_To_Route_Leg;
318      304
319      305      procedure Putnextprev
320      306          (
321      307              Process      : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
322      308              Leg_Index    : in Flight_Pln_Leg_Types.Leg_Index_Type;
323      309              Leg_After    : in Flight_Pln_Leg_Types.Leg_Index_Type;
324      310              Leg_Before   : in Flight_Pln_Leg_Types.Leg_Index_Type
325      311          ) is
326      312          --!
327      313          --|@DESCRIPTION: This routine controls the linking process.  Since the NEXTFPN
328      314          --|                  and PREVFPN fields are not writable directly, flight planning
329      315          --|                  must call this routine to update these fields.
330      316          --!
331      317      begin
332      318          null;
333      319      end PUTNEXTPREV;
334      320      procedure Requestleg
335      321          (
336      322              Process_Id    : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
337      323              Route         : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
338      324              Leg_Before   : in Flight_Pln_Leg_Types.Leg_Index_Type;
339      325              Leg_After    : in Flight_Pln_Leg_Types.Leg_Index_Type;

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

340 326 Leg_Index : out Flight_Pln_Leg_Types.Leg_Index_Type;
341 327 Lateral_Leg : out Flight_Pln_Leg_Types.Leg_Rec
342 328 ) is
343 329 --!
344 330 --|@DESCRIPTION: This routine controls the used legs list and reserves legs to
345 331 --| be built and put into the flight plan.
346 332 --| If the CheckAvail is true, then this routine will check the
347 333 --| leg availability to be sure that there is one available.
348 334 --| Otherwise it won't.
349 335 --!
350 336 begin
351 337 null;
352 338 end Requestleg;
353 339 procedure Delete_Fpln
354 340 (
355 341 Route : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type
356 342 ) is
357 343 begin
358 344 null;
359 345 end Delete_Fpln;
360 346
361 347 function Delta_Range
362 348 (
363 349 Source_Route, Overwrite_Route : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type
364 350 ) return Portable_Types_Pkg.Integer_32 is
365 351 begin
366 352 return Portable_Types_Pkg.Integer_32( Fp_Range( Overwrite_Route ).Min - Fp_Range( Source_Route ).Min );
367 353 end Delta_Range;
368 354
369 355 -- -----*
370 356 -- Visible routines --
371 357 -- -----*
372 358 function Leg_Valid
373 359 (
374 360 Leg : in Flight_Pln_Leg_Types.Leg_Index_Type
375 361 ) return Boolean is
376 362 --!
377 363 --|@DESCRIPTION: This function determines whether the input Leg is within the range of valid legs.
378 364 --|@SPECIAL_CONSIDERATIONS:
379 365 --| This is an overloaded function
380 366 --!
381 367 begin
382 368 return ( Leg in 1..Max_Legs );
383 369 end Leg_Valid;

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

384 370
385 371
386 372 function Lastleg
387 373 (
388 374   Route : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type
389 375 ) return Flight_Pln_Leg_Types.Leg_Index_Type is
390 376   --!
391 377   --|@DESCRIPTION: This routine finds the last leg of the route, and returns its index.
392 378   --|               This is a field in the flight plan header.
393 379   --|@SPECIAL_CONSIDERATIONS:
394 380   --|               This routine will be used when Build Leg Avail is
395 381   --|               called to ensure the header is still correct.
396 382   --|               Otherwise this routine should be called during
397 383   --|               initialization and after a flight plan size change.
398 384   --!
399 385
400 386   Leg_Index : Flight_Pln_Leg_Types.Leg_Index_Type := Flt_Plan_Hdr( Route ).Critidx( Flight_Pln_Hdr_Types.Firstleg );
401 387
402 388 begin
403 389   Legs_List.Legs_In_Use( Fp_Range( Route ).Min..Fp_Range( Route ).Max ) := (others => False);
404 390   Legs_List.Total_Used_Legs( Route ) := 0;
405 391   if Leg_Index = 0 then
406 392     return 0;
407 393   else
408 394     loop
409 395       Legs_List.Legs_In_Use( Leg_Index ) := True;
410 396       Legs_List.Total_Used_Legs( Route ) := Legs_List.Total_Used_Legs( Route ) + 1;
411 397       exit when Flight_Plan( Leg_Index ).Nextfpn = 0;
412 398       Leg_Index := Flight_Plan( Leg_Index ).Nextfpn;
413 399     end loop;
414 400     return Leg_Index;
415 401   end if;
416 402 end Lastleg;
417 403
418 404 procedure Get_Lgb_Ctrl_Data
419 405 (
420 406   Lgb_Ctrl_Data : out Header_Control_Rec
421 407 ) is
422 408   --!
423 409   --|@DESCRIPTION: This routine retrieves the lateral guidance buffer's control data record.
424 410   --|@SPECIAL_CONSIDERATIONS:
425 411   --|   N/A
426 412   --!
427 413 begin

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

428 414     Lgb_Ctrl_Data := Header_Control;
429 415 end Get_Lgb_Ctrl_Data;
430 416
431 417 procedure Put_Lgb_Ctrl_Data
432 418 (
433 419     Lgb_Ctrl_Data : in Header_Control_Rec
434 420 ) is
435 421     --!
436 422     --|@DESCRIPTION:  This routine stores the lateral guidance buffer's control data record.
437 423     --|@SPECIAL_CONSIDERATIONS:
438 424     --|    N/A
439 425     --!
440 426 begin
441 427     Header_Control := Lgb_Ctrl_Data;
442 428 end Put_Lgb_Ctrl_Data;
443 429
444 430 function Max_Lgb_Legs
445 431 (
446 432     Route : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type
447 433 ) return Portable_Types_Pkg.Integer_32 is
448 434     --!
449 435     --|@DESCRIPTION:  This routine returns the maximum number of legs that are allowed in the given route.
450 436     --|@SPECIAL_CONSIDERATIONS:
451 437     --|    N/A
452 438     --!
453 439 begin
454 440     return Fp_Range( Route ).Size;
455 441 end Max_Lgb_Legs;
456 442
457 443 procedure Check_Read_Write_Excwrite
458 444 (
459 445     Process      : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
460 446     Route        : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
461 447     Have_Read    : out Boolean;
462 448     Have_Write   : out Boolean;
463 449     Have_Excwrite : out Boolean
464 450 ) is
465 451     --!
466 452     --|@DESCRIPTION:  This routine sets the booleans to indicate if the process has access to the given route.
467 453     --|@SPECIAL_CONSIDERATIONS:
468 454     --|    N/A
469 455     --!
470 456 begin
471 457     Set_Bite_Process_Data( Process );

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

472 458
473 459     Have_Read := Reader( Route, Process );
474 460     Have_Write := Writer( Route, Process );
475 461     Have_Excwrite := Lgb_Access_Header( Route ).Operation = Flight_Pln_Hdr_Types.Exclusive_Write;
476 462 end Check_Read_Write_Excwrite;
477 463
478 464 procedure Clear_All_Critidx
479 465 (
480 466     Process : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
481 467     Route    : Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type
482 468 ) is
483 469
484 470     --!
485 471     --|@DESCRIPTION: This routine sets all critical indices in the input route to zero.
486 472     --|@SPECIAL_CONSIDERATIONS:
487 473     --|     Writes 0's into the critical indices
488 474     --!
489 475
490 476     I : Flight_Pln_Hdr_Types.Critidx_Enu;
491 477 begin
492 478     null;
493 479 end Clear_All_Critidx;
494 480
495 481 procedure Update_Legindex
496 482 (
497 483     Old_Legindex : in Flight_Pln_Leg_Types.Leg_Index_Type;
498 484     Desired_Fpln : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
499 485     New_Legindex : out Flight_Pln_Leg_Types.Leg_Index_Type
500 486 ) is
501 487
502 488     --!
503 489     --|@DESCRIPTION: This routine finds the offset of the leg in its flight plan, calculates
504 490     --|     the leg that is at that same offset but in the desired flight plan,
505 491     --|     and returns it in the new leg index parameter.
506 492     --|@SPECIAL_CONSIDERATIONS:
507 493     --|     This routine is used to update pointers when the entire route is copied.
508 494     --|     It depends on the offset from the start of the route to a particular leg
509 495     --|     staying the same when that route is copied (start leg change, but offset
510 496     --|     from start leg not changed). So, if that ever changes, this module will
511 497     --|     have to be rewritten.
512 498     --!
513 499
514 500     I : Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
515 501 begin

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

516 502   New_Legindex := 0;
517 503   if ( Old_Legindex > 0 ) then
518 504       I := 1;
519 505       while ( ( Old_Legindex > Fp_Range( I ).Max ) and ( Fp_Range( I ).Size > 0 ) ) loop
520 506           I := I + 1;
521 507       end loop;
522 508       New_Legindex := Old_Legindex - Fp_Range( I ).Min + Fp_Range( Desired_Fpln ).Min;
523 509   end if;
524 510 end Update_Legindex;
525 511
526 512 procedure Update_Critidx
527 513 (
528 514     Process   : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
529 515     Old_Fpln  : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
530 516     New_Fpln  : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type
531 517 ) is
532 518
533 519   --!
534 520   --|@DESCRIPTION:  On a Flight Plan Copy, the critical indices for the flight plan being copied
535 521   --|                  into need to be updated. This routine updates the critical indices in the
536 522   --|                  header of the old flight plan to point to the new flight plan. It also updates
537 523   --|                  the offset capture/return path start/end point lgb reference leg index in the
538 524   --|                  header.
539 525   --|@SPECIAL_CONSIDERATIONS:
540 526   --|      Implemented as a for loop to run through the critical indices. Leave it to
541 527   --|      the update legindex routine to implement the update of an individual leg.
542 528   --!
543 529
544 530   I : Flight_Pln_Hdr_Types.Critidx_Enu;
545 531 begin
546 532     Set_Bite_Process_Data( Process );
547 533
548 534     Verify_Route( Old_Fpln );
549 535     Verify_Route( New_Fpln );
550 536     if not Writer( New_Fpln, Process ) then
551 537         Common_Lgb_Int_Nonresync_Dpkg.Bite_Data.Route_Id_Valid := True;
552 538         Common_Lgb_Int_Nonresync_Dpkg.Bite_Data.Route_Id := New_Fpln;
553 539
554 540         Call_Bite_Recover( Lgb_Error_Code_Dpkg.Gb_No_Write_Access );
555 541     end if;
556 542     -- I don't need to check if PROCESS has declared themselves the reader
557 543     -- of OLD_FPLN because flight planning is the only process to update
558 544     -- the critical indexes on all airframes so there is no chance of inconsistent data. -JR
559 545     for I in Flight_Pln_Hdr_Types.Critidx_Enu'First..Flight_Pln_Hdr_Types.Critidx_Enu'Last loop

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

560 546      Update_Legindex( Flt_Plan_Hdr( Old_Fpln ).Critidx( I ), New_Fpln, Flt_Plan_Hdr( New_Fpln ).Critidx( I ) );
561 547      end loop;
562 548      -- update capture/return path start/end point lgb reference leg index to point to new fpln
563 549      Update_Legindex( Flt_Plan_Hdr( Old_Fpln ).Lateral_Offset.Capture_Path_Start_Pt.Lgb_Leg_Index, New_Fpln,
564 550          Flt_Plan_Hdr( New_Fpln ).Lateral_Offset.Capture_Path_Start_Pt.Lgb_Leg_Index );
565 551      Update_Legindex( Flt_Plan_Hdr( Old_Fpln ).Lateral_Offset.Capture_Path_End_Pt.Lgb_Leg_Index, New_Fpln,
566 552          Flt_Plan_Hdr( New_Fpln ).Lateral_Offset.Capture_Path_End_Pt.Lgb_Leg_Index );
567 553      Update_Legindex( Flt_Plan_Hdr( Old_Fpln ).Lateral_Offset.Return_Path_Start_Pt.Lgb_Leg_Index, New_Fpln,
568 554          Flt_Plan_Hdr( New_Fpln ).Lateral_Offset.Return_Path_Start_Pt.Lgb_Leg_Index );
569 555      Update_Legindex( Flt_Plan_Hdr( Old_Fpln ).Lateral_Offset.Return_Path_End_Pt.Lgb_Leg_Index, New_Fpln,
570 556          Flt_Plan_Hdr( New_Fpln ).Lateral_Offset.Return_Path_End_Pt.Lgb_Leg_Index );
571 557      end Update_Critidx;
572 558
573 559      procedure Put_Perf_Capture_Return_Path_Record
574 560      (
575 561          Capture_Return_Path_Record_Input      : in Flight_Pln_Hdr_Types.Offset_Capture_Return_Pt_Rec;
576 562          Capture_Return_Path_Record_To_Update : out Flight_Pln_Hdr_Types.Offset_Capture_Return_Pt_Rec
577 563      ) is
578 564
579 565      --!
580 566      --| @DESCRIPTION:   This procedure updates the Perf fields of specified capture/return path record in the
581 567      --|                  specified flight plan header.
582 568      --|
583 569      --|
584 570      --| @SPECIAL_CONSIDERATIONS: Write access should have been checked before calling this routine.
585 571      --|
586 572      --!
587 573
588 574      begin
589 575          Capture_Return_Path_Record_To_Update.Prdtas := Capture_Return_Path_Record_Input.Prdtas;
590 576          Capture_Return_Path_Record_To_Update.Prd_Wind_Mag := Capture_Return_Path_Record_Input.Prd_Wind_Mag;
591 577          Capture_Return_Path_Record_To_Update.Prd_Wind_True_Brg := Capture_Return_Path_Record_Input.Prd_Wind_True_Brg;
592 578          Capture_Return_Path_Record_To_Update.Prddataseq := Capture_Return_Path_Record_Input.Prddataseq;
593 579          Capture_Return_Path_Record_To_Update.Prdalt := Capture_Return_Path_Record_Input.Prdalt;
594 580          Capture_Return_Path_Record_To_Update.Prdgwttofix := Capture_Return_Path_Record_Input.Prdgwttofix;
595 581          Capture_Return_Path_Record_To_Update.Fixdistodest := Capture_Return_Path_Record_Input.Fixdistodest;
596 582          Capture_Return_Path_Record_To_Update.Fixdtdbias := Capture_Return_Path_Record_Input.Fixdtdbias;
597 583          Capture_Return_Path_Record_To_Update.Fltphasefix := Capture_Return_Path_Record_Input.Fltphasefix;
598 584          Capture_Return_Path_Record_To_Update.Prdterm := Capture_Return_Path_Record_Input.Prdterm;
599 585          Capture_Return_Path_Record_To_Update.Firstpass := Capture_Return_Path_Record_Input.Firstpass;
600 586      end Put_Perf_Capture_Return_Path_Record;
601 587
602 588      procedure Getlgbhdr
603 589      (

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

604	590	Process_Id : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
605	591	Rte : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
606	592	Returned_Header : out Flight_Pln_Hdr_Types.Flight_Pln_Hdr_Rec
607	593	) is separate;
608		-----+-----
609		<del>----- @DESCRIPTION: This routine reads from the Lateral Guidance Buffer header.</del>
610		<del>----- -----Note: This routine only returns a single Flight Plan header.</del>
611		-----+-----
	594	
612	595	
613	596	procedure Putlgbhdr
614	597	(
615	598	Process_Id : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
616	599	Rte : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
617	600	Input_Header : in Flight_Pln_Hdr_Types.Flight_Pln_Hdr_Rec
618	601	) is
619		-----+-----
620		<del>----- @DESCRIPTION: This routine will write to a flight plan header</del>
621		<del>----- -----without the control data described in the flight_pln_hdr_types.</del>
622		-----+-----
	602	
623	603	begin
624	604	null;
625	605	end Putlgbhdr;
626	606	procedure Getlgbleg
627	607	(
628	608	Process_Id : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
629	609	Leg_Index : in Flight_Pln_Leg_Types.Leg_Index_Type;
630	610	Lateral_Leg : out Flight_Pln_Leg_Types.Leg_Rec
631	611	) is separate;
632		-----+-----
633		<del>----- @DESCRIPTION: This routine will read from a leg of a flight plan. It will</del>
634		<del>----- -----output a leg record via the parameter list.</del>
635		-----+-----
	612	
636	613	
637	614	procedure Putlgbleg
638	615	(
639	616	Process_Id : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
640	617	Leg_Index : in Flight_Pln_Leg_Types.Leg_Index_Type;
641	618	Lateral_Leg : in Flight_Pln_Leg_Types.Leg_Rec
642	619	) is separate;
643		-----+-----
644		<del>----- @DESCRIPTION: This routine writes an entire lateral leg to the Guidance Buffer.</del>



File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

645		<del>-----+-----</del>	<del>Note: This routine only writes a single Flight Plan header and</del>
646		<del>-----+-----</del>	<del>the control data.</del>
647		<del>-----+-----</del>	
	620		
648	621		
649	622	procedure Initlgb	
650	623	(	
651	624	Last_Lgb_Process	:
652	625	in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;	
653	626	Pending_Flight_Plan	:
654	627	in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;	
655	628	Initial_Active	:
656	629	in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;	
657	630	Initial_Secondary	:
658	631	in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;	
659	632	Plan1_Size, Plan2_Size, Plan3_Size	:
660	633	in Flight_Pln_Leg_Types.Leg_Index_Type;	
661	634	Plan4_Size, Plan5_Size, Plan6_Size, Plan7_Size, Plan8_Size, Plan9_Size, Plan10_Size, Plan11_Size, Plan12_Size	:
662	635	in Flight_Pln_Leg_Types.Leg_Index_Type := 0;	
663	636	Read_Write_Init	:
664	637	in Read_Write_Access_Arr := (others => (others => Lo));	
665	638	Fpln4sema, Fpln5sema, Fpln6sema, Fpln7sema, Fpln8sema, Fpln9sema, Fpln10sema, Fpln11sema, Fpln12sema	:
666	639	Apex_Semaphore_Pkg.Semaphore_Id_Type := Ops_Semaphore_Id_Pkg.Ops_Semaphore_Table( Ops_Semaphore_Id_Pkg.Lgb_Fpl	
		» n1 )	
667	640	) is	
668	641	begin	
669	642	null;	
670	643	end INITLGB;	
671	644		
672	645	procedure Requestlgb	
673	646	(	
674	647	Lgb_Process_Id : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;	
675	648	Lgb_Operation  : in Flight_Pln_Hdr_Types.Type_Of_Access;	
676	649	Route_Id       : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;	
677	650	Granted        : out Boolean	
678	651	) is	
679		<del>-----+-----</del>	
680		<del>-----+-----</del>	<del>@DESCRIPTION: This routine controls the guidance buffer and its data. Users</del>
681		<del>-----+-----</del>	<del>cannot use the buffer without going through this routine. It</del>
682		<del>-----+-----</del>	<del>accepts the users nameand outputs a hidden</del>
683		<del>-----+-----</del>	<del>ID that is used to perform operations in a flight</del>
684		<del>-----+-----</del>	<del>plan. If the Active Access ID does not equal the access ID</del>
685		<del>-----+-----</del>	<del>given to do an operation, an assert is raised. A user must</del>
686		<del>-----+-----</del>	<del>also declare which flight plan is needed and the type of</del>

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

687		<del>operation that is going to be performed (READ, WRITE).</del>
688		<del>An assert will also be raised if the FA user requesting the</del>
689		<del>route is already accessing it.</del>
690		<del>@SPECIAL_CONSIDERATIONS:</del>
691		<del>1) A user must call this routine before attempting to do any</del>
692		<del>thing in the guidance buffer.</del>
693		<del>2) Must request one flight plan at a time, but can gain access</del>
694		<del>to all three.</del>
695		<del>3) This is an overloaded procedure.</del>
696		<del>+</del>
652		
697	653	
698	654	Found_Reader : Boolean;
699	655	Granted1 : Boolean;
700	656	New_Lock_Level : Apex_Partition_Pkg.Lock_Level_Type;
701	657	Status : Apex_Types_Pkg.Status_Code_Type;
702	658	
703	659	function Reader_Exists return Boolean is
704		<del>+</del>
705		<del>@DESCRIPTION: This module returns a flag indicating whether or not a reader(other than the caller) exists.</del>
706		<del>+</del>
707		<del>@SPECIAL_CONSIDERATIONS: N/A</del>
708		<del>+</del>
660		
709	661	
710	662	Local_Found_Reader : Boolean;
711	663	
712	664	Local_Lgb_User : Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
713	665	
714	666	begin
715	667	
716	668	Local_Found_Reader := False;
717	669	Local_Lgb_User := Fmcs_Fp_Guid_Btypes.No_Valid_Caller;
718	670	
719	671	while not Local_Found_Reader and then Local_Lgb_User /= Fmcs_Fp_Guid_Btypes.Last_Access_Id loop
720	672	
721	673	Local_Lgb_User := Local_Lgb_User + 1;
722	674	
723	675	-- set true if we found a reader other than the caller
724	676	Local_Found_Reader :=
725	677	Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Reader_Array( Local_Lgb_User, Route_Id ) and then
726	678	Local_Lgb_User /= Lgb_Process_Id;
727	679	end loop;
728	680	

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

729 681     return Local_Found_Reader;
730 682     end Reader_Exists;
731 683
732 684     begin
733 685
734 686         -- if the requestor is already a reader or a writer then they already have access, so
735 687         -- go ahead and grant access
736 688         Granted1 :=
737 689             ( Lgb_Operation = Flight_Pln_Hdr_Types.Read and then Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Reader_Array(
738 690                 Lgb_Process_Id, Route_Id ) ) or else ( Lgb_Process_Id = Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Writer( Rou
739 691 » te_Id ) );
740 692
741 693         -- if access hasn't already been granted and there is currently no writer, then lets try to get reader access
742 694         if not Granted1 and then
743 695             Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Writer( Route_Id ) = Fmcs_Fp_Guid_Btypes.No_Valid_Caller then
744 696 »
745 697             -- there's a chance we can get access, to lock up the processor and go for it
746 698             Apex_Partition_Pkg.Lock_Preemption( New_Lock_Level, Status );
747 699
748 700             Found_Reader := Reader_Exists;
749 701
750 702             -- we will get access only if the caller is getting read access when there is no writer or the caller
751 703             -- is getting write access when there is no reader and no writer
752 704             if ( Lgb_Operation = Flight_Pln_Hdr_Types.Read and then
753 705                 Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Writer( Route_Id ) = Fmcs_Fp_Guid_Btypes.No_Valid_Caller ) or els
754 706 » e
755 707                 ( Lgb_Operation = Flight_Pln_Hdr_Types.Write and then
756 708                 Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Writer( Route_Id ) = Fmcs_Fp_Guid_Btypes.No_Valid_Caller and then
757 709                 not Found_Reader ) then
758 710                 -- no writers, so we will get access
759 711                 Requestlgb( Lgb_Process_Id, Lgb_Operation, Route_Id );
760 712                 Granted1 := True;
761 713             end if;
762 714
763 715             Apex_Partition_Pkg.Unlock_Preemption( New_Lock_Level, Status );
764 716
765 717             Granted := Granted1;
766 718         end Requestlgb;
767 719
768 720     procedure Requestlgb
769 721     (

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

770	722	Lgb_Process_Id : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
771	723	Lgb_Operation : in Flight_Pln_Hdr_Types.Type_Of_Access;
772	724	Route_Id : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type
773	725	) is
774		<del>-----+-----</del>
775		<del>----- @DESCRIPTION: This routine controls the guidance buffer and its data. Users</del>
776		<del>-----  cannot use the buffer without going through this routine. It</del>
777		<del>-----  accepts the users nameand outputs a hidden</del>
778		<del>-----  ID that is used to perform operations in a flight</del>
779		<del>-----  plan. If the Active Access ID does not equal the access ID-</del>
780		<del>-----  given to do an operation, an assert is raised. A user must</del>
781		<del>-----  also declare which flight plan is needed and the type of-</del>
782		<del>-----  operation that is going to be performed (READ, WRITE).</del>
783		<del>-----  An assert will also be raised if the FA user requesting the</del>
784		<del>-----  route is already accessing it.</del>
785		<del>----- @SPECIAL_CONSIDERATIONS:-</del>
786		<del>-----  1) A user must call this routine before attempting to do any-</del>
787		<del>-----  thing in the guidance buffer.</del>
788		<del>-----  2) Must request one flight plan at a time, but can gain access</del>
789		<del>-----  to all three.</del>
790		<del>-----  3) This is an overloaded procedure.</del>
791		<del>----- -----+-----</del>
	726	
792	727	
793	728	Efis_Req_Timeout : Boolean;
794	729	Granted_Access : Lock_Status_Enu := Lo;
795	730	Hundred_Milisc : constant Portable_Types_Pkg.Integer_32 := 100;
796	731	Lgb_Reader : Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
797	732	Lgb_Writer : Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
798	733	New_Lock_Level : Apex_Partition_Pkg.Lock_Level_Type;
799	734	Reader : Boolean;
800	735	Status : Apex_Types_Pkg.Status_Code_Type;
801	736	Timeout : Apex_Types_Pkg.Timeout_Type := Apex_Types_Pkg.Indefinite_Timeout;
802	737	Writer : Boolean;
803	738	
804	739	procedure Find_User
805	740	(
806	741	Route_Id : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
807	742	Lgb_User : in out Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
808	743	User : in out Boolean;
809	744	User_Access : in Flight_Pln_Hdr_Types.Type_Of_Access
810	745	) is
811		<del>-----+-----</del>
812		<del>----- @DESCRIPTION: This module returns either the first LGB reader that is in the reader array or</del>

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

813		<del>the LGB writer depending on what the caller is looking for.</del>
814		<del>_____</del>
815		<del>@SPECIAL_CONSIDERATIONS: N/A</del>
816		<del>_____</del>
	746	
817	747	
818	748	begin
819	749	
820	750	Lgb_User := Fmcs_Fp_Guid_Btypes.No_Valid_Caller;
821	751	User := False;
822	752	
823	753	-- find the first reader in the reader array
824	754	if User_Access = Flight_Pln_Hdr_Types.Read then
825	755	
826	756	-- loop until we either find the first reader or until we get to the last
827	757	-- valid user
828	758	while not User and then Lgb_User /= Fmcs_Fp_Guid_Btypes.Last_Access_Id loop
829	759	
830	760	Lgb_User := Lgb_User + 1;
831	761	
832	762	-- set true if we found a user
833	763	User :=
834	764	not ( Lgb_User = Lgb_Process_Id or else not Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Reader_Array( Lgb
		» _User,
835	765	Route_Id ) or else Read_Write_Matrix( Lgb_Process_Id, Lgb_User ) /= Common_Lgb.Lo );
836	766	end loop;
837	767	else
838	768	
839	769	Lgb_User := Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Writer( Route_Id );
840	770	User := Lgb_User /= Fmcs_Fp_Guid_Btypes.No_Valid_Caller;
841	771	end if;
842	772	end Find_User;
843	773	
844	774	procedure Gb_Wait
845	775	(
846	776	Lgb_Process_Id : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type
847	777	) is
848	778	
849	779	Timeout : Apex_Types_Pkg.Timeout_Type;
850	780	
851	781	begin
852	782	
853	783	-- if the caller is EFIS then set the wait time accordingly
854	784	if Lgb_Process_Id = Fmcs_Fp_Guid_Btypes.Efis_Fg then

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

855 785
856 786     Timeout := Hundred_Milisec;
857 787     else
858 788
859 789     Timeout := Apex_Types_Pkg.Indefinite_Timeout;
860 790     end if;
861 791
862 792     Apex_Semaphore_Pkg.Wait( Writer_Semaphores( Route_Id ), Timeout, Apex_Types_Pkg.Dont_Replenish, Status );
863 793
864 794     if Lgb_Process_Id = Efis_Fg then
865 795
866 796         Efis_Req_Timeout := Status = Apex_Types_Pkg.Timeout_Expiration;
867 797     end if;
868 798     end Gb_Wait;
869 799
870 800 begin
871 801
872 802     Set_Bite_Process_Data( Lgb_Process_Id );
873 803     Lgb_Reader := Fmcs_Fp_Guid_Btypes.No_Valid_Caller;
874 804     Lgb_Writer := Fmcs_Fp_Guid_Btypes.No_Valid_Caller;
875 805     Reader := False;
876 806     Writer := False;
877 807
878 808     Verify_Route( Route_Id );
879 809
880 810     case Lgb_Operation is
881 811
882 812         when Flight_Pln_Hdr_Types.Read =>
883 813
884 814             if not Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Reader_Array( Lgb_Process_Id, Route_Id ) then
885 815                 Access_Read:
886 816
887 817                 loop
888 818
889 819                     Apex_Partition_Pkg.Lock_Preemption( New_Lock_Level, Status );
890 820                     Writer := Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Writer( Route_Id ) /= Fmcs_Fp_Guid_Btypes.No_Valid_Ca
891 821 » ller;
892 822
893 823                     -- Check to see the relationship between the requester and the writer
894 824                     if Writer then
895 825                         Lgb_Writer := Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Writer( Route_Id );
896 826                         Granted_Access := Read_Write_Matrix( Lgb_Writer, Lgb_Process_Id );
897 827

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

898 828         if Lgb_Writer /= Lgb_Process_Id and then Granted_Access = Common_Lgb.Lo then
899 829
900 830             -- suspend the process using semaphores.
901 831             Apex_Partition_Pkg.Unlock_Preemption( New_Lock_Level, Status );
902 832             Gb_Wait( Lgb_Process_Id );
903 833         end if;
904 834     end if;
905 835
906 836         exit when not Writer or else Granted_Access /= Lo or else Lgb_Writer = Lgb_Process_Id;
907 837     end loop Access_Read;
908 838
909 839         Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Reader_Array( Lgb_Process_Id, Route_Id ) := True;
910 840         Apex_Partition_Pkg.Unlock_Preemption( New_Lock_Level, Status );
911 841     end if;
912 842
913 843     when Flight_Pln_Hdr_Types.Write | Flight_Pln_Hdr_Types.Exclusive_Write =>
914 844
915 845         if Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Writer( Route_Id ) /= Lgb_Process_Id then
916 846             Access_Write:
917 847
918 848             loop
919 849
920 850                 Apex_Partition_Pkg.Lock_Preemption( New_Lock_Level, Status );
921 851
922 852                 -- is there already a writer
923 853                 Find_User( Route_Id, Lgb_Writer, Writer, Flight_Pln_Hdr_Types.Write );
924 854                 if not Writer then
925 855
926 856                     -- is there already a reader
927 857                     Find_User( Route_Id, Lgb_Reader, Reader, Flight_Pln_Hdr_Types.Read );
928 858
929 859                     if Reader then
930 860
931 861                         Apex_Partition_Pkg.Unlock_Preemption( New_Lock_Level, Status );
932 862                         Gb_Wait( Lgb_Process_Id );
933 863                     end if;
934 864                 else
935 865
936 866                     -- there is a writer already
937 867                     Apex_Partition_Pkg.Unlock_Preemption( New_Lock_Level, Status );
938 868                     Gb_Wait( Lgb_Process_Id );
939 869                 end if;
940 870
941 871         exit when not Writer and not Reader;

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

942	872	end loop Access_Write;
943	873	Set_Modification_Started( Route_Id);
944	874	Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Writer( Route_Id ) := Lgb_Process_Id;
945	875	Apex_Partition_Pkg.Unlock_Preemption( New_Lock_Level, Status );
946	876	end if;
947	877	end case;
948	878	end Requestlgb;
949	879	
950	880	procedure Releaselgb
951	881	(
952	882	Process_Id : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type
953	883	) is
954		<del>-----+-----</del>
955		<del>----- -----@DESCRIPTION: To relinquish privilege to Guidance Buffer and allow other users</del>
956		<del>----- -----this privilege. This version unlocks all flight plans.</del>
957		<del>----- -----@SPECIAL_CONSIDERATIONS:</del>
958		<del>----- -----1) No Processing is performed if this is called</del>
959		<del>----- -----without an active access ID.</del>
960		<del>----- -----2) This is an overloaded procedure.</del>
961		<del>-----+-----</del>
	884	
962	885	I : Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
963	886	begin
964	887	for I in 1..Last_Flight_Plan loop
965	888	Releaselgb( Process_Id, I );
966	889	end loop;
967	890	end Releaselgb;
968	891	
969	892	procedure Releaselgb
970	893	(
971	894	Process_Id : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
972	895	Rte : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type
973	896	) is
974	897	»
975		<del>-----+-----</del>
976		<del>----- -----@DESCRIPTION: To relinquish privilege to Guidance Buffer and allow other users</del>
977		<del>----- -----this privilege. This version unlocks both the route and its</del>
978		<del>----- -----header for the given flight plan.</del>
979		<del>----- -----@SPECIAL_CONSIDERATIONS:</del>
980		<del>----- -----1) No Processing is performed if this is called</del>
981		<del>----- -----without an active access ID.</del>
982		<del>----- -----2) This is an overloaded procedure.</del>
983		<del>-----+-----</del>



File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

	898	
984	899	
985	900	Current_Number, Max_Number : Apex_Types_Pkg.Semaphore_Value_Type;
986	901	Processes_Waiting : Portable_Types_Pkg.Natural_32;
987	902	Status : Apex_Types_Pkg.Status_Code_Type;
988	903	I : Integer_32;
989	904	Call_Signal : Boolean := False;
990	905	
991	906	begin
992	907	if Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Reader_Array( Process_Id, Rte ) then
993	908	Call_Signal := True;
994	909	Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Reader_Array( Process_Id, Rte ) := False;
995	910	end if;
996	911	if Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Writer( Rte ) = Process_Id then
997	912	Call_Signal := True;
998	913	Common_Lgb_Int_Nonresync_Dpkg.Access_Track.Writer( Rte ) := Fmcs_Fp_Guid_Btypes.No_Valid_Caller;
999	914	Set_Modification_Complete(Rte);
1000	915	
1001	916	end if;
1002	917	if Call_Signal then
1003	918	Apex_Semaphore_Pkg.Signal( Writer_Semaphores( Rte ), Status );
1004	919	end if;
1005	920	end ReleaseLgb;
1006	921	
1007	922	-- -----*
1008	923	-- Buffer maintenance Routines
1009	924	-- -----*
1010	925	
1011	926	-- -----*
1012	927	-- -- Leg Operations
1013	928	-- -----*
1014	929	function Countunusedlegs
1015	930	(
1016	931	Route : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type
1017	932	) return Portable_Types_Pkg.Integer_32 is
		»
1018		<del>-----+ @DESCRIPTION: This routine returns the number leg records currently not in the flight plan. @SPECIAL_CONSIDERATIONS: N/A -----+</del>
	933	
1024	934	begin

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

1025	935	Common_Lgb_Int_Nonresync_Dpkg.Bite_Data := Init_Bite_Data;
1026	936	Verify_Route( Route );
1027	937	return Max_Lgb_Legs( Route ) - Legs_List.Total_Used_Legs( Route );
1028	938	end Countunusedlegs;
1029	939	
1030	940	
1031	941	function Leg_Index_In_Use
1032	942	(
1033	943	Leg_Index : in Flight_Pln_Leg_Types.Leg_Index_Type
1034	944	) return Boolean is
1035	945	
1036		<del>-----+  @DESCRIPTION: This routine checks the inuse array to see if the leg is being used.  -----+  @SPECIAL_CONSIDERATIONS:  -----+   N/A  -----+  -----+</del>
	946	
1041	947	begin
1042	948	return Legs_List.Legs_In_Use( Leg_Index );
1043	949	end Leg_Index_In_Use;
1044	950	
1045	951	procedure Requestlegbefore
1046	952	(
1047	953	Process_Id : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
1048	954	Route : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
1049	955	Leg_After : in Flight_Pln_Leg_Types.Leg_Index_Type;
1050	956	Leg_Index : out Flight_Pln_Leg_Types.Leg_Index_Type;
1051	957	Lateral_Leg : out Flight_Pln_Leg_Types.Leg_Rec
1052	958	) is
1053		<del>-----+  @DESCRIPTION: This routine retrieves and inserts a new leg into the flight before the  -----+   Leg After that is passed in.  -----+  @SPECIAL_CONSIDERATIONS:  -----+   N/A  -----+  -----+</del>
	959	
1059	960	begin
1060	961	if Leg_After /= 0 then
1061	962	Requestleg( Process_Id, Route, Flight_Plan( Leg_After ).Prevfpn, Leg_After, Leg_Index, Lateral_Leg );
1062	963	else
1063	964	Requestleg( Process_Id, Route, Flt_Plan_Hdr( Route ).Critidx( Flight_Pln_Hdr_Types.Lastleg ), Leg_After, Leg_Ind
		» ex,
1064	965	Lateral_Leg );
1065	966	end if;

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

1066	967	end Requestlegbefore;
1067	968	
1068	969	procedure Requestlegafter
1069	970	(
1070	971	Process_Id : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
1071	972	Route : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
1072	973	Leg_Before : in Flight_Pln_Leg_Types.Leg_Index_Type;
1073	974	Leg_Index : out Flight_Pln_Leg_Types.Leg_Index_Type;
1074	975	Lateral_Leg : out Flight_Pln_Leg_Types.Leg_Rec
1075	976	) is
1076		<del>-----+-----</del>
1077		<del>--- @DESCRIPTION: This routine retrieves and inserts a new leg into the flight after the</del>
1078		<del>---  Leg Before that is passed in.</del>
1079		<del>--- @SPECIAL_CONSIDERATIONS:-</del>
1080		<del>---  N/A</del>
1081		<del>-----+-----</del>
	977	
1082	978	begin
1083	979	if Leg_Before /= 0 then
1084	980	Requestleg( Process_Id, Route, Leg_Before, Flight_Plan( Leg_Before ).Nextfpn, Leg_Index, Lateral_Leg );
1085	981	else
1086	982	Requestleg( Process_Id, Route, Leg_Before, Flt_Plan_Hdr( Route ).Critidx( Flight_Pln_Hdr_Types.Firstleg ), Leg_I
		» ndex,
1087	983	Lateral_Leg );
1088	984	end if;
1089	985	end Requestlegafter;
1090	986	
1091	987	procedure Deleteleg
1092	988	(
1093	989	Process : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
1094	990	Leg_Index : in Flight_Pln_Leg_Types.Leg_Index_Type
1095	991	) is
1096		<del>-----+-----</del>
1097		<del>--- @DESCRIPTION: This routine controls the unlinking of legs, it re-points all</del>
1098		<del>---  the pointers in the Next and Previous legs, and zeroes out the leg.</del>
1099		<del>---  It will also update the used legs list.</del>
1100		<del>-----+-----</del>
	992	
1101	993	begin
1102	994	null;
1103	995	end Deleteleg;
1104	996	-- -----*
1105	997	-- -- Route Operations
1106	998	-- -----*

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

1107 999
1108 1000 procedure Rtedelete
1109 1001 (
1110 1002 Process : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
1111 1003 Route : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type
1112 1004 ) is
1113 1005 --|@DESCRIPTION: This routine clears out the Total Legs Used
1114 1006 --| and Legs In Use. It will also update the
1115 1007 --| control header if necessary.
1116 1008 begin
1117 1009 null;
1118 1010 end Rtedelete;
1119 1011 procedure Rtecopy
1120 1012 (
1121 1013 Process : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
1122 1014 Source_Route, Overwrite_Route : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type
1123 1015 ) is
1124 1016 --| DESCRIPTION: This routine will copy the source flight plan header to the
1125 1017 --| destination already locked. It will copy the source flight
1126 1018 --| plan legs to the destination flight plan legs.
1127 1019 begin
1128 1020 null;
1129 1021 end Rtecopy;
1130 1022
1131 1023 -- -----*
1132 1024 -- Read Custom Routines (for legs)
1133 1025 -- -----*
1134 1026
1135 1027 function Pathterm
1136 1028 (
1137 1029 Process : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
1138 1030 Leg_Index : in Flight_Pln_Leg_Types.Leg_Index_Type
1139 1031 ) return Lateral_Path_Type_Tpkg.Pathtype is
1140 1032
1141 1033 -----|
1142 1034 -----|@DESCRIPTION: This routine reads the Pathterm field, and returns it
1143 1035 -----| in the specified flight plan leg
1144 1036 -----|@SPECIAL_CONSIDERATIONS:
1145 1037 -----| N/A
1146 1038 -----|
1147 1039
1148 1040 begin
1149 1041 Set_Bite_Process_Data( Process );
1150 1042
1151 1043 Verify_Leg( Leg_Index );

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

1150	1037	return Flight_Plan( Leg_Index ).Pathterm;
1151	1038	end Pathterm;
1152	1039	
1153	1040	
1154	1041	-----
1155	1042	-- Quick Access Routines
1156	1043	-----
1157	1044	-- These routines do not require access to a flight plan. A user may want to
1158	1045	-- get access to be sure they are receiving the most up to date data.
1159	1046	
1160	1047	
1161	1048	
1162	1049	function Actnavptr
1163	1050	(
1164	1051	Process : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
1165	1052	Active_Route : in Boolean
1166	1053	) return Flight_Pln_Leg_Types.Leg_Index_Type is
1167		-----+
1168		<del>--- @DESCRIPTION: This routine will return the Active Nav Pointer field in the</del>
1169		<del>specified flight plan header. If the Active Route flag is true,</del>
1170		<del>then it returns the active flight plan's pointer, if the flag</del>
1171		<del>is false it returns the provisional flight plan's pointer.</del>
1172		<del>--- @SPECIAL_CONSIDERATIONS:</del>
1173		<del>--- N/A</del>
1174		-----+
	1054	
1175	1055	begin
1176	1056	if Active_Route then
1177	1057	return Header_Control_Ptr.Act_Legptr.Actnavptr;
1178	1058	else
1179	1059	return Header_Control_Ptr.Prov_Legptr.Actnavptr;
1180	1060	end if;
1181	1061	end Actnavptr;
1182	1062	
1183	1063	
1184	1064	function Actfpln return Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type is
1185		-----+
1186		<del>--- @DESCRIPTION: This routine will return the Active Flight Plan field of the header.</del>
1187		<del>--- The calling routine does not need access to a flight plan to</del>
1188		<del>read this.</del>
1189		<del>--- @SPECIAL_CONSIDERATIONS:</del>
1190		<del>--- N/A</del>
1191		-----+
	1065	

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

1192	1066	begin
1193	1067	return Header_Control_Ptr.Actfpln;
1194	1068	end Actfpln;
1195	1069	
1196	1070	
1197	1071	function Fplnptr
1198	1072	(
1199	1073	Process : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
1200	1074	Route : Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type
1201	1075	) return Flight_Pln_Leg_Types.Leg_Index_Type is
1202		<del>-----+-----</del>
1203		<del>    --@DESCRIPTION: This routine returns the Common.Critidx(Firstleg) field of the header</del>
1204		<del>    control record without requiring a Request LGB call. The calling</del>
1205		<del>    routine does not need access to a flight plan to read this.</del>
1206		<del>    --@SPECIAL_CONSIDERATIONS:-</del>
1207		<del>    N/A</del>
1208		<del>-----+-----</del>
	1076	
1209	1077	begin
1210	1078	Common_Lgb_Int_Nonresync_Dpkg.Bite_Data := Init_Bite_Data;
1211	1079	Verify_Route( Route );
1212	1080	return Flt_Plan_Hdr( Route ).Critidx( Flight_Pln_Hdr_Types.Firstleg );
1213	1081	end Fplnptr;
1214	1082	
1215	1083	
1216	1084	-- Set_Modification_Started set the state monitor to indicate that fpln modification has started.
1217	1085	procedure Set_Modification_Started
1218	1086	(
1219	1087	Route_Id : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type
1220	1088	) is
1221	1089	
1222	1090	begin
1223	1091	--stubbed body
1224	1092	null;
1225	1093	end Set_Modification_Started;
1226	1094	
1227	1095	-- Set_Modification_Complete set the state monitor to indicate that fpln modification is completed.
1228	1096	procedure Set_Modification_Complete
1229	1097	(
1230	1098	Rte : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type
1231	1099	) is
1232	1100	begin
1233	1101	--stubbed body
1234	1102	null;

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

1235 1103 end Set_Modification_Complete;
1236 1104 -- Get_State_Monitor outputs the state monitor data for the flight plan
1237 1105 -- corresponding to the input fpln.
1238 1106 procedure Get_State_Monitor
1239 1107 (
1240 1108     Route_Id : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
1241 1109     Change_In_Progress : out Boolean;
1242 1110     Change_Counter : out Portable_Types_Pkg.Unsigned_32
1243 1111 ) is
1244 1112 begin
1245 1113     --stubbed
1246 1114     null;
1247 1115 end Get_State_Monitor;
1248 1116 -- -----*
1249 1117 -- -- Search Routines --
1250 1118 -- -----*
1251 1119 function Fp_Search_Waypoint
1252 1120 (
1253 1121     Identifier      : in Io_Interface_Tpkg.Fix_Ident_Type;
1254 1122     Process         : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
1255 1123     Identifier_2     : in Io_Interface_Tpkg.Fix_Ident_Type := (others => ' ');
1256 1124     Bearing         : in Standard_Angle_Pkg.Saf_32 := 0.0;
1257 1125     Distance        : in Portable_Types_Pkg.Float_32 := 0.0;
1258 1126     Find_Non_Pbd    : in Boolean := False;
1259 1127     Find_Pbd        : in Boolean := False;
1260 1128     Find_Id_2       : in Boolean := False;
1261 1129     First_Only      : in Boolean := True;
1262 1130     Route           : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type := 1;
1263 1131     Use_Active_Actnavptr : in Boolean := False;
1264 1132     Use_Prov_Actnavptr : in Boolean := False;
1265 1133     Starting_Leg     : in Flight_Pln_Leg_Types.Leg_Index_Type := 0;
1266 1134     True_North       : in Boolean := True
1267 1135 ) return Matched_List_Type is

```

```

1268 1135 -----|
1269 1135 |@DESCRIPTION: This function replaces the old FPSRCHWPT module, placed here-
1270 1135 |and modified to improve performance.
1271 1135 -----|
1272 1135 |This function shall cycle through the specified Route
1273 1135 |searching for appropriate legs matching the specified-
1274 1135 |identifiers. A list of matching legs shall be returned with
1275 1135 |some associated Lateral Guidance Buffer Data.
1276 1135 -----|
1277 1135 |The following requirements shall be used for matching:
1278 1135 |ONLY Legs defined by a fix shall be tested

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

1279		<del>Latitude and Longitude Reporting Points shall NOT be tested</del>
1280		<del>If non PBDs are being searched, then Legs whose</del>
1281		<del>Fix Ident = Identifier, shall be a match</del>
1282		<del>If a second Identifier is being searched, then Legs whose</del>
1283		<del>Fix Ident = Identifier_2, shall also be a match</del>
1284		<del>If PBDs are being searched, then PBD Legs, whose</del>
1285		<del>PBD Parent = Identifier, and Fix Bearing = Bearing shall also</del>
1286		<del>be a match</del>
1287		<del>Return all matching legs unless ONE is specified.</del>
1288		<del>If True North is not being used, then use Fix Bearing Magnetic in</del>
1289		<del>place of Fix Bearing</del>
1290		<del>!</del>
1291		<del>TODO Haraway: This function will be reimplemented for A2.</del>
	1136	
1292	1137	
1293	1138	Matched_List : List_Type (1 .. 200) := (others => 0);
1294	1139	Match_Count : Flight_Pln_Leg_Types.Leg_Index_Type := 0;
1295	1140	
1296	1141	begin
1297	1142	
1298	1143	return (Count => Match_Count, List => Matched_List (1 .. Match_Count));
1299	1144	
1300	1145	end Fp_Search_Waypoint;
1301	1146	
1302	1147	procedure Fp_Search_Lat_Lon
1303	1148	(
1304	1149	Process        : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
1305	1150	Route         : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
1306	1151	Position      : in out Base_Domain_Services_Tpkg.Lat_Lon_32_Type;
1307	1152	Fixident      : out Io_Interface_Tpkg.Fix_Ident_Type;
1308	1153	Fixindex      : out Flight_Pln_Leg_Types.Leg_Index_Type;
1309	1154	Found         : in out Boolean;
1310	1155	Starting_Leg  : in Flight_Pln_Leg_Types.Leg_Index_Type := 0
1311	1156	) is
1312		<del>!</del>
1313		<del>@DESCRIPTION: This function replaces the old FPSRCHLATION module, placed</del>
1314		<del>here to improve performance.</del>
1315		<del>!</del>
1316		<del>This function shall cycle through the specified Route</del>
1317		<del>searching for appropriate legs matching the specified</del>
1318		<del>position. A fix identifier and position shall be returned</del>
1319		<del>for the LGB leg that matches.</del>
1320		<del>!</del>
1321		<del>The following requirements shall be used for matching:</del>



File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

1322		<del>ONLY Legs defined by a fix shall be tested</del>
1323		<del>ONLY Latitude/Longitude position Legs shall be tested</del>
1324		<del>(Fix Type Valid = False)</del>
1325		<del>Legs, whose To Lat Lon = Position, shall be a match</del>
1326		<del>+</del>
1327		<del>TODO Haraway: This function will be reimplemented for A2.</del>
1157		
1328	1158	
1329	1159	begin
1330	1160	Fixindex := 0;
1331	1161	Fixident := (others => ' ');
1332	1162	end Fp_Search_Lat_Lon;
1333	1163	
1334	1164	function Get_Capture_Return_Path_Record
1335	1165	(
1336	1166	Process                : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
1337	1167	Route                 : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
1338	1168	Lateral_Offset_Point : in Lateral_Offset_Segment_Type_Tpkg.Offset_Point_Subtype
1339	1169	) return Flight_Pln_Hdr_Types.Offset_Capture_Return_Pt_Rec is
1340	1170	
1341		<del>+</del>
1342		<del>@DESCRIPTION: This function reads the specified capture/return path record from the specified flight plan header.</del>
1343		<del>»</del>
1344		<del>+</del>
1345		<del>@SPECIAL_CONSIDERATIONS: N/A</del>
1346		<del>+</del>
1347		<del>+</del>
1348	1171	
1349	1172	begin
1350	1173	
1351	1174	-- TODO Haraway: Enable and recode after A0
1352	1175	
1353	1176	-- Set_Bite_Process_Data( Process );
1354	1177	-- if not Reader( Route, Process ) then
1355	1178	--     Common_Lgb_Int_Nonresync_Dpkg.Bite_Data.Route_Id_Valid := True;
1356	1179	--     Common_Lgb_Int_Nonresync_Dpkg.Bite_Data.Route_Id := Route;
1357	1180	--     Call_Bite_Recover( Lgb_Error_Code_Dpkg.Gb_No_Read_Access );
1358	1181	-- end if;
1359	1182	
1360	1183	-- if Lateral_Offset_Point = Lateral_Offset_Segment_Type_Tpkg.Capture_Path_Start then
1361	1184	--     return Flt_Plan_Hdr( Route ).Lateral_Offset.Capture_Path_Start_Pt;
1362	1185	-- elsif Lateral_Offset_Point = Lateral_Offset_Segment_Type_Tpkg.Capture_Path_End then
1363	1186	--     return Flt_Plan_Hdr( Route ).Lateral_Offset.Capture_Path_End_Pt;

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

1364 1187 --      elsif Lateral_Offset_Point = Return_Path_Start then
1365 1188 --          return Flt_Plan_Hdr( Route ).Lateral_Offset.Return_Path_Start_Pt;
1366 1189 --      else
1367 1190 --          return Flight_Plan_Hdr_Types..Lateral_Offset.Return_Path_End_Pt;
1368 1191 --          return ( Capture_Path_Start_Pt => ( Fromlatlon => ( Lat => 0.0, Lon => 0.0 ),
1369 1192 --              Tolatlon => ( Lat => 0.0, Lon => 0.0 ), True_Inbound_Course => 0.0, True_Outbound_Course => 0.0,
1370 1193 --              Inbndlegdist => 0.0, Outbndlegdist => 0.0, Prdtas => 0.0, Prd_Wind_Mag => 0.0, Prd_Wind_True_Brg => 0
1371 1194 --              » .0,
1372 1195 --              Prddataseq => 0, Prdalt => 0.0, Prdgwtttofix => 0.0, Fixdistodest => 0.0, Fixdtdbias => 0.0, Lgb_Leg_I
1373 1196 --              » ndex => 0,
1374 1197 --              Fltphasefix=>Base_Domain_Services_Tpkg.Flight_Phase_Type'First, Prdterm=>False,Firstpass=>False,Spare_1=>0,Spare_2
1375 1198 --              » => 0 )
1376 1199 --          return ( ( Fromlatlon => ( Lat => 0.0, Lon => 0.0 ),
1377 1200 --              Tolatlon => ( Lat => 0.0, Lon => 0.0 ), True_Inbound_Course => 0.0, True_Outbound_Course => 0.0,
1378 1201 --              Inbndlegdist => 0.0, Outbndlegdist => 0.0, Prdtas => 0.0, Prd_Wind_Mag => 0.0, Prd_Wind_True_Brg => 0.0
1379 1202 --              » ,
1380 1203 --              Prddataseq => 0, Prdalt => 0.0, Prdgwtttofix => 0.0, Fixdistodest => 0.0, Fixdtdbias => 0.0, Lgb_Leg_Ind
1381 1204 --              » ex => 0,
1382 1205 --              Fltphasefix => Base_Domain_Services_Tpkg.Flight_Phase_Type'First, Prdterm => False,Firstpass=>False,Spare_1=>0, Spare_
1383 1206 --              » 2 => 0 ));
1384 1207 --      end if;
1385 1208 --  end Get_Capture_Return_Path_Record;
1386 1209
1387 1210 procedure Put_Capture_Return_Path_Record
1388 1211 (
1389 1212     Process                : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
1390 1213     Route                  : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
1391 1214     Lateral_Offset_Point   : in Lateral_Offset_Segment_Type_Tpkg.Offset_Point_Subtype;
1392 1215     Capture_Return_Path_Record : in Flight_Pln_Hdr_Types.Offset_Capture_Return_Pt_Rec
1393 1216 ) is
1394 1217 --!
1395 1218 --| @DESCRIPTION: This procedure updates the specified capture/return path record in the specified flight plan head
1396 1219 --|
1397 1220 --| @SPECIAL_CONSIDERATIONS: N/A
1398 1221 --|
1399 1222 --!
1400 1223
1401 1224 New_Lock_Level : Apex_Partition_Pkg.Lock_Level_Type;
1402 1225 Status         : Apex_Types_Pkg.Status_Code_Type;

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_COM\_LGB.STB (continued)

```

1401 1224
1402 1225     begin
1403 1226         -- TODO Haraway: Enable and recode after A0
1404 1227     --     Set_Bite_Process_Data( Process );
1405 1228     --
1406 1229     --     if Writer( Route, Process ) then
1407 1230     --         Apex_Partition_Pkg.Lock_Preemption( New_Lock_Level, Status );
1408 1231     --
1409 1232     --         if Lateral_Offset_Point = Lateral_Offset_Segment_Type_Tpkg.Capture_Path_Start then
1410 1233     --             Put_Perf_Capture_Return_Path_Record( Capture_Return_Path_Record,
1411 1234     --                 Flt_Plan_Hdr( Route ).Lateral_Offset.Capture_Path_Start_Pt );
1412 1235     --         elsif Lateral_Offset_Point = Lateral_Offset_Segment_Type_Tpkg.Capture_Path_End then
1413 1236     --             Put_Perf_Capture_Return_Path_Record( Capture_Return_Path_Record, Flt_Plan_Hdr( Route ).Lateral_Offset.Capture_Path
1414 1237     --                 » _End_Pt );
1415 1238     --         elsif Lateral_Offset_Point = Return_Path_Start then
1416 1239     --             Put_Perf_Capture_Return_Path_Record( Capture_Return_Path_Record,
1417 1240     --                 Flt_Plan_Hdr( Route ).Lateral_Offset.Return_Path_Start_Pt );
1418 1241     --         else
1419 1242     --             Put_Perf_Capture_Return_Path_Record( Capture_Return_Path_Record, Flt_Plan_Hdr( Route ).Lateral_Offset.Return_Path_
1420 1243     --                 » End_Pt );
1421 1244     --         end if;
1422 1245     --         Apex_Partition_Pkg.Unlock_Preemption( New_Lock_Level, Status );
1423 1246     --
1424 1247     --         -- do not checksum if the FPLN is Undoprimary, Undoalternate, Scratchfpln, or Eosidfpln
1425 1248     --         if Route <= Airbus_Lgbm.Last_Checksummed_Flight_Plan then
1426 1249     --             -- TODO Haraway - Determine how to checksum/crc fpln using CoreFP
1427 1250     --             Checksum_Utils.Checksum_Header( Route );
1428 1251     --             null;
1429 1252     --         end if;
1430 1253     --     else
1431 1254     --
1432 1255     --         Common_Lgb_Int_Nonresync_Dpkg.Bite_Data.Route_Id_Valid := True;
1433 1256     --         Common_Lgb_Int_Nonresync_Dpkg.Bite_Data.Route_Id := Route;
1434 1257     --
1435 1258     --         Call_Bite_Recover( Lgb_Error_Code_Dpkg.Gb_No_Write_Access );
1436 1259     --     end if;
1437 1260
1438 1261     null;
1439 1262
1440 1263     end Put_Capture_Return_Path_Record;
1441 1264
1442 1265 end Common_Lgb;

```

Mode: All Lines

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_GTLGBHDR.STB

1	1	--
2	2	-- STUB FILE
3	3	--
4	4	-- CTP_A340S1A_PERF_BND_PUT_BK_DAT_GTLGBHDR.STB
5	5	--
6	6	-- REASON FOR STUBBING : The procedure getlgbhdr in the common_lgb package has been stubbed out to aid for CTP tes » ting.
7	7	--
8	8	
9		<del>-----+ DATA RIGHTS: HONEYWELL CONFIDENTIAL &amp; PROPRIETARY -----+ THIS WORK CONTAINS VALUABLE CONFIDENTIAL AND PROPRIETARY -----+ INFORMATION. DISCLOSURE, USE OR REPRODUCTION OUTSIDE OF -----+ HONEYWELL, INC. IS PROHIBITED EXCEPT AS AUTHORIZED IN WRITING. -----+ THIS UNPUBLISHED WORK IS PROTECTED BY THE LAWS OF THE UNITED -----+ STATES AND OTHER COUNTRIES. IN THE EVENT OF PUBLICATION, THE -----+ FOLLOWING NOTICE SHALL APPLY: COPR. 1999 HONEYWELL, INC. ALL -----+ RIGHTS RESERVED. -----+</del>
19	9	with Ctp_Perf_Bkgnd_Put_Bk_Data;
20	10	
21	11	separate (Common_Lgb)
22	12	procedure Getlgbhdr (Process_Id : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
23	13	Rte : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
24	14	Returned_Header : out Flight_Pln_Hdr_Types.Flight_Pln_Hdr_Rec) is
25		<del>-----+ --@DESCRIPTION: This routine reads from the Lateral Guidance Buffer header. -----+ Note: This routine only returns a single Flight Plan header. -----+</del>
	15	
29	16	
30	17	begin
31	18	Returned_Header := Ctp_Perf_Bkgnd_Put_Bk_Data.Guidhdr;
32	19	end Getlgbhdr;

Mode: All Lines

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_GTLGBLEG.STB

1	1	--
2	2	-- STUB FILE
3	3	--
4	4	-- CTP_A340S1A_PERF_BND_PUT_BK_DAT_GTLGBLEG.STB
5	5	--
6	6	-- REASON FOR STUBBING : The procedure Getlgbleg in the Common_Lgb package has been stubbed out to aid for CTP testi
7	7	» ng.
8	8	--
9	9	<del>DATA RIGHTS: HONEYWELL CONFIDENTIAL &amp; PROPRIETARY</del>
10	10	<del>THIS WORK CONTAINS VALUABLE CONFIDENTIAL AND PROPRIETARY</del>
11	11	<del>INFORMATION. DISCLOSURE, USE OR REPRODUCTION OUTSIDE OF</del>
12	12	<del>HONEYWELL INTERNATIONAL, INC. IS PROHIBITED EXCEPT AS</del>
13	13	<del>AUTHORIZED IN WRITING. THIS UNPUBLISHED WORK IS PROTECTED BY</del>
14	14	<del>THE LAWS OF THE UNITED STATES AND OTHER COUNTRIES. IN THE</del>
15	15	<del>EVENT OF PUBLICATION, THE FOLLOWING NOTICE SHALL APPLY:</del>
16	16	<del>COPR. 2003 HONEYWELL INTERNATIONAL, INC. ALL RIGHTS RESERVED.</del>
17	17	<del></del>
18	18	<del></del>
19	10	with Ctp_Perf_Bkgnd_Put_Bk_Data;
20	11	
21	12	--with Fpp_Common_Lgb_Wrap_Pkg;
22	13	--with Fpp_Interface_Type;
23	14	
24	15	separate (Common_Lgb)
25	16	procedure Getlgbleg (Process_Id : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
26	17	Leg_Index : in Flight_Pln_Leg_Types.Leg_Index_Type;
27	18	Lateral_Leg : out Flight_Pln_Leg_Types.Leg_Rec) is
28	19	--!
29	20	-- @DESCRIPTION: This routine will read from a leg of a flight plan. It will
30	21	--  output a leg record via the parameter list.
31	22	
32	23	
33	24	-- Indices of neighboring legs
34	25	Next_Leg : Flight_Pln_Leg_Types.Leg_Index_Type;
35	26	Previous_Leg : Flight_Pln_Leg_Types.Leg_Index_Type;
36	27	
37	28	begin
38	29	
39	30	Lateral_Leg := Ctp_Perf_Bkgnd_Put_Bk_Data.Gleg;
40	31	CTP_PERF_BKGND_PUT_BK_DATA.Getlgbleg_Exec := True;

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_GTLGBLEG.STB (continued)

41	32	
42	33	
43	34	end Getlgbleg;
44	35	

Mode: All Lines

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB

1	1	--	
2	2	--	STUB FILE
3	3	--	
4	4	--	CTP_A340S1A_PERF_BND_PUT_BK_DAT_INTR_DKG.STB
5	5	--	
6	6	--	REASON FOR STUBBING : The following procedure Put_Hm_Preds in the package body perf_interface_dpkg is stubbed o
		--	» ut to
7	7	--	aid for CTP testing.
8		--	
9		--	
10		--	<del>DATA RIGHTS: HONEYWELL CONFIDENTIAL &amp; PROPRIETARY</del>
11		--	<del>THIS WORK CONTAINS VALUABLE CONFIDENTIAL AND PROPRIETARY</del>
12		--	<del>INFORMATION. DISCLOSURE, USE OR REPRODUCTION OUTSIDE OF</del>
13		--	<del>HONEYWELL INTERNATIONAL, INC. IS PROHIBITED EXCEPT AS</del>
14		--	<del>AUTHORIZED IN WRITING. THIS UNPUBLISHED WORK IS PROTECTED BY</del>
15		--	<del>THE LAWS OF THE UNITED STATES AND OTHER COUNTRIES. IN THE</del>
16		--	<del>EVENT OF PUBLICATION, THE FOLLOWING NOTICE SHALL APPLY:</del>
17		--	<del>COPR. 2005 HONEYWELL INTERNATIONAL, INC. ALL RIGHTS RESERVED.</del>
18		--	
19	8	--	
20		--	<del>File Name: CTP_A340S1A_PERF_BND_PUT_BK_DAT_INTR_DKG.STB</del>
	9	--	<del>Original File Name: PERF_INTERFACE_DPKG.ADA</del>
21	10	--	
22		--	<del>with Portable_Types_Pkg; common sw</del>
23	11	--	<del>with Apex_Types_Pkg; common</del>
24		--	<del>with Perf_Flight_Test_Dpkg; shared fmf objects</del>
25		--	<del>with Tailpostyp_Types; gray</del>
	12	--	<del>with Conversion_Const_Pkg;</del>
26	13	--	<del>with Destdata_Tpkg; shared fmf types</del>
27	14	--	<del>with Hm_Pred_Tpkg; shared fmf types</del>
	15	--	<del>with Io_Fmf_Out_Dpkg; shared fmf types</del>
	16	--	<del>with Perf_Ac_Spec_Const_Dpkg;</del>
	17	--	<del>with Perf_Dpkg;</del>
28	18	--	<del>with Perf_Ext_Tpkg; shared fmf types</del>
	19	--	<del>with Perf_Flight_Test_Dpkg; shared fmf objects</del>
	20	--	<del>with Portable_Types_Pkg; common sw</del>
	21	--	<del>with Radian_Uilities_Pkg;</del>
	22	--	<del>with Sys_Perf_Interface_Dpkg; shared fmf objects</del>
	23	--	<del>with Tailpostyp_Types; gray</del>
29	24	--	<del>with Xatermtyp_Tpkg; shared fmf types</del>
30		--	<del>with Io_Fmf_Out_Dpkg; shared fmf types</del>
	25	--	<del>with Flight_Pln_Leg_Types;</del>

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB (continued)

	26	with Fprequestrec_Types;
	27	with Perf_Ac_Spec_Const_Dpkg;
	28	with Radian_Uilities_Pkg;
31	29	
	30	use Portable_Types_Pkg;
	31	use Fprequestrec_Types;
	32	use Xatermtyp_Tpkg;
32	33	
	34	
	35	
33	36	with CTP_PERF_BKGND_PUT_BK_DATA;
34	37	
35	38	package body Perf_Interface_Dpkg is
36		<del>--- </del>
37		<del>---  @DESCRIPTION: This Object Manager handles storing and retrieving various simple</del>
38		<del>---  interfaces from Perf to multiple functional areas.</del>
39		<del>--- </del>
40		<del>--- </del>
41		<del>--- </del>
	39	
42	40	
43	41	type Storage_Record_Type is
44	42	record
45	43	Active_Legdist : Perf_Ext_Tpkg.Inputfparr;
46	44	Hm_Preds : Hm_Pred_Tpkg.Hmpredarrtyp;
47		<del>-----Pgcrzisadev : Perf_Ext_Tpkg.Aorsrealtyp;</del>
48	45	Pgdestdata : Destdata_Tpkg.Destdataarr;
49	46	Dest_Efob_Below_Min : Boolean;
50		<del>-----Pgprddataseq : Perf_Ext_Tpkg.Aorsinttyp;</del>
	47	Pgprddataseq : Perf_Ext_Tpkg.Aorsinttyp;
51	48	Pgvisadev : Portable_Types_Pkg.Float_32;
52	49	Pgvpredavail : Perf_Ext_Tpkg.Aorsbooltyp;
53		<del>-----Pgxaterm : Xatermtyp_Tpkg.Xatermtyp;</del>
	50	Pgxaterm : Xatermtyp_Tpkg.Xa_Leg_Array;
54	51	Preds_Complete : Perf_Ext_Tpkg.Aorsbooltyp;
55	52	Prglbgwt : Perf_Ext_Tpkg.Aorsrealtyp;
56	53	Prglbgwtind : Perf_Ext_Tpkg.Aorspcalctyp;
57	54	Pslev3Dist : Perf_Ext_Tpkg.Lev3Rectyp;
58	55	Psnewpreds : Perf_Ext_Tpkg.Aorsbooltyp;
59	56	Psnukepdchg : Boolean;
60	57	Psspdlimdist : Io_Interface_Tpkg.Float_32_Valid.Normal;
61	58	Pstod2Valid : Boolean;
62		<del>-----Psaltplnchg : Boolean;</del>
	59	Psaltplnchg : Boolean;



File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB (continued)

63	60	Strat_Preds_Stale : Perf_Ext_Tpkg.Aorsbooltyp;
64	61	end record;
65	62	
66	63	Data_Storage : Storage_Record_Type;
67	64	New_Lock_Level : Apex_Partition_Pkg.Lock_Level_Type;
68	65	Status : Apex_Types_Pkg.Status_Code_Type;
69	66	
70	67	
71	68	
72	69	procedure Initialize (Init_Type : in Apex_Partition_Pkg.Operating_Mode_Type) is
73		<del>-----+-----</del>
74		<del>-----@DESCRIPTION: This procedure initializes the interface object manager.</del>
75		<del>-----The parameter defines the type of event prompting the</del>
76		<del>-----initialization.</del>
77		<del>-----+-----</del>
78		<del>-----+-----</del>
79		<del>-----+-----</del>
80		<del>-----@SPECIAL_CONSIDERATIONS: N/A</del>
81		<del>-----+-----</del>
	70	
82	71	
83	72	begin
84	73	null;
85	74	end Initialize;
86	75	
87	76	
88	77	function Hm_Preds (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type) return Hm_Pred_Tpkg.Hmpredtyp is
89		<del>-----+-----</del>
90		<del>-----@DESCRIPTION: Retrieves the hold predictions data for the requested fpln.</del>
91		<del>-----+-----</del>
92		<del>-----+-----</del>
93		<del>-----@UNITS: Fpln - Predictable major flight plan, see type definition</del>
94		<del>-----Return Value - Record type, see type definition</del>
95		<del>-----+-----</del>
96		<del>-----+-----</del>
97		<del>-----@SPECIAL_CONSIDERATIONS: N/A</del>
98		<del>-----+-----</del>
	78	
99	79	
100	80	begin
101	81	return Data_Storage.Hm_Preds (Fpln);
102	82	end Hm_Preds;
103	83	
104	84	

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB (continued)

105	85	procedure Put_Hm_Preds (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type; Data : in Hm_Pred_Tpkg.Hmpredtyp) is
106		<del>-----+-----</del>
107		<del>----- @DESCRIPTION: Stores the hold predictions data for the flight plan that is passed in.</del>
108		<del>-----+-----</del>
109		<del>-----+-----</del>
110		<del>----- @UNITS: Fpln Predictable major flight plan, see type definition</del>
111		<del>----- Data Record type, see type definition</del>
112		<del>-----+-----</del>
113		<del>-----+-----</del>
114		<del>----- @SPECIAL_CONSIDERATIONS: N/A</del>
115		<del>-----+-----</del>
	86	
116	87	
117	88	begin
118	89	Ctp_Perf_Bkgnd_Put_Bk_Data.Pshmpreddata := Data;
119	90	Ctp_Perf_Bkgnd_Put_Bk_Data.Put_Hm_Preds_Exec := True;
120	91	end Put_Hm_Preds;
121	92	
122		
123		<del>function Pgerzisadev (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type) return Portable_Types_Pkg.Float_32 is</del>
124		<del>-----+-----</del>
125		<del>----- @DESCRIPTION: Retrieves the cruise ISA deviation for the requested fpln.</del>
126		<del>-----+-----</del>
127		<del>-----+-----</del>
128		<del>----- @UNITS: Fpln Predictable major flight plan, see type definition</del>
129		<del>----- Return Value degrees C</del>
130		<del>-----+-----</del>
131		<del>-----+-----</del>
132		<del>----- @SPECIAL_CONSIDERATIONS: N/A</del>
133		<del>-----+-----</del>
134		
135		<del>begin</del>
136		<del>return Data_Storage.Pgerzisadev (Fpln);</del>
137		<del>end Pgerzisadev;</del>
138		
139		
140		<del>procedure Put_Pgerzisadev (Data : in Portable_Types_Pkg.Float_32; Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type) is</del>
141		<del>-----+-----</del>
142		<del>----- @DESCRIPTION: Stores the cruise ISA deviation for the flight plan that is passed in.</del>
143		<del>-----+-----</del>
144		<del>-----+-----</del>
145		<del>----- @UNITS: Data degrees C</del>
146		<del>----- Fpln Predictable major flight plan, see type definition</del>
147		<del>-----+-----</del>

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB (continued)

148		<del>-----+-----</del>
149		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>
150		<del>-----+-----</del>
151		
152		<del>-----begin</del>
153		<del>-----Data_Storage.Pgerzisadev (Fpln) := Data;</del>
154		<del>-----end Put_Pgerzisadev;</del>
155		
156		
157	93	function Pgdestdata (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type) return Destdata_Tpkg.Destdatarec is
158		<del>-----+-----</del>
159		<del>-----+-----@DESCRIPTION: Retrieves the destination data for the requested flight plan.</del>
160		<del>-----+-----</del>
161		<del>-----+-----</del>
162		<del>-----+-----@UNITS: Fpln Predictable major flight plan, see type definition</del>
163		<del>-----+-----Return Value Record type, see type definition</del>
164		<del>-----+-----</del>
165		<del>-----+-----</del>
166		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>
167		<del>-----+-----</del>
	94	
168	95	
169	96	begin
170	97	return Data_Storage.Pgdestdata (Fpln);
171	98	end Pgdestdata;
172	99	
173	100	
174	101	procedure Put_Pgdestdata (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type; Data : in Destdata_Tpkg.Destdatarec) is
175		<del>-----+-----</del>
176		<del>-----+-----@DESCRIPTION: Stores the destination data for the flight plan that is passed in.</del>
177		<del>-----+-----</del>
178		<del>-----+-----</del>
179		<del>-----+-----@UNITS: Fpln Predictable major flight plan, see type definition</del>
180		<del>-----+-----Data Record type, see type definition</del>
181		<del>-----+-----</del>
182		<del>-----+-----</del>
183		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>
184		<del>-----+-----</del>
	102	
185	103	
186	104	begin
187	105	Apex_Partition_Pkg.Lock_Preemption (New_Lock_Level, Status);
188	106	Data_Storage.Pgdestdata (Fpln) := Data;
	107	

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB (continued)

	108	
189	109	Apex_Partition_Pkg.Unlock_Preemption (New_Lock_Level, Status);
190	110	
191	111	Perf_Flight_Test_Dpkg.Put_Pgdestdata_Ftb_Flags (Fpln, Data);
192	112	end Put_Pgdestdata;
193	113	
194	114	
195	115	function Pgprddataseq (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type) return Portable_Types_Pkg.Integer_32 is
196		<del>-----+ @DESCRIPTION: Retrieves the predictions data sequence counter for the requested fpln. -----+ @UNITS: Fpln Predictable major flight plan, see type definition -----+ Return Value Unitless (should be an integer value ranging from 1 to 31) -----+ -----+ @SPECIAL_CONSIDERATIONS: N/A -----+</del>
	116	
206	117	
207	118	begin
208	119	return Data_Storage.Pgprddataseq (Fpln);
209	120	end Pgprddataseq;
210	121	
211	122	
212	123	procedure Put_Pgprddataseq (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type; Data : in Portable_Types_Pkg.Integer_32) is
213		<del>-----+ @DESCRIPTION: Stores the predictions data sequence counter for the -----+ flight plan that is passed in. -----+ @UNITS: Fpln Predictable major flight plan, see type definition -----+ Data Unitless (should be an integer value ranging from 1 to 31) -----+ -----+ @SPECIAL_CONSIDERATIONS: N/A -----+</del>
	124	
224	125	
225	126	begin
226	127	Data_Storage.Pgprddataseq (Fpln) := Data;
227	128	end Put_Pgprddataseq;
228	129	
229	130	

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB (continued)

230	131	procedure Put_Dest_Efob_Below_Min (Data : in Boolean) is
231		<del>-----+-----</del>
232		<del>----- @DESCRIPTION: Stores a flag indicating the Active flight plan's Dest EFOB is below the Min Dest Fob value.</del>
233		<del>----- ----- This flag is then passed to IO through a Put call via the IO-owned interface.</del>
234		<del>-----+-----</del>
235		<del>----- @UNITS: Data T/F</del>
236		<del>-----+-----</del>
237		<del>-----+-----</del>
238		<del>----- @SPECIAL_CONSIDERATIONS: In the Put call the IO interface, the data validity is always assumed to be True (i.e.</del>
239		<del>      » , valid);</del>
		<del>-----+-----</del>
	132	
240	133	
241	134	begin
242	135	Io_Fmf_Out_Dpkg.Destination_EFOB_Below_Min.Put (Data => Data, Is_Valid => True);
243	136	Data_Storage.Dest_Efob_Below_Min := Data;
244	137	end Put_Dest_Efob_Below_Min;
245	138	
246	139	
247	140	function Pgvisadev return Portable_Types_Pkg.Float_32 is
248		<del>-----+-----</del>
249		<del>----- @DESCRIPTION: Retrieves the ISA temperature deviation.</del>
250		<del>-----+-----</del>
251		<del>-----+-----</del>
252		<del>----- @UNITS: Return Value degrees C</del>
253		<del>-----+-----</del>
254		<del>-----+-----</del>
255		<del>----- @SPECIAL_CONSIDERATIONS: N/A</del>
256		<del>-----+-----</del>
	141	
257	142	
258	143	begin
259	144	return Data_Storage.Pgvisadev;
260	145	end Pgvisadev;
261	146	
262	147	
263	148	procedure Put_Pgvisadev (Data : in Portable_Types_Pkg.Float_32) is
264		<del>-----+-----</del>
265		<del>----- @DESCRIPTION: Stores the ISA temperature deviation.</del>
266		<del>-----+-----</del>
267		<del>-----+-----</del>
268		<del>----- @UNITS: Data degrees C</del>
269		<del>-----+-----</del>
270		<del>-----+-----</del>

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB (continued)

271		<del>-----+-----</del>	<del>@SPECIAL_CONSIDERATIONS: N/A</del>
272		<del>-----+-----</del>	
	149		
273	150		
274	151	begin	
275	152	Data_Storage.Pgvisadev := Data;	
276	153	end Put_Pgvisadev;	
277	154		
278	155		
279	156	function Pgvpredavail ( Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type := Fprequestrec_Types.Active ) return Boolean is	
280		<del>-----+-----</del>	
281		<del>-----+-----</del>	<del>@DESCRIPTION: Retrieves the flag indicating that there is sufficient critical data (pilot entered and pulled fr</del>
		<del>» em</del>	
282		<del>-----+-----</del>	<del>I/O busses - external LRUs) in order to compute trajectory predictions.</del>
283		<del>-----+-----</del>	<del>If no flight plan is specified, the default is the Active flight plan.</del>
284		<del>-----+-----</del>	
285		<del>-----+-----</del>	
286		<del>-----+-----</del>	<del>@UNITS: Return Value   T : Sufficient data</del>
287		<del>-----+-----</del>	<del>                          F : Insufficient data</del>
288		<del>-----+-----</del>	
289		<del>-----+-----</del>	<del>@RATE: Asynchronous   called in Perf Background, IO, VG, Lat Path, CI.</del>
290		<del>-----+-----</del>	
291		<del>-----+-----</del>	
292		<del>-----+-----</del>	<del>@SPECIAL_CONSIDERATIONS: N/A</del>
293		<del>-----+-----</del>	
	157		
294	158		
295	159	begin	
296	160	return Data_Storage.Pgvpredavail ( Fpln );	
297	161	end Pgvpredavail;	
298	162		
299	163		
300	164	procedure Put_Pgvpredavail ( Data : in Boolean;	
301	165	Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type := Fprequestrec_Types.Active ) is	
302		<del>-----+-----</del>	
303		<del>-----+-----</del>	<del>@DESCRIPTION: Stores the flag indicating that there is sufficient critical data (pilot entered and pulled from</del>
304		<del>-----+-----</del>	<del>I/O busses - external LRUs) in order to compute trajectory predictions.</del>
305		<del>-----+-----</del>	<del>If no flight plan is specified, the default is the Active flight plan.</del>
306		<del>-----+-----</del>	
307		<del>-----+-----</del>	
308		<del>-----+-----</del>	<del>@UNITS: Stored Data    T : Sufficient data</del>
309		<del>-----+-----</del>	<del>                          F : Insufficient data</del>
310		<del>-----+-----</del>	<del>                          Fpln   any valid flight plan that Perf predicts</del>
311		<del>-----+-----</del>	

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB (continued)

312		<del>-----@RATE: Asynchronous called in Perf Background and Perf Demand.</del>
313		<del>-----</del>
314		<del>-----</del>
315		<del>-----@SPECIAL_CONSIDERATIONS: N/A</del>
316		<del>-----</del>
	166	
317	167	
318	168	begin
319	169	Data_Storage.Pgvpredavail ( Fpln ) := Data;
320	170	end Put_Pgvpredavail;
321	171	
322	172	
323		<del>function Pgxaterm return Xatermtyp_Tpkg.Xatermtyp is</del>
324		<del>-----</del>
325		<del>-----@DESCRIPTION: Retrieves the XA termination data.</del>
326		<del>-----</del>
327		<del>-----</del>
328		<del>-----@UNITS: Return Value Record type, see type definition</del>
329		<del>-----</del>
330		<del>-----</del>
331		<del>-----@SPECIAL_CONSIDERATIONS: N/A</del>
332		<del>-----</del>
	173	function Pgxaterm ( Legindex : in Flight_Pln_Leg_Types.Leg_Index_Type ;
	174	Fpln      : in Perf_Ext_Tpkg.Pred_Major_Fp_Type := Fprequestrec_Types.Active) return Xatermtyp_Tp
		» kg.Xatermtyp is
	175	
	176	Xaleg_Arrayindx : Portable_Types_Pkg.Integer_32 := 0;
	177	Leg_Found : Boolean := False;
	178	Default_Xatermtyp_Data : Xatermtyp_Tpkg.Xatermtyp := (FPA =>0.0, LEGIDENT =>0, PRDDATASEQ => 0);
333	179	
334	180	begin
335		<del>return Data_Storage.Pgxaterm;</del>
	181	if (Legindex /=0) then
	182	loop
	183	Xaleg_Arrayindx := Xaleg_Arrayindx + 1;
	184	if ( Data_Storage.Pgxaterm ( Fpln ) ( Xaleg_Arrayindx ).Legident = Legindex ) then
	185	Leg_Found := True;
	186	end if;
	187	exit when (Leg_Found or else (Xaleg_Arrayindx >= Xatermtyp_Tpkg.Max_Xalegs_Array_Idx));
	188	end loop;
	189	if Leg_Found then
	190	return Data_Storage.Pgxaterm ( Fpln ) ( Xaleg_Arrayindx ) ;
	191	else
	192	return Default_Xatermtyp_Data;

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB (continued)

	193	end if;
	194	else
	195	return Default_Xatermtyp_Data;
	196	end if;
336	197	end Pgxaterm;
337	198	
338		<del>procedure Put_Pgxaterm (Data : in Xatermtyp_Tpkg.Xatermtyp) is</del>
339		<del>-----+-----</del>
340		<del>-----+-----</del>
341		<del>-----+-----</del>
342		<del>-----+-----</del>
343		<del>-----+-----</del>
344		<del>-----+-----</del>
345		<del>-----+-----</del>
346		<del>-----+-----</del>
347		<del>-----+-----</del>
348		<del>-----+-----</del>
	199	
	200	
	201	procedure Put_Pgxaterm (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type := Fprequestrec_Types.Active;
	202	Xaleg_Arrayindx : in Xatermtyp_Tpkg.Xaleg_Array_Range;
	203	Data : in Xatermtyp_Tpkg.Xatermtyp) is
	204	
349	205	begin
350	206	Apex_Partition_Pkg.Lock_Preemption (New_Lock_Level, Status);
351	207	Apex_Partition_Pkg.Unlock_Preemption (New_Lock_Level, Status);
352		<del>Data_Storage.Pgxaterm := Data;</del>
	208	<del>Data_Storage.Pgxaterm (Fpln) (Xaleg_Arrayindx) := Data;</del>
353	209	Apex_Partition_Pkg.Unlock_Preemption (New_Lock_Level, Status);
354	210	end Put_Pgxaterm;
355	211	
356	212	
	213	
357	214	function Prglbgwt (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type) return Portable_Types_Pkg.Float_32 is
358		<del>-----+-----</del>
359		<del>-----+-----</del>
360		<del>-----+-----</del>
361		<del>-----+-----</del>
362		<del>-----+-----</del>
363		<del>-----+-----</del>
364		<del>-----+-----</del>
365		<del>-----+-----</del>
366		<del>-----+-----</del>
367		<del>-----+-----</del>



File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB (continued)

	215	
368	216	
369	217	begin
370	218	return Data_Storage.Prglbgwt (Fpln);
371	219	end Prglbgwt;
372	220	
373	221	
374	222	procedure Put_Prglbgwt (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type; Data : in Portable_Types_Pkg.Float_32) is
375		<del>-----+ @DESCRIPTION: Stores the gross weight for the flight plan that is passed in. -----+ @UNITS: Fpln Predictable major flight plan, see type definition -----+           Data kg -----+ @SPECIAL_CONSIDERATIONS: N/A -----+</del>
	223	
385	224	
386	225	begin
387	226	Data_Storage.Prglbgwt (Fpln) := Data;
388	227	end Put_Prglbgwt;
389	228	
390	229	
391	230	function Prglbgwtind (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type) return Io_Interface_Tpkg.Entry_Stat_Type is
392		<del>-----+ @DESCRIPTION: Retrieves the gross weight status indicator for the requested fpln. -----+ @UNITS: Fpln Predictable major flight plan, see type definition -----+           Return Value Invalid, Valid, Pilot_Entered -----+ @SPECIAL_CONSIDERATIONS: N/A -----+</del>
	231	
402	232	
403	233	begin
404	234	return Data_Storage.Prglbgwtind (Fpln);
405	235	end Prglbgwtind;
406	236	
407	237	
408	238	procedure Put_Prglbgwtind (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type; Status : in Io_Interface_Tpkg.Entry_Stat_Type

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB (continued)

		» ) is
409		<del>-----+-----</del>
410		<del>-----+-----@DESCRIPTION: Stores the gross weight status indicator for the flight plan that is passed in.</del>
411		<del>-----+-----</del>
412		<del>-----+-----</del>
413		<del>-----+-----@UNITS: Fpln Predictable major flight plan, see type definition</del>
414		<del>-----+-----Status Invalid, Valid, Pilot_Entered</del>
415		<del>-----+-----</del>
416		<del>-----+-----</del>
417		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>
418		<del>-----+-----</del>
	239	
419	240	
420	241	begin
421	242	Data_Storage.Prglbgwtind (Fpln) := Status;
422	243	end Put_Prglbgwtind;
423	244	
424	245	
425	246	function Pslev3Dist return Perf_Ext_Tpkg.Lev3Rectyp is
426		<del>-----+-----</del>
427		<del>-----+-----@DESCRIPTION: Retrieves the Level 3 distance data.</del>
428		<del>-----+-----</del>
429		<del>-----+-----</del>
430		<del>-----+-----@UNITS: Return Value Record type, see type definition</del>
431		<del>-----+-----</del>
432		<del>-----+-----</del>
433		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>
434		<del>-----+-----</del>
	247	
435	248	
436	249	begin
437	250	return Data_Storage.Pslev3Dist;
438	251	end Pslev3Dist;
439	252	
440	253	
441	254	procedure Put_Pslev3Dist (Data : in Perf_Ext_Tpkg.Lev3Rectyp) is
442		<del>-----+-----</del>
443		<del>-----+-----@DESCRIPTION: Stores the Level 3 distance data.</del>
444		<del>-----+-----</del>
445		<del>-----+-----</del>
446		<del>-----+-----@UNITS: Data Record type, see type definition</del>
447		<del>-----+-----</del>
448		<del>-----+-----</del>
449		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB (continued)

450		-----+ 255
451	256	
452	257	begin
453	258	Apex_Partition_Pkg.Lock_Preemption (New_Lock_Level, Status);
454	259	Data_Storage.Pslev3Dist := Data;
	260	Data_Storage.Pslev3Dist.Distance := Radian_Uilities_Pkg.Urlim( Numl => Data_Storage.Pslev3Dist.Distance,
	261	Lower => 0.0,
	262	Upper => Perf_Ac_Spec_Const_Dpkg.Max_Flight_Plan_D
		>> instance );
	263	
455	264	Apex_Partition_Pkg.Unlock_Preemption (New_Lock_Level, Status);
456	265	end Put_Pslev3Dist;
457	266	
458	267	
459	268	function Psnewpreds (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type) return Boolean is
460		-----+ 461  @DESCRIPTION: Returns a flag indicating if the first two passes of predictions 462  are in progress for the input flight plan. 463 -----+ 464 -----+ 465  --@UNITS: Return Value -- T/F 466 -----+ 467 -----+ 468  @SPECIAL_CONSIDERATIONS: N/A 469 -----+
	269	
470	270	
471	271	begin
472	272	return Data_Storage.Psnewpreds (Fpln);
473	273	end Psnewpreds;
474	274	
475	275	
476	276	procedure Put_Psnewpreds (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type; Data : in Boolean) is
477		-----+ 478  @DESCRIPTION: Stores a flag indicating if the first two passes of predictions 479  are in progress for the input flight plan. 480 -----+ 481 -----+ 482  @UNITS: Data -- T/F 483 -----+ 484 -----+ 485  @SPECIAL_CONSIDERATIONS: N/A 486 -----+

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB (continued)

	277	
487	278	
488	279	begin
489	280	Data_Storage.Psnewpreds (Fpln) := Data;
490	281	end Put_Psnewpreds;
491	282	
492	283	
493	284	function Psnukespdchg return Boolean is
494		<del>-----+ @DESCRIPTION: Retrieves the flag indicating if the speed change point should not be displayed on the EFIS. -----+ @UNITS: Return Value T/F -----+ @SPECIAL_CONSIDERATIONS: N/A -----+</del>
	285	
504	286	
505	287	begin
506	288	return Data_Storage.Psnukespdchg;
507	289	end Psnukespdchg;
508	290	
509	291	
510	292	procedure Put_Psnukespdchg (Data : in Boolean) is
511		<del>-----+ @DESCRIPTION: Stores the flag indicating if the speed change point should not be displayed on the EFIS. -----+ @UNITS: Data T/F -----+ @SPECIAL_CONSIDERATIONS: N/A -----+</del>
	293	
521	294	
522	295	begin
523	296	Data_Storage.Psnukespdchg := Data;
524	297	end Put_Psnukespdchg;
525	298	
526	299	
527	300	function Psspdlimdist return Io_Interface_Tpkg.Float_32_Valid.Normal is

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB (continued)

528		-----+-----
529		----- -----@DESCRIPTION: Retrieves the distance to the point where the speed target changes
530		----- -----due to a speed limit when lateral auto mode is not engaged.
531		-----+-----
532		-----+-----
533		----- -----@UNITS: Return Value Record type with the following components:
534		----- -----Data nm
535		----- -----Valid T/F
536		-----+-----
537		-----+-----
538		----- -----@SPECIAL_CONSIDERATIONS: N/A
539		-----+-----
	301	
540	302	
541	303	begin
542	304	return Data_Storage.Psspdlimdist;
543	305	end Psspdlimdist;
544	306	
545	307	
546	308	procedure Put_Psspdlimdist (Data : in Io_Interface_Tpkg.Float_32_Valid.Normal) is
547		-----+-----
548		----- -----@DESCRIPTION: Stores the distance to the point where the speed target changes
549		----- -----due to a speed limit when lateral auto mode is not engaged.
550		-----+-----
551		-----+-----
552		----- -----@UNITS: Data Record type with the following components:
553		----- -----Data nm
554		----- -----Valid T/F
555		-----+-----
556		-----+-----
557		----- -----@SPECIAL_CONSIDERATIONS: N/A
558		-----+-----
	309	
559	310	
560	311	begin
561	312	Data_Storage.Psspdlimdist := Data;
562	313	end Put_Psspdlimdist;
563	314	
564	315	
565	316	function Pstod2Valid return Boolean is
566		-----+-----
567		----- -----@DESCRIPTION: Retrieves the flag indicating if EFIS can display TOD2 when not in
568		----- -----level change auto control.
569		-----+-----

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB (continued)

570		<del>-----+-----</del>
571		<del>-----+-----@UNITS: Return Value T/F</del>
572		<del>-----+-----</del>
573		<del>-----+-----</del>
574		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>
575		<del>-----+-----</del>
	317	
576	318	
577	319	begin
578	320	return Data_Storage.Pstod2Valid;
579	321	end Pstod2Valid;
580	322	
581	323	
582	324	procedure Put_Pstod2Valid (Data : in Boolean) is
583		<del>-----+-----</del>
584		<del>-----+-----@DESCRIPTION: Stores the flag indicating if EFIS can display TOD2 when not in</del>
585		<del>-----+-----level change auto control.</del>
586		<del>-----+-----</del>
587		<del>-----+-----</del>
588		<del>-----+-----@UNITS: Data T/F</del>
589		<del>-----+-----</del>
590		<del>-----+-----</del>
591		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>
592		<del>-----+-----</del>
	325	
593	326	
594	327	begin
595	328	Data_Storage.Pstod2Valid := Data;
596	329	end Put_Pstod2Valid;
597	330	
598	331	
599	332	function Psaltplnchg return Boolean is
600		<del>-----+-----</del>
601		<del>-----+-----@DESCRIPTION: Retrieves the flag indicating if data has changed since the altitude</del>
602		<del>-----+-----planning task started.</del>
603		<del>-----+-----</del>
604		<del>-----+-----</del>
605		<del>-----+-----@UNITS: Return Value T/F</del>
606		<del>-----+-----</del>
607		<del>-----+-----</del>
608		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>
609		<del>-----+-----</del>
	333	
610	334	

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB (continued)

611	335	begin
612	336	return Data_Storage.Psaltplnchg;
613	337	end Psaltplnchg;
614	338	
615	339	
616	340	procedure Put_Psaltplnchg (Data : in Boolean) is
617		<del>-----+-----</del>
618		<del>----- @DESCRIPTION: Stores the flag indicating if data has changed since the altitude</del>
619		<del>-----                     planning task started.</del>
620		<del>----- </del>
621		<del>----- </del>
622		<del>----- --@UNITS: Data -- T/F</del>
623		<del>----- </del>
624		<del>----- </del>
625		<del>----- @SPECIAL_CONSIDERATIONS: N/A</del>
626		<del>----- </del>
	341	
627	342	begin
628	343	Data_Storage.Psaltplnchg := Data;
629	344	end Put_Psaltplnchg;
630	345	
631	346	
632	347	
633	348	
634	349	function Active_Legdist (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type) return Io_Interface_Tpkg.Float_32_Valid.Normal » is
635		<del>-----+-----</del>
636		<del>----- @DESCRIPTION: Returns the leg distance of the active leg in the input flight plan.</del>
637		<del>-----                     This leg distance will be valid only for copy Active Secondary(1-3) and</del>
638		<del>-----                     Temporary flight plans when the active leg does not match the active leg</del>
639		<del>-----                     of the Active flight plan (see Fpln_Ext_Dpkg.Active_Legs_Match).</del>
640		<del>----- --Note:</del>
641		<del>-----                     This interface should not be called for the Active fpln. Use LG's active leg distance.</del>
642		<del>----- --This interface should not be called for the Temporary fpln if the active leg matches</del>
643		<del>-----                     the active leg of the Active fpln. Instead, use LG's active leg distance.</del>
644		<del>-----                     This interface should not be called for From To Secondary(1-3) fplns. Instead, use</del>
645		<del>----- --the Fixdistodest of the From and To waypoints of the active leg to compute the leg distance.</del>
646		<del>-----                     This interface should not be called for a copy Active Secondary(1-3) fpln when the</del>
647		<del>----- --active leg matches the active leg of the Active fpln. Instead, use LG's active leg distance.</del>
648		<del>----- </del>
649		<del>----- </del>
650		<del>----- --@UNITS: Fpln -- Predictable major flight plan, see type definition</del>
651		<del>-----                     Return Value Record type with the following components:</del>
652		<del>----- --Data -- nm</del>

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB (continued)

653		<del>Valid T/F</del>
654		<del>-----</del>
655		<del>-----</del>
656		<del>--@SPECIAL_CONSIDERATIONS: Described above.</del>
657		<del>-----</del>
	350	
658	351	begin
659	352	return Data_Storage.Active_Legdist (Fpln);
660	353	end Active_Legdist;
661	354	
662	355	
663	356	procedure Put_Active_Legdist (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type;
664	357	Data : in Io_Interface_Tpkg.Float_32_Valid.Normal) is
665		<del>-----</del>
666		<del>--@DESCRIPTION: Stores the active leg distance for the input flight plan.</del>
667		<del>-----</del>
668		<del>    This leg distance shall be set valid only for copy Active Secondary(1-3) and</del>
669		<del>    Temporary flight plans when the active leg does not match the active leg</del>
670		<del>    of the Active flight plan (see Fpln_Ext_Dpkg.Active_Legs_Match).</del>
671		<del>    This interface shall be called to invalidate the active leg distance for the</del>
672		<del>    following:</del>
673		<del>    Temporary fpln when the active leg matches the active leg of the Active fpln;</del>
674		<del>    From To Secondary(1-3) fplns;</del>
675		<del>    Copy Active Secondary(1-3) fplns when the active leg matches the active leg of the</del>
676		<del>    Active fpln.</del>
677		<del>    This interface should not be called for the Active fpln.</del>
678		<del>-----</del>
679		<del>--@UNITS: Fpln Predictable major flight plan, see type definition</del>
680		<del>    Data Record type with the following components:</del>
681		<del>    Data nm</del>
682		<del>    Valid T/F</del>
683		<del>-----</del>
684		<del>-----</del>
685		<del>--@SPECIAL_CONSIDERATIONS: Described above.</del>
686		<del>-----</del>
	358	
687	359	begin
688	360	Data_Storage.Active_Legdist (Fpln) := Data;
689	361	end Put_Active_Legdist;
690	362	
691	363	
692	364	function Preds_Complete (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type) return Boolean is
693		<del>-----</del>
694		<del>--@DESCRIPTION: Retrieves the flag indicating the completion of the active</del>



File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_INTR\_DKG.STB (continued)

695		<del>primary flight plan predictions.</del>
696		
697		
698		<del>@UNITS: Return Value T/F</del>
699		
700		
701		<del>@SPECIAL_CONSIDERATIONS: N/A</del>
702		
	365	
703	366	
704	367	begin
705	368	return Data_Storage.Preds_Complete (Fpln);
706	369	end Preds_Complete;
707	370	
708	371	
709	372	procedure Put_Preds_Complete (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type;
710	373	Data : in Boolean) is
711		
712		<del>@DESCRIPTION: Stores the flag indicating the completion of the input</del>
713		<del>primary flight plan predictions.</del>
714		
715		
716		<del>@UNITS: Data T/F</del>
717		
718		
719		<del>@SPECIAL_CONSIDERATIONS: N/A</del>
720		
	374	
721	375	
722	376	begin
723	377	Data_Storage.Preds_Complete (Fpln) := Data;
724	378	end Put_Preds_Complete;
725	379	
726	380	function Strategic_Preds_Stale (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type) return Boolean is
727		
728		<del>@DESCRIPTION: True indicates that strategic primary flight plan predictions are invalid due</del>
729		<del>to having gone stale.</del>
730		<del>False indicates that strategic primary flight plan predictions are not stale. Note</del>
731		<del>that a return of False does not imply validity of predictions.</del>
732		
733		
734		<del>@UNITS: Return Value T/F</del>
735		
736		



Mode: All Lines

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_LGB\_INTR.STB

1	1	--
2	2	-- STUB FILE
3	3	--
4	4	-- CTP_A340S1A_PERF_BND_PUT_BK_DAT_LGB_INTR.STB
5	5	--
6	6	-- REASON FOR STUBBING : The procedure Requestlgb AND Releaselgb in the package body Perf_Lgb_Interface_Mgr_Pkg
7	7	-- have been stubbed out to aid CTP testing.
8	8	--
9		<del>DATA_RIGHTS: HONEYWELL CONFIDENTIAL &amp; PROPRIETARY</del>
10		<del>THIS WORK CONTAINS VALUABLE CONFIDENTIAL AND PROPRIETARY</del>
11		<del>INFORMATION. DISCLOSURE, USE OR REPRODUCTION OUTSIDE OF</del>
12		<del>HONEYWELL INTERNATIONAL, INC. IS PROHIBITED EXCEPT AS</del>
13		<del>AUTHORIZED IN WRITING. THIS UNPUBLISHED WORK IS PROTECTED BY</del>
14		<del>THE LAWS OF THE UNITED STATES AND OTHER COUNTRIES. IN THE</del>
15		<del>EVENT OF PUBLICATION, THE FOLLOWING NOTICE SHALL APPLY:</del>
16		<del>COPR. 2010 HONEYWELL INTERNATIONAL, INC. ALL RIGHTS RESERVED.</del>
17		<del>---</del>
18		<del>---</del>
19		<del>File Name: CTP_A340S1A_PERF_BND_PUT_BK_DAT_LGB_INTR.STB</del>
	9	-- Original File Name: Perf_Lgb_Interface_Mgr_Pkg.ada
20	10	--
21	11	--
22	12	with Apex_Process_Pkg; -- common
23	13	with Apex_Types_Pkg; -- common
24	14	with Common_Lgb; -- common
25	15	with Flight_Pln_Hdr_Types; -- common
26	16	with Fmcs_Fp_Guid_Btypes; -- common
27	17	with Perf_Task_Priority; -- shared fmf types
28	18	with Perf_Background_Dpkg; -- perf
29	19	with Portable_Types_Pkg; -- common
30	20	with Ctp_Perf_Bkgnd_Put_Bk_Data;
31	21	
32	22	package body Perf_Lgb_Interface_Mgr_Pkg is
33		<del>---</del>
34		<del>@DESCRIPTION: This package contains procedures to be used by Perf_Background</del>
35		<del>when accessing the Lateral Guidance Buffer (LGB). The procedures</del>
36		<del>in this package replace similarly named procedures in Common_Lgb.</del>
37		<del>---</del>
38		<del>---</del>
	23	
39	24	
40	25	procedure Requestlgb (Lgb_Operation : in Flight_Pln_Hdr_Types.Type_Of_Access;

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_LGB\_INTR.STB (continued)

41	26	Route_Id : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type) is
42		<del>-----+-----</del>
43		<del>-----+-----</del> @DESCRIPTION: This procedure interfaces with the guidance buffer for requesting
44		<del>-----+-----</del> the LGB and in the process boosts Perf BK's priority. This is to prevent a heartbeat
45		<del>-----+-----</del> in other higher priority tasks while PerfBK is in the guidance buffer. What's at stake here
46		<del>-----+-----</del> is that Perf BK must have a higher priority than EFIS BK while in the guidance buffer
47		<del>-----+-----</del> since EFIS BK does not access the GB and can starve Perf BK, which causes a domino
48		<del>-----+-----</del> effect.
49		<del>-----+-----</del>
50		<del>-----+-----</del>
51		<del>-----+-----</del> @SPECIAL_CONSIDERATIONS:
52		<del>-----+-----</del> Perf should not be in the guidance buffer any more than necessary so as not to disturb
53		<del>-----+-----</del> the system intent of making PERF preds strictly a background task.
54		<del>-----+-----</del>
	27	
55	28	
56	29	Status_Code : Apex_Types_Pkg.Status_Code_Type;
57	30	
58	31	begin
59	32	
60	33	CTP_PERF_BKGND_PUT_BK_DATA.Requestlgb_Exec := True;
61	34	
62	35	end Requestlgb;
63	36	
64	37	
65	38	procedure Releaselgb (Rte : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type) is
66		<del>-----+-----</del>
67		<del>-----+-----</del> @DESCRIPTION : This procedure interfaces with the guidance buffer for releasing the LGB and
68		<del>-----+-----</del> restoring Perf BK's priority to its normal priority.
69		<del>-----+-----</del>
70		<del>-----+-----</del>
71		<del>-----+-----</del> @SPECIAL_CONSIDERATIONS :
72		<del>-----+-----</del> None
73		<del>-----+-----</del>
	39	
74	40	
75	41	Status_Code : Apex_Types_Pkg.Status_Code_Type;
76	42	
77	43	begin
78	44	
79	45	CTP_PERF_BKGND_PUT_BK_DATA.Releaselgb_Exec := True;
80	46	
81	47	end Releaselgb;
82	48	

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_LGB\_INTR.STB (continued)

83	49	
84	50	procedure Update_Legindex (Fpln_Leg_Is_In : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
85	51	Fpln_To_Search : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
86	52	Input_Leg_Index : in Flight_Pln_Leg_Types.Leg_Index_Type;
87	53	Found_Leg_Idx : out Flight_Pln_Leg_Types.Leg_Index_Type;
88	54	Lgb_Process_Id : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type := Fmcs_Fp_Guid_Btypes.Perf_B
		» g) is
89	55	
90	56	begin
91	57	
92	58	Found_Leg_Idx := Ctp_Perf_Bkgnd_Put_Bk_Data.Chk_Idx;
93	59	
94	60	end Update_Legindex;
95	61	
96	62	
97	63	function Valid_Legindex (Index : in Flight_Pln_Leg_Types.Leg_Index_Type;
98	64	Fpln : in Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
99	65	Lgb_Process_Id : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type := Fmcs_Fp_Guid_Btypes.Perf_Bg)
100	66	return Boolean is
101	67	begin
102	68	
103	69	return TRUE;
104	70	
105	71	end Valid_Legindex;
106	72	
107	73	procedure Full_Perf_Bg_Lgb_Release is
108		<del>-----+-----</del>
109		<del>-----+-----</del> @DESCRIPTION : This procedure interfaces with the guidance buffer for releasing the LGB for all flight plans
110		<del>-----+-----</del> for Perf Background processing. This is protection code in case Perf has accidentally held ont
		<del>» e</del>
111		<del>-----+-----</del> a flight plan during processing but hasn't released it. Deadline reset protection.
112		<del>-----+-----</del>
113		<del>-----+-----</del>
114		<del>-----+-----</del> @SPECIAL_CONSIDERATIONS :
115		<del>-----+-----</del> The LGB for the flight plan does not have to be reserved. This is protection for Perf accidentally keepi
		<del>» ng it</del>
116		<del>-----+-----</del> reserved. This procedure will release all the flight plans at the end of background predictions
117		<del>-----+-----</del>
	74	
118	75	
119	76	begin
120	77	
121	78	For Rte in Portable_Types_Pkg.Integer_32 range 1 .. Fmcs_Fp_Guid_Btypes.Max_Total_Routes loop
122	79	Common_Lgb.ReleaseIgb (Fmcs_Fp_Guid_Btypes.Perf_Bg, Rte);

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_LGB\_INTR.STB (continued)

123	80	end loop;
124	81	
125	82	end Full_Perf_Bg_Lgb_Release;
126	83	end Perf_Lgb_Interface_Mgr_Pkg;

Mode: All Lines

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_OPS\_DELTA\_TIME.STB

1	1	--
2	2	-- STUB FILE
3	3	--
4	4	-- CTP_A340S1A_PERF_BND_PUT_BK_DAT_OPS_DELTA_TIME.STB
5	5	--
6	6	-- REASON FOR STUBBING : The function Ops_Delta_Time in the package body Ops_Timer_Pkg
7	7	-- is stubbed out to aid for CTP testing.
8	8	--
9		<del>--- </del>
10		<del>---  DATA_RIGHTS: HONEYWELL CONFIDENTIAL &amp; PROPRIETARY</del>
11		<del>---  THIS WORK CONTAINS VALUABLE CONFIDENTIAL AND PROPRIETARY</del>
12		<del>---  INFORMATION. DISCLOSURE, USE OR REPRODUCTION OUTSIDE OF</del>
13		<del>---  HONEYWELL INTERNATIONAL, INC. IS PROHIBITED EXCEPT AS</del>
14		<del>---  AUTHORIZED IN WRITING. THIS UNPUBLISHED WORK IS PROTECTED BY</del>
15		<del>---  THE LAWS OF THE UNITED STATES AND OTHER COUNTRIES. IN THE</del>
16		<del>---  EVENT OF PUBLICATION, THE FOLLOWING NOTICE SHALL APPLY:</del>
17		<del>---  COPR. 2004 HONEYWELL INTERNATIONAL, INC. ALL RIGHTS RESERVED.</del>
18		<del>--- </del>
	9	-- Original File Name: Ops_Timer_Pkg.ada
19	10	
20	11	with Portable_Types_Pkg;
21	12	use Portable_Types_Pkg;
22	13	--with Apex_Types_Pkg;
23	14	with Apex_Partition_Pkg;
24	15	with Fmcs_Partition_Data_Pkg;
25	16	with Fcs_Date_Time_Pkg;
26	17	
27	18	package body Ops_Timer_Pkg is
28	19	
29		<del>--- </del>
30		<del>---  @DESCRIPTION: Implementation of GPC delta timers.</del>
31		<del>--- </del>
32		<del>--- </del>
33	20	
34		
35	21	Ops_Time : Fcs_Date_Time_Pkg.Time_Rec renames Fmcs_Partition_Data_Pkg.Ops_Time;
36	22	
37	23	Max_Time_Value : constant Portable_Types_Pkg.Integer_32 := 16#7FFFFFFF#;
38	24	
39	25	function Ops_Delta_Time
40	26	(Start_Time : in Portable_Types_Pkg.Integer_32; Delta_Time : in Portable_Types_Pkg.Integer_32) return Bo
		» olean is

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_OPS\_DELTA\_TIME.STB (continued)

41	27	
42		<del>-----+  </del>
43		<del>-----</del>
44		<del>----- DESCRIPTION: This procedure will indicate when the users specified time</del>
45		<del>----- duration has transpired. The using application must provide the time</del>
46		<del>----- to begin counting from along with the time duration that must occur.</del>
47		<del>-----</del>
48		<del>----- SHARED_DATA: Both parameters that are passed in to this function has to be positive numbers.</del>
49		<del>-----</del>
50		<del>----- SPECIAL_CONSIDERATIONS: N/A</del>
51		<del>-----</del>
52		<del>-----+  </del>
	28	
53	29	type Integer32 is new Integer_32;
54	30	
55	31	Delta_Time_Expired : Boolean;
56	32	
57	33	begin
58	34	
59	35	return (Start_Time > 0);
60	36	
61	37	end Ops_Delta_Time;
62	38	
63	39	
64	40	
65	41	function Ops_Return_Delta (Start_Time : in Portable_Types_Pkg.Integer_32) return Portable_Types_Pkg.Integer_32 is
66	42	
67		<del>-----+  </del>
68		<del>----- SOURCE: FMFSDD; FMCS_12_21001684  </del>
69		<del>----- DESCRIPTION: This function takes as input parameter a start time and</del>
70		<del>----- returns the delta time elapsed since the start time.</del>
71		<del>-----</del>
72		<del>----- SHARED_DATA: N/A</del>
73		<del>-----</del>
74		<del>----- SPECIAL_CONSIDERATIONS: N/A</del>
75		<del>-----</del>
76		<del>-----+  </del>
	43	
77	44	type Integer32 is new Integer_32;
78	45	
79	46	Delta_Time : Portable_Types_Pkg.Integer_32;
80	47	
81	48	begin
82	49	



File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_OPS\_DELTA\_TIME.STB (continued)

83	50	-- Handle rollover of the GPC counter from largest positive number to zero.
84	51	-- Has the Delta time period occurred?
85	52	
86	53	if (Ops_Time.Gpc_Time >= Start_Time) then
87	54	Delta_Time := Ops_Time.Gpc_Time - Start_Time;
88	55	else
89		<del>-----</del>
90		<del>----- The GPC counter has rolled over so remember that when a counter goes</del>
91		<del>----- from max value to zero it takes a count (a clock). That is why the</del>
92		<del>----- 1 is in the equation.</del>
93		<del>-----</del>
	56	
94	57	Delta_Time := Max_Time_Value - Start_Time + Ops_Time.Gpc_Time + 1;
95	58	end if;
96	59	
97	60	return Delta_Time;
98	61	
99	62	end Ops_Return_Delta;
100	63	
101	64	
102	65	function Gpc2Ms (Gpc_Time : in Portable_Types_Pkg.Integer_32) return Portable_Types_Pkg.Integer_32 is
103	66	
104		<del>-----+-----</del>
105		<del>----- SOURCE: FMFSDD; FMCS_12_21001683 +-----</del>
106		<del>----- DESCRIPTION: This routine takes a GPC time as input and converts it to</del>
107		<del>----- milliseconds.</del>
108		<del>----- SHARED_DATA: N/A</del>
109		<del>-----</del>
110		<del>----- SPECIAL_CONSIDERATIONS: N/A</del>
111		<del>-----</del>
112		<del>-----+-----</del>
	67	
113	68	
114	69	begin
115	70	
116	71	return Portable_Types_Pkg.Integer_32 ((Portable_Types_Pkg.Float_32 (Gpc_Time) *
117	72	Gpc_Time_Constant / Portable_Types_Pkg.Float_32 (1000)));
118	73	
119	74	end Gpc2Ms;
120	75	
121	76	
122	77	function Subtract_Time (Start_Time : in Portable_Types_Pkg.Integer_32;
123	78	Delta_Value : in Portable_Types_Pkg.Integer_32) return Portable_Types_Pkg.Integer_32 is
124	79	

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_OPS\_DELTA\_TIME.STB (continued)

125	80	
126		<del>-----!</del>
127		<del>-----  @DESCRIPTION: This function returns the value that represents the time at a given</del>
128		<del>-----  Delta_Value before the input Start_Time. Note that it will never return</del>
129		<del>-----  a negative time value. If (Start_Time - Delta_Value) would result in a</del>
130		<del>-----  negative number, the result is wrapped around to count down from the</del>
131		<del>-----  end of the time range.</del>
132		<del>----- </del>
133		<del>----- </del>
134		<del>-----  @SPECIAL_CONSIDERATIONS: N/A</del>
135		<del>----- </del>
	81	
136	82	
137	83	begin -- Subtract_Time
138	84	
139	85	if (Start_Time - Delta_Value >= 0) then
140	86	return (Start_Time - Delta_Value);
141	87	else
142	88	return (Max_Time_Value - Delta_Value + Start_Time + 1);
143	89	end if;
144	90	
145	91	end Subtract_Time;
146	92	
147	93	end Ops_Timer_Pkg;

Mode: All Lines

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PERF\_BFR.STB

1	1	--
2	2	-- STUB FILE
3	3	--
4	4	-- CTP_A340S1A_PERF_BND_PUT_BK_DAT_PERF_BFR.STB
5	5	--
6	6	-- REASON FOR STUBBING : The procedure Putperfleg and function Getperfleg in the package body Perf_Buffer
7	7	-- are stubbed out to aid for CTP testing.
8	8	--
9		<del>DATA_RIGHTS: HONEYWELL CONFIDENTIAL &amp; PROPRIETARY</del>
10		<del>THIS WORK CONTAINS VALUABLE CONFIDENTIAL AND PROPRIETARY</del>
11		<del>INFORMATION. DISCLOSURE, USE OR REPRODUCTION OUTSIDE OF</del>
12		<del>HONEYWELL INTERNATIONAL, INC. IS PROHIBITED EXCEPT AS</del>
13		<del>AUTHORIZED IN WRITING. THIS UNPUBLISHED WORK IS PROTECTED BY</del>
14		<del>THE LAWS OF THE UNITED STATES AND OTHER COUNTRIES. IN THE</del>
15		<del>EVENT OF PUBLICATION, THE FOLLOWING NOTICE SHALL APPLY:</del>
16		<del>COPR. 2001 HONEYWELL INTERNATIONAL, INC. ALL RIGHTS RESERVED.</del>
17		<del></del>
18		<del></del>
19		<del></del>
20		<del>File Name: CTP_A340S1A_PERF_BND_PUT_BK_DAT_PERF_BFR.STB</del>
	9	-- Original File Name: Perf_Buffer.ada
21	10	--
22	11	--
23	12	with Portable_Types_Pkg; -- common sw
24	13	with Apex_Types_Pkg; -- common
25	14	with Apex_Partition_Pkg; -- common
26	15	with Bite_Fault_Recovery_Tpkg; -- common
27	16	with Bite_Recover_Gpkg; -- common
28	17	with Fmcs_Base_Types; -- common
29	18	with Base_Domain_Services_Tpkg; -- common
30	19	with Io_Interface_Tpkg; -- common
31	20	with Lateral_Path_Type_Tpkg; -- common
32	21	with Ndb_Tpkg; -- common
33	22	with Ops_Data_Retained_Pkg; -- common
34	23	with Perf_Buffer_Types; -- shared fmf types
35	24	with Ctp_Perf_Bkgnd_Put_Bk_Data;
36	25	
37	26	use Fmcs_Base_Types;
38	27	use Base_Domain_Services_Tpkg;
39	28	use Io_Interface_Tpkg;
40	29	use Lateral_Path_Type_Tpkg;
41	30	use Ndb_Tpkg;

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PERF\_BFR.STB (continued)

42	31	use Portable_Types_Pkg;
43	32	
44	33	package body Perf_Buffer is
45		<del>---!</del>
46		<del>---  @DESCRIPTION: The Perf Buffer is a collection of several sets of logical records</del>
47		<del>---  (the exact number is airframe specific) containing Performance/</del>
48		<del>---  Prediction related data. The legs are static and are not linked</del>
49		<del>---  and unlinked as in the LGB. While they cannot be truly deleted</del>
50		<del>---  they can be deactivated by overwriting them with the Initialization</del>
51		<del>---  record.</del>
52		<del>--- </del>
53		<del>---  The Perf Buffer must be initialized on power up by Init. It can be</del>
54		<del>---  restored from checkpointed data after a cold or warm start by calling</del>
55		<del>---  Restart. Legacy systems included functions which requested or</del>
56		<del>---  demanded access to the Buffer. These functions are no longer needed.</del>
57		<del>---  Access to the route is automatically controlled within the individual</del>
58		<del>---  Perf Buffer routine. Thus, if a module in the Flight Planning process</del>
59		<del>---  needed the Topelb leg from the Activecfp route, you would simply do</del>
60		<del>---  the following:</del>
61		<del>--- </del>
62		<del>---  TOC_leg := Getperflleg (Flight_planning, Activecfp, Topelb);</del>
63		<del>--- </del>
64		<del>---  You would not have to request or release the route (this is done</del>
65		<del>---  inside the Getperflleg function). Nor do you have to request or</del>
66		<del>---  demand access. This is true for all Perf Buffer functions.</del>
67		<del>--- </del>
68		<del>---!</del>
	34	
69	35	
70	36	Max_Routes : Perf_Route_Type;
71	37	
72	38	type Access_Type is (Read, Write);
73	39	
74	40	-- The following type is used instead of Boolean because HADS does
75	41	-- not handle boolean array indexes correctly.
76	42	type Ping_Pong_Type is (Buffer1, Buffer2);
77	43	for Ping_Pong_Type use (Buffer1 => 0, Buffer2 => 1);
78	44	
79	45	type Perf_Route_Ping_Pong is array (Ping_Pong_Type) of Perf_Route;
80	46	type Perf_Has_Updated_Ping_Pong is array (Ping_Pong_Type) of Boolean;
81	47	type Ping_Pong_Leg_Array is array (Perf_Leg_Type range 1 .. Perf_Leg_Type'Last) of Boolean;
82	48	type Corruption_Array is array (Perf_Process_Val range Perf_Process_Val'First .. Perf_Process_Val'Last) of Ping_Pong
		» _Leg_Array;
83	49	type Data_Corruption_Type is array (Ping_Pong_Type) of Corruption_Array;

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PERF\_BFR.STB (continued)

```

84      50      type Users_Read_Array is array (Perf_Process_Val range Perf_Process_Val'First .. Perf_Process_Val'Last) of Ping_Pong
      » _Type;
85      51
86      52      type Perf_Route_Ping_Pong_Rec is
87      53          record
88      54              Pb_Data : Perf_Route_Ping_Pong;
89      55              Perf_Has_Updated_Route : Perf_Has_Updated_Ping_Pong;
90      56              Current_Read : Ping_Pong_Type;
91      57              Writer : Perf_Process_Val;
92      58              Guard : Data_Corruption_Type;
93      59              Users_Read : Users_Read_Array;
94      60          end record;
95      61
96      62      type Perf_Route_Ping_Pong_Array is array (Perf_Route_Type range 1 .. Perf_Route_Type'Last) of Perf_Route_Ping_Pong_R
      » ec;
97      63      Perf_Routes : Perf_Route_Ping_Pong_Array;
98      64      Perf_Buffer_Data_Xmit_Sync : array (Perf_Route_Type range 1 .. Perf_Route_Type'Last) of Boolean;
99      65
100     66      New_Lock_Level : Apex_Partition_Pkg.Lock_Level_Type;
101     67      Comp_Status : Apex_Types_Pkg.Status_Code_Type;
102     68
103     69      --
104     70      -- BITE fault code and error subcodes for PDB errors
105     71      --
106     72      Perf_Db_Fault_Code : constant Portable_Types_Pkg.Byte_Type := 29; -- Fault code designated by BITE
107     73      Pb_Route_Out_Of_Range : constant Portable_Types_Pkg.Byte_Type := 0;
108     74
109     75      type Bite_Recover_Rec is
110     76          record
111     77              User : Perf_Process_Val;
112     78              Route : Perf_Route_Type;
113     79              Max_Routes : Perf_Route_Type;
114     80          end record;
115     81
116     82      Bite_Data : Bite_Recover_Rec;
117     83
118     84      function "not" (Left : in Ping_Pong_Type) return Ping_Pong_Type is
119     85      begin
120     86          if Left = Buffer1 then
121     87              return Buffer2;
122     88          else
123     89              return Buffer1;
124     90          end if;
125     91      end "not";

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PERF\_BFR.STB (continued)

126	92	
127	93	package Call_Bite is new Bite_Recover_Gpkg (Data_Type => Bite_Recover_Rec);
128	94	
129	95	
130	96	function Is_Mcd_u_Pseudo (Pleg : in Perf_Leg_Type) return Boolean is
131		<del>-----+-----</del>
132		<del>-----  @DESCRIPTION: This function indicates whether or not the Pseudo waypoint is one displayed</del>
133		<del>-----  on a MCDU page. CDCK must be notified when these points change.</del>
134		<del>----- -----</del>
135		<del>-----  @SPECIAL_CONSIDERATIONS: If additional pseudo waypoints are added that must be displayed</del>
136		<del>-----  on a MCDU page, they should be added here so that CDCK can be</del>
137		<del>-----  correctly informed when they change.</del>
138		<del>-----+-----</del>
	97	
139	98	begin
140	99	return ((Pleg = Perf_Buffer_Types.Clb_Spdlim) or else (Pleg = Perf_Buffer_Types.Toc) or else
141	100	(Pleg = Perf_Buffer_Types.Tod1) or else (Pleg = Perf_Buffer_Types.Des_Spdlim) or else
142	101	(Pleg = Perf_Buffer_Types.Decelpt) or else (Pleg in Perf_Buffer_Types.Timemark1 .. Perf_Buffer_Types.Timem
		» ark4) or else
143	102	(Pleg in Perf_Buffer_Types.Stpstart1 .. Perf_Buffer_Types.Stepend4));
144	103	end Is_Mcd_u_Pseudo;
145	104	
146	105	
147	106	function No_Of_Routes return Perf_Route_Type is
148		<del>-----+-----</del>
149		<del>-----  @DESCRIPTION: This function is for use primarily for common software. Returns</del>
150		<del>-----  the number of routes defined for the current incarnation of the</del>
151		<del>-----  buffer.</del>
152		<del>----- -----</del>
153		<del>----- </del>
154		<del>-----  @SPECIAL_CONSIDERATIONS: N/A</del>
155		<del>-----+-----</del>
	107	
156	108	begin
157	109	return Max_Routes;
158	110	end No_Of_Routes;
159	111	
160	112	
161	113	
162	114	function Valid_Route (Route : in Perf_Route_Type) return Boolean is
163		<del>-----+-----</del>
164		<del>-----  @DESCRIPTION: This function is for use primarily for common software. Takes a</del>
165		<del>-----  numbered route and returns whether it is defined for this</del>
166		<del>-----  incarnation of the buffer.</del>

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PERF\_BFR.STB (continued)

167		<del>-----+-----</del>
168		<del>-----+ @DATA_RIGHTS: Honeywell ATSD Proprietary</del>
169		<del>-----+-----</del>
170		<del>-----+-----</del>
171		<del>-----+ @SPECIAL_CONSIDERATIONS: N/A</del>
172		<del>-----+-----</del>
	115	
173	116	begin
174	117	return Route in 1 .. Max_Routes;
175	118	end Valid_Route;
176	119	
177	120	
178	121	
179	122	procedure Init (No_Of_Routes : in Perf_Route_Type; Clear_Out_Buffers : in Boolean) is
180		<del>-----+-----</del>
181		<del>-----+ @DESCRIPTION: This is called only once per power up to initialize the buffer.</del>
182		<del>-----+ The parameter is the number of routes that must be defined for</del>
183		<del>-----+ this incarnation of the Perf Buffer.</del>
184		<del>-----+-----</del>
185		<del>-----+ @DATA_RIGHTS: Honeywell ATSD Proprietary</del>
186		<del>-----+-----</del>
187		<del>-----+-----</del>
188		<del>-----+ @SPECIAL_CONSIDERATIONS: Must be called on Power up.</del>
189		<del>-----+-----</del>
	123	
190	124	begin
191	125	
192	126	Max_Routes := No_Of_Routes;
193	127	
194	128	if Clear_Out_Buffers or else not Ops_Data_Retained_Pkg.Ops_Sram_Valid then
195	129	
196	130	-- The initialization abomination below is brought to you courtesy of
197	131	-- the inadequacies of the HADS compiler...enjoy.
198	132	
199	133	for H in 1 .. Perf_Route_Type'Last loop
200	134	for I in Ping_Pong_Type'First .. Ping_Pong_Type'Last loop
201	135	for J in Perf_Process_Val'First .. Perf_Process_Val'Last loop
202	136	for K in 1 .. Perf_Leg_Type'Last loop
203	137	Perf_Routes (H).Guard (I) (J) (K) := False;
204	138	end loop;
205	139	end loop;
206	140	end loop;
207	141	Perf_Routes (H).Users_Read := (others => Buffer1);
208	142	Perf_Routes (H).Writer := Fmcs_Fp_Guid_Btypes.No_Valid_Caller;

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PERF\_BFR.STB (continued)

209	143	Perf_Routes (H).Current_Read := Ping_Pong_Type'First;
210	144	Perf_Routes (H).Pb_Data := (others => (others => Init_Perflegrec));
211	145	Perf_Routes (H).Perf_Has_Updated_Route := (others => False);
212	146	end loop;
213	147	
214	148	-- set the hot-spare transmit sync flag.
215	149	
216	150	Perf_Buffer_Data_Xmit_Sync := (others => True);
217	151	
218	152	end if;
219	153	
220	154	end Init;
221	155	
222	156	
223	157	
224	158	function Readrte (User : in Perf_Process_Val; Route : in Perf_Route_Type) return Ping_Pong_Type is
225		<del>-----+-----</del>
226		<del>-----  @DESCRIPTION: This routine returns the read route for the user.</del>
227		<del>-----+-----</del>
228		<del>-----  @DATA_RIGHTS: Honeywell ATSD Proprietary</del>
229		<del>-----+-----</del>
230		<del>-----  @SPECIAL_CONSIDERATIONS: N/A</del>
231		<del>-----+-----</del>
	159	
232	160	begin
233	161	return Perf_Routes (Route).Users_Read (User);
234	162	end Readrte;
235	163	pragma Inline (Readrte);
236	164	
237	165	
238	166	
239	167	function Writerte (User : in Perf_Process_Val; Route : in Perf_Route_Type) return Ping_Pong_Type is
240		<del>-----+-----</del>
241		<del>-----  @DESCRIPTION: This routine returns the write route for the user.</del>
242		<del>-----+-----</del>
243		<del>-----  @DATA_RIGHTS: Honeywell ATSD Proprietary</del>
244		<del>-----+-----</del>
245		<del>-----  @SPECIAL_CONSIDERATIONS: N/A</del>
246		<del>-----+-----</del>
	168	
247	169	begin
248	170	return not Readrte (User, Route);
249	171	end Writerte;
250	172	pragma Inline (Writerte);



File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PERF\_BFR.STB (continued)

251	173	
252	174	
253	175	
254	176	procedure Requestperf (User : in Perf_Process_Val; Route : in Perf_Route_Type; Type_Of_Access : in Access_Type) is
255		<del>-----+-----</del>
256		<del>-----+ @DESCRIPTION: Does bookkeeping for access to a route.</del>
257		<del>-----+-----</del>
258		<del>-----+ @DATA_RIGHTS: Honeywell ATSD Proprietary</del>
259		<del>-----+-----</del>
260		<del>-----+ @SPECIAL_CONSIDERATIONS: N/A</del>
261		<del>-----+-----</del>
	177	
262	178	
263	179	begin
264	180	if not (Route in 1 .. Max_Routes) then
265	181	Bite_Data.User := User;
266	182	Bite_Data.Route := Route;
267	183	Bite_Data.Max_Routes := Max_Routes;
268	184	
269	185	Call_Bite.Recover (Bite_Data, (Perf_Db_Fault_Code, Pb_Route_Out_Of_Range),
270	186	Bite_Fault_Recovery_Tpkg.Record_And_Raise_Exception);
271	187	end if;
272	188	Perf_Routes (Route).Users_Read (User) := Perf_Routes (Route).Current_Read;
273	189	Perf_Routes (Route).Guard (Readrte (User, Route)) (User) := (others => False);
274	190	if Type_Of_Access = Write then
275	191	Perf_Routes (Route).Writer := User;
276	192	end if;
277	193	end Requestperf;
278	194	
279	195	
280	196	
281	197	function Releaseperf (User : in Perf_Process_Val; Route : in Perf_Route_Type; Leg_No : in Perf_Leg_Type := 0) return
		» Boolean is
282		<del>-----+-----</del>
283		<del>-----+ @DESCRIPTION: Does bookkeeping for release of a route.</del>
284		<del>-----+-----</del>
285		<del>-----+ @DATA_RIGHTS: Honeywell ATSD Proprietary</del>
286		<del>-----+-----</del>
287		<del>-----+ @SPECIAL_CONSIDERATIONS: N/A</del>
288		<del>-----+-----</del>
	198	
289	199	All_False : constant Ping_Pong_Leg_Array := (others => False);
290	200	
291	201	begin

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PERF\_BFR.STB (continued)

```

292 202   if User = Perf_Routes (Route).Writer then
293 203       Perf_Routes (Route).Writer := Fmcs_Fp_Guid_Btypes.No_Valid_Caller;
294 204       Perf_Routes (Route).Pb_Data (Readrte (User, Route)) := Perf_Routes (Route).Pb_Data (Writerte (User, Route));
295 205       Perf_Routes (Route).Perf_Has_Updated_Route (Readrte (User, Route)) :=
296 206           Perf_Routes (Route).Perf_Has_Updated_Route (Writerte (User, Route));
297 207       for I in Perf_Process_Val'First .. Perf_Process_Val'Last loop
298 208           Perf_Routes (Route).Guard (Readrte (User, Route)) (I) :=
299 209               Perf_Routes (Route).Guard (Writerte (User, Route)) (I) and Perf_Routes (Route).Guard (Readrte (User, Route)
» ) (I);
300 210       end loop;
301 211       Perf_Routes (Route).Current_Read := not Perf_Routes (Route).Current_Read;
302 212       return True;
303 213   else
304 214       case Leg_No is
305 215           when 0 =>
306 216               if Perf_Routes (Route).Guard (Readrte (User, Route)) (User) = All_False then
307 217                   return True;
308 218               else
309 219                   return False;
310 220               end if;
311 221       when others =>
312 222               if Perf_Routes (Route).Guard (Readrte (User, Route)) (User) (Leg_No) = False then
313 223                   return True;
314 224               else
315 225                   return False;
316 226               end if;
317 227       end case;
318 228   end if;
319 229   end Releaseperf;
320 230
321 231
322 232
323 233   function Getperfleg (User : in Perf_Process_Val; Route : in Perf_Route_Type; Leg_No : in Perf_Leg_Type) return Perfleg
»   egrec is
324 234   -----+
325 235   -----+ @DESCRIPTION: Returns a perf buffer leg from a specific route.
326 236   -----+
327 237   -----+ @DATA_RIGHTS: Honeywell ATSD Proprietary
328 238   -----+
329 239   -----+
330 240   -----+ @SPECIAL_CONSIDERATIONS: N/A
331 241   -----+
332 234
332 235   Leg : Perflegrec;

```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PERF\_BFR.STB (continued)

333	236	begin
334	237	return Ctp_Perf_Bkgnd_Put_Bk_Data.Perfleg;
335	238	end Getperfleg;
336	239	
337	240	
338	241	
339	242	function Getperfroute (User : in Perf_Process_Val; Route : in Perf_Route_Type) return Perf_Route is
340		<del>-----+-----</del>
341		<del>-----+-----@DESCRIPTION: Returns a specific perf buffer route.</del>
342		<del>-----+-----</del>
343		<del>-----+-----@DATA_RIGHTS: Honeywell ATSD Proprietary</del>
344		<del>-----+-----</del>
345		<del>-----+-----</del>
346		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>
347		<del>-----+-----</del>
	243	
348	244	Rte : Perf_Route;
349	245	begin
350	246	Requestperf (User, Route, Read);
351	247	loop
352	248	Perf_Routes (Route).Users_Read (User) := Perf_Routes (Route).Current_Read;
353	249	Rte := Perf_Routes (Route).Pb_Data (Readrte (User, Route));
354	250	exit when Releaseperf (User, Route);
355	251	Perf_Routes (Route).Guard (Readrte (User, Route)) (User) := (others => False);
356	252	end loop;
357	253	return Rte;
358	254	end Getperfroute;
359	255	
360	256	
361	257	
362	258	procedure Putperfleg (User : in Perf_Process_Val; Route : in Perf_Route_Type; Leg_No : in Perf_Leg_Type; Leg : in Pe » rflegrec) is
363		<del>-----+-----</del>
364		<del>-----+-----@DESCRIPTION: Replaces a perf buffer leg. Sets updated if displayed on MCDU</del>
365		<del>-----+-----</del>
366		<del>-----+-----@DATA_RIGHTS: Honeywell ATSD Proprietary</del>
367		<del>-----+-----</del>
368		<del>-----+-----</del>
369		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>
370		<del>-----+-----</del>
	259	
371	260	Bogus : Boolean;
372	261	begin
373	262	Ctp_Perf_Bkgnd_Put_Bk_Data.Perfleg := Leg;

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PERF\_BFR.STB (continued)

374	263	Ctp_perf_Bkgnd_put_bk_data.Leg_Ctr := Ctp_Perf_Bkgnd_Put_Bk_Data.Leg_Ctr + 1;
375	264	CTP_PERF_BKGND_PUT_BK_DATA.Putperfleg := True;
376	265	end Putperfleg;
377	266	
378	267	
379	268	
380	269	procedure Putperfroute (User : in Perf_Process_Val; Route_No : in Perf_Route_Type; Route : in Perf_Route) is
381		<del>-----+-----</del>
382		<del>----- -----</del>
383		<del>----- -----</del>
384		<del>----- -----</del>
385		<del>----- -----</del>
386		<del>----- -----</del>
387		<del>----- -----</del>
388		<del>-----+-----</del>
	270	
389	271	Bogus : Boolean;
390	272	begin
391	273	Apex_Partition_Pkg.Lock_Preemption (New_Lock_Level, Comp_Status);
392	274	Requestperf (User, Route_No, Write);
393	275	Perf_Routes (Route_No).Pb_Data (Writerte (User, Route_No)) := Route;
394	276	Perf_Routes (Route_No).Guard (Writerte (User, Route_No)) := (others => (others => True));
395	277	Bogus := Releaseperf (User, Route_No);
396	278	Apex_Partition_Pkg.Unlock_Preemption (New_Lock_Level, Comp_Status);
397	279	Perf_Buffer_Data_Xmit_Sync (Route_No) := True;
398	280	end Putperfroute;
399	281	
400	282	
401	283	
402	284	procedure Deleteperfleg (User : in Perf_Process_Val; Route : in Perf_Route_Type; Leg_No : in Perf_Leg_Type) is
403		<del>-----+-----</del>
404		<del>----- -----</del>
405		<del>----- -----</del>
406		<del>----- -----</del>
407		<del>----- -----</del>
408		<del>----- -----</del>
409		<del>----- -----</del>
410		<del>----- -----</del>
411		<del>----- -----</del>
412		<del>----- -----</del>
413		<del>-----+-----</del>
	285	
414	286	begin
415	287	Putperfleg (User, Route, Leg_No, Init_Perflegrec);

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PERF\_BFR.STB (continued)

416	288	end Deleteperfleg;
417	289	
418	290	
419	291	
420	292	procedure Deleteperfroute (User : in Perf_Process_Val; Route_No : in Perf_Route_Type) is
421		<del>-----+-----</del>
422		<del>-----  @DESCRIPTION: Overwrites a perf route with a route filled with legs set to</del>
423		<del>-----  the initialization constant for a perf leg. If any leg in the</del>
424		<del>-----  route needs special initialization after deletion it is left up</del>
425		<del>-----  to the user to do so.</del>
426		<del>----- -----</del>
427		<del>-----  @DATA_RIGHTS: Honeywell ATSD Proprietary</del>
428		<del>----- -----</del>
429		<del>----- -----</del>
430		<del>-----  @SPECIAL_CONSIDERATIONS: N/A</del>
431		<del>-----+-----</del>
	293	
432	294	begin
433	295	Putperfroute (User, Route_No, (others => Init_Perflegrec));
434	296	end Deleteperfroute;
435	297	
436	298	
437	299	
438	300	procedure Copyperfroute (User : in Perf_Process_Val; From_Route, To_Route : in Perf_Route_Type) is
439		<del>-----+-----</del>
440		<del>-----  @DESCRIPTION: Copies one route onto another.</del>
441		<del>----- -----</del>
442		<del>-----  @DATA_RIGHTS: Honeywell ATSD Proprietary</del>
443		<del>----- -----</del>
444		<del>----- -----</del>
445		<del>-----  @SPECIAL_CONSIDERATIONS: N/A</del>
446		<del>-----+-----</del>
	301	
447	302	begin
448	303	Putperfroute (User, To_Route, Getperfroute (User, From_Route));
449	304	end Copyperfroute;
450	305	
451	306	
452	307	
453	308	procedure Restart is
454		<del>-----+-----</del>
455		<del>-----  @DESCRIPTION: Called only once during a warm start or cold start. It is not</del>
456		<del>-----  necessary to call this routine during initialization.</del>
457		<del>----- -----</del>

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PERF\_BFR.STB (continued)

458		<del>-----+-----</del>	<del>@DATA_RIGHTS: Honeywell ATSD Proprietary</del>
459		<del>-----+-----</del>	
460		<del>-----+-----</del>	
461		<del>-----+-----</del>	<del>@SPECIAL_CONSIDERATIONS: Should only be called on a warm or cold start</del>
462		<del>-----+-----</del>	
	309		
463	310		begin
464	311		for I in 1 .. Max_Routes loop
465	312		Perf_Routes (I).Writer := Fmcs_Fp_Guid_Btypes.No_Valid_Caller;
466	313		Perf_Routes (I).Pb_Data (not Perf_Routes (I).Current_Read) := Perf_Routes (I).Pb_Data (Perf_Routes (I).Current_R
			» ead);
467	314		end loop;
468	315		Perf_Buffer_Data_Xmit_Sync := (others => True);
469	316		end Restart;
470	317		
471	318		
472	319		
473	320		function Get_Vert_Seq (Data : Perf_Buffer_Types.Perf_Leg_Type) return Flight_Pln_Hdr_Types.Vrtseqset_Enu is
474		<del>-----+-----</del>	
475		<del>-----+-----</del>	<del>@DESCRIPTION: This procedure provides a translation between the vertical sequence set</del>
476		<del>-----+-----</del>	<del>and the Perf buffer leg types for Airbus. Given an Airbus Pseudo waypoint</del>
477		<del>-----+-----</del>	<del>Perf_Leg_Type constant, it will return the appropriate vertical sequence</del>
478		<del>-----+-----</del>	<del>set element. This functionality is needed since the Vertical sequence set</del>
479		<del>-----+-----</del>	<del>is based on MD11 pseudo waypoints and the Airbus pseudo waypoints need to be</del>
480		<del>-----+-----</del>	<del>mapped to it.</del>
481		<del>-----+-----</del>	
482		<del>-----+-----</del>	<del>@DATA_RIGHTS: Honeywell ATSD Proprietary</del>
483		<del>-----+-----</del>	
484		<del>-----+-----</del>	
485		<del>-----+-----</del>	<del>@SPECIAL_CONSIDERATIONS:-</del>
486		<del>-----+-----</del>	<del>This procedure will need to be updated when the vertical sequence set or</del>
487		<del>-----+-----</del>	<del>airbus pseudo waypoint constants are updated.</del>
488		<del>-----+-----</del>	
	321		
489	322		Temp : Flight_Pln_Hdr_Types.Vrtseqset_Enu;
490	323		
491	324		
492	325		begin
493	326		case Data is
494	327		when Perf_Buffer_Types.Strtclb2 =>
495	328		Temp := Flight_Pln_Hdr_Types.Strtclb2;
496	329		when Perf_Buffer_Types.Spdchgpt =>
497	330		Temp := Flight_Pln_Hdr_Types.Spdchgpt;
498	331		when Perf_Buffer_Types.Clb_Spdlim =>

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PERF\_BFR.STB (continued)

```
499 332 Temp := Flight_Pln_Hdr_Types.Clbspdlm;
500 333 when Perf_Buffer_Types.Des_Spdlim =>
501 334 Temp := Flight_Pln_Hdr_Types.Desspdlim;
502 335 when Perf_Buffer_Types.Level1 =>
503 336 Temp := Flight_Pln_Hdr_Types.Level1;
504 337 when Perf_Buffer_Types.Toc =>
505 338 Temp := Flight_Pln_Hdr_Types.Toc;
506 339 when Perf_Buffer_Types.Clrncealt =>
507 340 Temp := Flight_Pln_Hdr_Types.Clralt;
508 341 when Perf_Buffer_Types.Predtoalt =>
509 342 Temp := Flight_Pln_Hdr_Types.Altintcp;
510 343 when Perf_Buffer_Types.Stpstart1 =>
511 344 Temp := Flight_Pln_Hdr_Types.Stpstart1;
512 345 when Perf_Buffer_Types.Stpstart2 =>
513 346 Temp := Flight_Pln_Hdr_Types.Stpstart2;
514 347 when Perf_Buffer_Types.Stpstart3 =>
515 348 Temp := Flight_Pln_Hdr_Types.Stpstart3;
516 349 when Perf_Buffer_Types.Stpstart4 =>
517 350 Temp := Flight_Pln_Hdr_Types.Stpstart4;
518 351 when Perf_Buffer_Types.Stepend1 =>
519 352 Temp := Flight_Pln_Hdr_Types.Stepend1;
520 353 when Perf_Buffer_Types.Stepend2 =>
521 354 Temp := Flight_Pln_Hdr_Types.Stepend2;
522 355 when Perf_Buffer_Types.Stepend3 =>
523 356 Temp := Flight_Pln_Hdr_Types.Stepend3;
524 357 when Perf_Buffer_Types.Stepend4 =>
525 358 Temp := Flight_Pln_Hdr_Types.Stepend4;
526 359 when Perf_Buffer_Types.Strtclb1 =>
527 360 Temp := Flight_Pln_Hdr_Types.Strtclb1;
528 361 when Perf_Buffer_Types.Tod1 =>
529 362 Temp := Flight_Pln_Hdr_Types.Tod1;
530 363 when Perf_Buffer_Types.Tod2 =>
531 364 Temp := Flight_Pln_Hdr_Types.Tod2;
532 365 when Perf_Buffer_Types.Intercept1 =>
533 366 Temp := Flight_Pln_Hdr_Types.Intercept1;
534 367 when Perf_Buffer_Types.Intercept2 =>
535 368 Temp := Flight_Pln_Hdr_Types.Intercept;
536 369 when Perf_Buffer_Types.Decelpt =>
537 370 Temp := Flight_Pln_Hdr_Types.Decelpt;
538 371 when Perf_Buffer_Types.Drftdnpt =>
539 372 Temp := Flight_Pln_Hdr_Types.Drftdnpt;
540 373 when Perf_Buffer_Types.Timemark1 =>
541 374 Temp := Flight_Pln_Hdr_Types.Timemark1;
542 375 when Perf_Buffer_Types.Timemark2 =>
```

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PERF\_BFR.STB (continued)

```

543 376      Temp := Flight_Pln_Hdr_Types.Timemark2;
544 377      when Perf_Buffer_Types.Timemark3 =>
545 378      Temp := Flight_Pln_Hdr_Types.Timemark3;
546 379      when Perf_Buffer_Types.Timemark4 =>
547 380      Temp := Flight_Pln_Hdr_Types.Timemark4;
548 381      when Perf_Buffer_Types.Equitime =>
549 382      Temp := Flight_Pln_Hdr_Types.Equitime;
550 383      when Perf_Buffer_Types.Adsttg1 =>
551 384      Temp := Flight_Pln_Hdr_Types.Adsttg1;
552 385      when Perf_Buffer_Types.Adsttg2 =>
553 386      Temp := Flight_Pln_Hdr_Types.Adsttg2;
554 387      when Perf_Buffer_Types.Adsttg3 =>
555 388      Temp := Flight_Pln_Hdr_Types.Adsttg3;
556 389      when Perf_Buffer_Types.Adsttg4 =>
557 390      Temp := Flight_Pln_Hdr_Types.Adsttg4;
558 391      when Perf_Buffer_Types.Adsttg5 =>
559 392      Temp := Flight_Pln_Hdr_Types.Adsttg5;
560 393      when others =>
561 394      Temp := Flight_Pln_Hdr_Types.Predstart; -- This element not used by Airbus
562 395      end case;
563 396
564 397      return Temp;
565 398
566 399      end Get_Vert_Seq;
567 400
568 401
569 402
570 403      function Get_Pseudo_Wpt (Data : Flight_Pln_Hdr_Types.Vrtseqset_Enu) return Perf_Buffer_Types.Perf_Leg_Type is

```

```

571 404      -----+
572 405      |@DESCRIPTION: This procedure provides a translation between the vertical sequence set
573 406      |and the Perf buffer leg types for Airbus. Given an vertical sequence set
574 407      |element, it will return the appropriate Airbus Pseudo waypoint Perf_Leg_Type
575 408      |constant. This functionality is needed since the Vertical sequence set is
576 409      |based on MD11 pseudo waypoints and the Airbus pseudo waypoints need to be
577 410      |mapped to it.
578 411      |
579 412      |@DATA_RIGHTS: Honeywell ATSD Proprietary
580 413      |
581 414      |
582 415      |@SPECIAL_CONSIDERATIONS:
583 416      |This procedure will need to be updated when the vertical sequence set or
584 417      |airbus pseudo waypoint constants are updated.
585 418      |
586 419      |Entry of a sequence set that does not match an Airbus Pseudo will result in

```



File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PERF\_BFR.STB (continued)

587		<del>a Perf_Buffer_Types.Not_Supported (:= 0) sent back for pseudo waypoint. This</del>
588		<del>value cannot be used to access the performance buffer. Although 0 is a valid</del>
589		<del>value for Perf_Leg_Type, it is not a valid index into the perf buffer arrays.</del>
590		<del>-----+</del>
	404	
591	405	
592	406	Temp : Perf_Buffer_Types.Perf_Leg_Type;
593	407	
594	408	begin
595	409	case Data is
596	410	when Flight_Pln_Hdr_Types.Strtclb2 =>
597	411	Temp := Perf_Buffer_Types.Strtclb2;
598	412	when Flight_Pln_Hdr_Types.Spdchgpt =>
599	413	Temp := Perf_Buffer_Types.Spdchgpt;
600	414	when Flight_Pln_Hdr_Types.Clbspdlm =>
601	415	Temp := Perf_Buffer_Types.Clb_Spdlm;
602	416	when Flight_Pln_Hdr_Types.Desspdlm =>
603	417	Temp := Perf_Buffer_Types.Des_Spdlm;
604	418	when Flight_Pln_Hdr_Types.Level1 =>
605	419	Temp := Perf_Buffer_Types.Level1;
606	420	when Flight_Pln_Hdr_Types.Toc =>
607	421	Temp := Perf_Buffer_Types.Toc;
608	422	when Flight_Pln_Hdr_Types.Clralt =>
609	423	Temp := Perf_Buffer_Types.Clrncealt;
610	424	when Flight_Pln_Hdr_Types.Altintcp =>
611	425	Temp := Perf_Buffer_Types.Predtoalt;
612	426	when Flight_Pln_Hdr_Types.Stpstart1 =>
613	427	Temp := Perf_Buffer_Types.Stpstart1;
614	428	when Flight_Pln_Hdr_Types.Stpstart2 =>
615	429	Temp := Perf_Buffer_Types.Stpstart2;
616	430	when Flight_Pln_Hdr_Types.Stpstart3 =>
617	431	Temp := Perf_Buffer_Types.Stpstart3;
618	432	when Flight_Pln_Hdr_Types.Stpstart4 =>
619	433	Temp := Perf_Buffer_Types.Stpstart4;
620	434	when Flight_Pln_Hdr_Types.Stepend1 =>
621	435	Temp := Perf_Buffer_Types.Stepend1;
622	436	when Flight_Pln_Hdr_Types.Stepend2 =>
623	437	Temp := Perf_Buffer_Types.Stepend2;
624	438	when Flight_Pln_Hdr_Types.Stepend3 =>
625	439	Temp := Perf_Buffer_Types.Stepend3;
626	440	when Flight_Pln_Hdr_Types.Stepend4 =>
627	441	Temp := Perf_Buffer_Types.Stepend4;
628	442	when Flight_Pln_Hdr_Types.Strtclb1 =>
629	443	Temp := Perf_Buffer_Types.Strtclb1;

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PERF\_BFR.STB (continued)

```

630 444      when Flight_Pln_Hdr_Types.Tod1 =>
631 445          Temp := Perf_Buffer_Types.Tod1;
632 446      when Flight_Pln_Hdr_Types.Tod2 =>
633 447          Temp := Perf_Buffer_Types.Tod2;
634 448      when Flight_Pln_Hdr_Types.Intercept1 =>
635 449          Temp := Perf_Buffer_Types.Intercept1;
636 450      when Flight_Pln_Hdr_Types.Intercept =>
637 451          Temp := Perf_Buffer_Types.Intercept2;
638 452      when Flight_Pln_Hdr_Types.Decelpt =>
639 453          Temp := Perf_Buffer_Types.Decelpt;
640 454      when Flight_Pln_Hdr_Types.Drftdnpt =>
641 455          Temp := Perf_Buffer_Types.Drftdnpt;
642 456      when Flight_Pln_Hdr_Types.Timemark1 =>
643 457          Temp := Perf_Buffer_Types.Timemark1;
644 458      when Flight_Pln_Hdr_Types.Timemark2 =>
645 459          Temp := Perf_Buffer_Types.Timemark2;
646 460      when Flight_Pln_Hdr_Types.Timemark3 =>
647 461          Temp := Perf_Buffer_Types.Timemark3;
648 462      when Flight_Pln_Hdr_Types.Timemark4 =>
649 463          Temp := Perf_Buffer_Types.Timemark4;
650 464      when Flight_Pln_Hdr_Types.Equitime =>
651 465          Temp := Perf_Buffer_Types.Equitime;
652 466      when Flight_Pln_Hdr_Types.Adsttg1 =>
653 467          Temp := Perf_Buffer_Types.Adsttg1;
654 468      when Flight_Pln_Hdr_Types.Adsttg2 =>
655 469          Temp := Perf_Buffer_Types.Adsttg2;
656 470      when Flight_Pln_Hdr_Types.Adsttg3 =>
657 471          Temp := Perf_Buffer_Types.Adsttg3;
658 472      when Flight_Pln_Hdr_Types.Adsttg4 =>
659 473          Temp := Perf_Buffer_Types.Adsttg4;
660 474      when Flight_Pln_Hdr_Types.Adsttg5 =>
661 475          Temp := Perf_Buffer_Types.Adsttg5;
662 476      when others =>
663 477          Temp := Perf_Buffer_Types.Not_Supported;
664 478      end case;
665 479
666 480      return Temp;
667 481
668 482  end Get_Pseudo_Wpt;
669 483
670 484
671 485
672 486  function Perf_Has_Updated_Route (User : in Perf_Process_Val; Route : in Perf_Route_Type) return Boolean is

```

673

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PERF\_BFR.STB (continued)

674		<del>-----+-----</del>	<del>@DESCRIPTION: This function returns a boolean value indicating whether performance has</del>
675		<del>-----+-----</del>	<del>written to the supplied route since the last time the routine was called.</del>
676		<del>-----+-----</del>	<del>Calling this function has the side effect of automatically resetting the</del>
677		<del>-----+-----</del>	<del>stored value to false.</del>
678		<del>-----+-----</del>	
679		<del>-----+-----</del>	<del>@DATA_RIGHTS: Honeywell ATSD Proprietary</del>
680		<del>-----+-----</del>	
681		<del>-----+-----</del>	
682		<del>-----+-----</del>	<del>@SPECIAL_CONSIDERATIONS: N/A</del>
683		<del>-----+-----</del>	
	487		
684	488		Return_Value, Bogus : Boolean;
685	489		
686	490		begin
687	491		Apex_Partition_Pkg.Lock_Preemption (New_Lock_Level, Comp_Status);
688	492		
689	493		Requestperf (User, Route, Write);
690	494		Return_Value := Perf_Routes (Route).Perf_Has_Updated_Route (Readrte (User, Route));
691	495		Perf_Routes (Route).Perf_Has_Updated_Route (Writerte (User, Route)) := False;
692	496		Perf_Routes (Route).Guard (Writerte (User, Route)) := (others => (others => True));
693	497		Bogus := Releaseperf (User, Route);
694	498		
695	499		Apex_Partition_Pkg.Unlock_Preemption (New_Lock_Level, Comp_Status);
696	500		
697	501		Perf_Buffer_Data_Xmit_Sync (Route) := True;
698	502		
699	503		return Return_Value;
700	504		
701	505		end Perf_Has_Updated_Route;
702	506		
703	507		
704	508		
705	509		procedure Set_Update_Flag (User : in Perf_Process_Val; Route : in Perf_Route_Type) is
706		<del>-----+-----</del>	
707		<del>-----+-----</del>	<del>@DESCRIPTION: This procedure sets to True the boolean value that indicates whether the</del>
708		<del>-----+-----</del>	<del>supplied route has been written to.</del>
709		<del>-----+-----</del>	
710		<del>-----+-----</del>	<del>@DATA_RIGHTS: Honeywell ATSD Proprietary</del>
711		<del>-----+-----</del>	
712		<del>-----+-----</del>	
713		<del>-----+-----</del>	<del>@SPECIAL_CONSIDERATIONS: N/A</del>
714		<del>-----+-----</del>	
	510		
715	511		Bogus : Boolean;

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PERF\_BFR.STB (continued)

716	512	begin
717	513	Apex_Partition_Pkg.Lock_Preemption (New_Lock_Level, Comp_Status);
718	514	Requestperf (User, Route, Write);
719	515	Perf_Routes (Route).Perf_Has_Updated_Route (Writerte (User, Route)) := True;
720	516	Perf_Routes (Route).Guard (Writerte (User, Route)) := (others => (others => True));
721	517	Bogus := Releaseperf (User, Route);
722	518	
723	519	Apex_Partition_Pkg.Unlock_Preemption (New_Lock_Level, Comp_Status);
724	520	
725	521	Perf_Buffer_Data_Xmit_Sync (Route) := True;
726	522	end Set_Update_Flag;
727	523	
728	524	end Perf_Buffer;
729	525	

Mode: All Lines

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PF\_TO\_CK.STB

1	1	--
2	2	-- STUB FILE
3	3	--
4	4	-- CTP_A340S1A_PERF_BND_PUT_BK_DAT_PF_TO_CK.STB
5	5	--
6	6	-- REASON FOR STUBBING : The following procedures Put_Cdoptalt,Put_Optimum_Step In the package body Perf_To_Cdck_Dp
		» kg
7	7	-- are stubbed out to aid for CTP testing.
8		<del>DATA_RIGHTS: HONEYWELL CONFIDENTIAL &amp; PROPRIETARY</del>
9		<del>THIS WORK CONTAINS VALUABLE CONFIDENTIAL AND PROPRIETARY</del>
10		<del>INFORMATION. DISCLOSURE, USE OR REPRODUCTION OUTSIDE OF</del>
11		<del>HONEYWELL INTERNATIONAL, INC. IS PROHIBITED EXCEPT AS</del>
12		<del>AUTHORIZED IN WRITING. THIS UNPUBLISHED WORK IS PROTECTED BY</del>
13		<del>THE LAWS OF THE UNITED STATES AND OTHER COUNTRIES. IN THE</del>
14		<del>EVENT OF PUBLICATION, THE FOLLOWING NOTICE SHALL APPLY:</del>
15		<del>COPR. 2005 HONEYWELL INTERNATIONAL, INC. ALL RIGHTS RESERVED.</del>
16		
17		
18	8	--
19		<del>File Name: CTP_A340S1A_PERF_BND_PUT_BK_DAT_PF_TO_CK.STB</del>
20		
	9	-- Original File Name: Perf_To_Cdck_Dpkg.ada
21	10	
22	11	with Apex_Types_Pkg; -- common
23	12	with Perf_Ext_Tpkg; -- shared fmf types
24	13	with Perf_Shared_Retained_Dpkg;
25	14	
26	15	with Ctp_Perf_Bkgnd_Put_Bk_Data;
27	16	
28	17	package body Perf_To_Cdck_Dpkg is
29		<del>@DESCRIPTION: This Object Manager handles storing and retrieving various simple</del>
30		<del>interfaces between Perf and CDCK.</del>
31		
32		
33		
34		
35	18	
36		
37	19	
38	20	type Storage_Record_Type is
39	21	record
40	22	Cddisttoland : Io_Interface_Tpkg.Float_32_Valid.Normal;

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PF\_TO\_CK.STB (continued)

41	23	Cdprdtimedst : Perf_Ext_Tpkg.Predtoarr;
42	24	Pgccabrate : Io_Interface_Tpkg.Float_32_Valid.Normal;
43	25	Pgoptstep : Optstep_Tpkg.Optimum_Step_Rec_Array;
44	26	Cdcrzpredto : Perf_Ext_Tpkg.Crzpredtoarr;
45	27	end record;
46	28	
47	29	Storage_Record_Init : constant Storage_Record_Type := (Cddisttoland => (0.0, False),
48	30	Cdprdtimedst => ((0.0, 0.0, False), (0.0, 0.0, False), (0.0,
		» 0.0, False),
49	31	(0.0, 0.0, False), (0.0, 0.0, False)),
50	32	Pgccabrate => (0.0, False),
51	33	Pgoptstep => (others => (0.0, 0.0, 0.0, 0.0, (Optstep_Tpkg.Da
		» shes, 0))),
52	34	Cdcrzpredto => ((0.0, 0.0, False), (0.0, 0.0, False), (0.0, 0
		» .0, False)));
53	35	
54	36	Data_Storage : Storage_Record_Type;
55	37	New_Lock_Level : Apex_Partition_Pkg.Lock_Level_Type;
56	38	Status : Apex_Types_Pkg.Status_Code_Type;
57	39	
58	40	procedure Initialize (Init_Type : in Apex_Partition_Pkg.Operating_Mode_Type) is
59		<del>-----+-----</del>
60		<del>-----+-----@DESCRIPTION: This procedure initializes the interface object manager.</del>
61		<del>-----+-----The parameter defines the type of event prompting the</del>
62		<del>-----+-----initialization.</del>
63		<del>-----+-----</del>
64		<del>-----+-----</del>
65		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>
66		<del>-----+-----</del>
	41	
67	42	
68	43	begin
69	44	null;
70	45	end Initialize;
71	46	
72	47	
73	48	function Cddisttoland return Io_Interface_Tpkg.Float_32_Valid.Normal is
74		<del>-----+-----</del>
75		<del>-----+-----@DESCRIPTION: This procedure retrieves the value of Cddisttoland.</del>
76		<del>-----+-----</del>
77		<del>-----+-----</del>
78		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>
79		<del>-----+-----</del>
	49	

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PF\_TO\_CK.STB (continued)

80	50	
81	51	begin
82	52	return Data_Storage.Cddisttoland;
83	53	end Cddisttoland;
84	54	
85	55	
86	56	procedure Put_Cddisttoland (Data : in Io_Interface_Tpkg.Float_32_Valid.Normal) is
87		<del>-----+-----</del>
88		<del>----- @DESCRIPTION: This procedure stores the value for Cddisttoland.</del>
89		<del>-----+-----</del>
90		<del>-----+-----</del>
91		<del>----- @SPECIAL_CONSIDERATIONS: N/A</del>
92		<del>-----+-----</del>
	57	
93	58	
94	59	begin
95	60	Data_Storage.Cddisttoland := Data;
96	61	end Put_Cddisttoland;
97	62	
98	63	
99	64	function Cdoptalt return Io_Interface_Tpkg.Float_32_Valid.Normal is
100		<del>-----+-----</del>
101		<del>----- @DESCRIPTION: This procedure retrieves the value of Cdoptalt.</del>
102		<del>-----+-----</del>
103		<del>-----+-----</del>
104		<del>----- @SPECIAL_CONSIDERATIONS: N/A</del>
105		<del>-----+-----</del>
	65	
106	66	
107	67	begin
108	68	return Perf_Shared_Retained_Dpkg.Opt_Alt;
109	69	end Cdoptalt;
110	70	
111	71	
112	72	procedure Put_Cdoptalt (Data : in Io_Interface_Tpkg.Float_32_Valid.Normal) is
113		<del>-----+-----</del>
114		<del>----- @DESCRIPTION: This procedure stores the value for Cdoptalt.</del>
115		<del>-----+-----</del>
116		<del>-----+-----</del>
117		<del>----- @SPECIAL_CONSIDERATIONS: N/A</del>
118		<del>-----+-----</del>
	73	
119	74	
120	75	begin





File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PF\_TO\_CK.STB (continued)

162	102	
163	103	
164	104	procedure Put_Optimum_Step ( Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type;
165	105	Data : in Optstep_Tpkg.Optsteprec ) is
166		-----+  @DESCRIPTION: This procedure stores the value for Pgoptstep i.e. the optimum step data.  -----+  -----+  -----+  -- @SPECIAL_CONSIDERATIONS: N/A  -----+
	106	
172	107	
173	108	begin
174	109	Ctp_Perf_Bkgnd_Put_Bk_Data.Opt_Step_Data := Data;
175	110	end Put_Optimum_Step;
176	111	
177	112	
178	113	function Cdprdtimedst (Mode : in Perf_Ext_Tpkg.Cur_Exped_Enum) return Perf_Ext_Tpkg.Predtorec is
179		-----+  @DESCRIPTION: This procedure retrieves the value of Cdprdtimedst.  -----+  -----+  @SPECIAL_CONSIDERATIONS: N/A  -----+
185	114	
186	115	begin
187	116	return Data_Storage.Cdprdtimedst (Mode);
188	117	end Cdprdtimedst;
189	118	
190	119	
191	120	procedure Put_Cdprdtimedst (Mode : in Perf_Ext_Tpkg.Cur_Exped_Enum; Data : in Perf_Ext_Tpkg.Predtorec) is
192		-----+  @DESCRIPTION: This procedure stores the value for Cdprdtimedst.  -----+  -----+  @SPECIAL_CONSIDERATIONS: N/A  -----+
	121	
198	122	
199	123	begin
200	124	Data_Storage.Cdprdtimedst (Mode) := Data;
201	125	end Put_Cdprdtimedst;
202	126	
203	127	function Cdcrzpredto (Mode : in Perf_Ext_Tpkg.Crz_Predto_Enum) return Perf_Ext_Tpkg.Predtorec is

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PF\_TO\_CK.STB (continued)

204		-----+
205		----- @DESCRIPTION: This procedure retrieves the value of Cdcrzpredto.
206		-----+
207		-----+
208		----- @SPECIAL_CONSIDERATIONS: N/A
209		-----+
	128	
210	129	
211	130	begin
212	131	return Data_Storage.Cdcrzpredto (Mode);
213	132	end Cdcrzpredto;
214	133	
215	134	
216	135	procedure Put_Cdcrzpredto (Mode : in Perf_Ext_Tpkg.Crz_Predto_Enum; Data : in Perf_Ext_Tpkg.Predtorec) is
217		-----+
218		----- @DESCRIPTION: This procedure stores the value for Cdcrzpredto.
219		-----+
220		-----+
221		----- @SPECIAL_CONSIDERATIONS: N/A
222		-----+
	136	
223	137	
224	138	begin
225	139	Data_Storage.Cdcrzpredto (Mode) := Data;
226	140	end Put_Cdcrzpredto;
227	141	
228	142	end Perf_To_Cdck_Dpkg;

Mode: All Lines

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PTLGBLEG.STB

1	1	--
2	2	-- STUB FILE
3	3	--
4	4	CTP_A340S1A_PERF_BND_PUT_BK_DAT_PTLGBLEG.STB
5	5	--
6	6	REASON FOR STUBBING: The procedure Putlgbleg in the Common_Lgb PACKAGE has been stubbed out to aid for CTP testi
7	7	» ng.
8	8	--
9		<del>DATA_RIGHTS: HONEYWELL CONFIDENTIAL &amp; PROPRIETARY</del>
10		<del>THIS WORK CONTAINS VALUABLE CONFIDENTIAL AND PROPRIETARY</del>
11		<del>INFORMATION. DISCLOSURE, USE OR REPRODUCTION OUTSIDE OF</del>
12		<del>HONEYWELL INTERNATIONAL, INC. IS PROHIBITED EXCEPT AS</del>
13		<del>AUTHORIZED IN WRITING. THIS UNPUBLISHED WORK IS PROTECTED BY</del>
14		<del>THE LAWS OF THE UNITED STATES AND OTHER COUNTRIES. IN THE</del>
15		<del>EVENT OF PUBLICATION, THE FOLLOWING NOTICE SHALL APPLY:</del>
16		<del>COPR. 2000 HONEYWELL INTERNATIONAL, INC. ALL RIGHTS RESERVED.</del>
17		<del></del>
	8	--
18	9	with Airbus_Lgbm;
19	10	with Ctp_Perf_Bkgnd_Put_Bk_Data;
20	11	
21	12	separate (Common_Lgb)
22	13	procedure Putlgbleg (Process_Id : in Fmcs_Fp_Guid_Btypes.Lgb_Caller_Id_Type;
23	14	Leg_Index : in Flight_Pln_Leg_Types.Leg_Index_Type;
24	15	Lateral_Leg : in Flight_Pln_Leg_Types.Leg_Rec) is
25		<del>-- </del>
26		<del>@DESCRIPTION: This routine writes an entire lateral leg to the Guidance Buffer.</del>
27		<del>Note: This routine only writes a single Flight Plan header and</del>
28		<del>the control data.</del>
29		<del>-- </del>
30		<del>-- </del>
	16	
31	17	
32	18	Dummyfloat : Portable_Types_Pkg.Float_32;
33	19	Lclnextfpn, Lclprevfpn : Flight_Pln_Leg_Types.Leg_Index_Type;
34	20	Local_Leg : Flight_Pln_Leg_Types.Leg_Rec;
35	21	New_Index : Flight_Pln_Leg_Types.Leg_Index_Type;
36	22	New_Lock_Level : Apex_Partition_Pkg.Lock_Level_Type;
37	23	Rte : Fmcs_Fp_Guid_Btypes.Flight_Plan_Id_Type;
38	24	Status : Apex_Types_Pkg.Status_Code_Type;
39	25	

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_PTLGBLEG.STB (continued)

40	26	begin
41	27	Ctp_Perf_Bkgnd_Put_Bk_Data.Out_Gleg := Lateral_Leg;
42	28	end Putlgbleg;
43	29	

Mode: All Lines

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_SYS\_PERF.STB

1	1	--
2	2	-- STUB FILE
3	3	--
4	4	-- CTP_A340S1A_PERF_BND_PUT_BK_DAT_SYS_PERF.STB
5	5	--
6	6	-- REASON FOR STUBBING : The procedures Put_Pcaltnpreds,Put_Pctriptime in the package body Sys_Perf_Interface_Dpkg
7	7	-- are stubbed out to aid for CTP testing.
8	8	--
9		<del>DATA_RIGHTS: HONEYWELL CONFIDENTIAL &amp; PROPRIETARY</del>
10		<del>THIS WORK CONTAINS VALUABLE CONFIDENTIAL AND PROPRIETARY</del>
11		<del>INFORMATION. DISCLOSURE, USE OR REPRODUCTION OUTSIDE OF</del>
12		<del>HONEYWELL INTERNATIONAL, INC. IS PROHIBITED EXCEPT AS</del>
13		<del>AUTHORIZED IN WRITING. THIS UNPUBLISHED WORK IS PROTECTED BY</del>
14		<del>THE LAWS OF THE UNITED STATES AND OTHER COUNTRIES. IN THE</del>
15		<del>EVENT OF PUBLICATION, THE FOLLOWING NOTICE SHALL APPLY:</del>
16		<del>COPR. 2004 HONEYWELL INTERNATIONAL, INC. ALL RIGHTS RESERVED.</del>
17		<del></del>
18		<del></del>
19		<del></del>
20		<del>File Name: CTP_A340S1A_PERF_BND_PUT_BK_DAT_SYS_PERF.STB</del>
21		<del></del>
	9	-- Original File Name: Sys_Perf_Interface_Dpkg.ada
22	10	
23	11	with Apex_Types_Pkg; -- common
24	12	with Fmcs_Partition_Data_Pkg; -- common
25	13	with Perf_Ext_Tpkg; -- perf
26	14	with Ctp_Perf_Bkgnd_Put_Bk_Data;
27	15	
28	16	package body Sys_Perf_Interface_Dpkg is
29		<del>DESCRIPTION: This Object Manager handles storing and retrieving various simple</del>
30		<del>interfaces from Perf to multiple functional areas.</del>
31		<del></del>
32		<del></del>
33		<del></del>
	17	
34	18	
35	19	type Storage_Record_Type is
36	20	record
37	21	Pcaltnpreds : Altn_And_Fuels_Tpkg.Altntperfarr;
38	22	Pctriptime : Altn_And_Fuels_Tpkg.Triptimearr;
39	23	Pflvlaltcstr : Lvlcstrtyp_Tpkg.Lvlcstrtyp;
40	24	Pbupdtcomplt : Boolean;

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_SYS\_PERF.STB (continued)

41	25	Pfpnewdest : Boolean;
42	26	Pgwaitonfaze : Boolean;
43	27	Psperfregst : Boolean;
44	28	Interruptflttst : Boolean;
45	29	Prsegtoc : Boolean;
46	30	end record;
47	31	
48	32	Data_Storage : Storage_Record_Type;
49	33	New_Lock_Level : Apex_Partition_Pkg.Lock_Level_Type;
50	34	Status : Apex_Types_Pkg.Status_Code_Type;
51	35	
52	36	procedure Initialize (Init_Type : in Apex_Partition_Pkg.Operating_Mode_Type) is
53		<del>-----+-----</del>
54		<del>-----+-----@DESCRIPTION: This procedure initializes the interface object manager.</del>
55		<del>-----+-----The parameter defines the type of event prompting the</del>
56		<del>-----+-----initialization.</del>
57		<del>-----+-----</del>
58		<del>-----+-----</del>
59		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>
60		<del>-----+-----</del>
	37	
61	38	
62	39	begin
63	40	null;
64	41	end Initialize;
65	42	
66	43	
67	44	function Pbupdtcomplt return Boolean is
68		<del>-----+-----</del>
69		<del>-----+-----@DESCRIPTION: Retrieves the flag indicating if performance buffer updates have been completed.</del>
70		<del>-----+-----</del>
71		<del>-----+-----@UNITS: Return Value -- T/F</del>
72		<del>-----+-----</del>
73		<del>-----+-----</del>
74		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>
75		<del>-----+-----</del>
	45	
76	46	
77	47	begin
78	48	return Data_Storage.Pbupdtcomplt;
79	49	end Pbupdtcomplt;
80	50	
81	51	
82	52	procedure Put_Pbupdtcomplt (Data : in Boolean) is







File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_SYS\_PERF.STB (continued)

166	93	
167	94	begin
168	95	return Data_Storage.Pgwaitonfaze;
169	96	end Pgwaitonfaze;
170	97	
171	98	
172	99	procedure Put_Pgwaitonfaze (Data : in Boolean) is
173		<del>-----+-----</del>
174		<del>-----+-----@DESCRIPTION: Stores the flag indicating if Perf should wait for flight phase change.</del>
175		<del>-----+-----</del>
176		<del>-----+-----@UNITS: Data     T/F</del>
177		<del>-----+-----</del>
178		<del>-----+-----</del>
179		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>
180		<del>-----+-----</del>
	100	
181	101	
182	102	begin
183	103	Data_Storage.Pgwaitonfaze := Data;
184	104	end Put_Pgwaitonfaze;
185	105	
186	106	
187	107	function Psperfreqst return Boolean is
188		<del>-----+-----</del>
189		<del>-----+-----@DESCRIPTION: Retrieves the flag indicating if Perf should restart predictions.</del>
190		<del>-----+-----</del>
191		<del>-----+-----@UNITS: Return Value     T/F</del>
192		<del>-----+-----</del>
193		<del>-----+-----</del>
194		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>
195		<del>-----+-----</del>
	108	
196	109	
197	110	begin
198	111	--
199	112	-- Check for Dual signaling a resync
200	113	--
201	114	if Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress then
202	115	Data_Storage.Psperfreqst := True;
203	116	end if;
204	117	
205	118	return Data_Storage.Psperfreqst;
206	119	end Psperfreqst;
207	120	

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_SYS\_PERF.STB (continued)

208	121	
209	122	procedure Put_Psperfreqst (Data : in Boolean) is
210		-----+-----
211		----- @DESCRIPTION: Stores the flag indicating if Perf should restart predictions.
212		-----+-----
213		----- @UNITS: Data T/F
214		-----+-----
215		-----+-----
216		----- @SPECIAL_CONSIDERATIONS: N/A
217		-----+-----
	123	
218	124	
219	125	begin
220	126	Data_Storage.Psperfreqst := Data;
221	127	end Put_Psperfreqst;
222	128	
223	129	function Interruptflttst return Boolean is
224		-----+-----
225		----- @DESCRIPTION: Retrieves the flag indicating if Flight Test Bus processing should cease.
226		-----+-----
227		----- @UNITS: Return Value T/F
228		-----+-----
229		-----+-----
230		----- @SPECIAL_CONSIDERATIONS: N/A
231		-----+-----
	130	
232	131	
233	132	begin
234	133	--
235	134	-- Check for Dual signaling a resync
236	135	--
237	136	if Fmcs_Partition_Data_Pkg.Is_Sync_In_Progress then
238	137	Data_Storage.Interruptflttst := True;
239	138	end if;
240	139	
241	140	return Data_Storage.Interruptflttst;
242	141	end Interruptflttst;
243	142	
244	143	
245	144	procedure Put_Interruptflttst (Data : in Boolean) is
246		-----+-----
247		----- @DESCRIPTION: Stores the flag indicating if Flight Test Bus processing should cease.
248		-----+-----
249		----- @UNITS: Data T/F

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_SYS\_PERF.STB (continued)

250		
251		
252		@SPECIAL_CONSIDERATIONS: N/A
253		
	145	
254	146	
255	147	begin
256	148	Data_Storage.Interruptfltst := Data;
257	149	end Put_Interruptfltst;
258	150	
259	151	function Prsegtoc return Boolean is
260		
261		<del>@DESCRIPTION: Retrieves the flag indicating if the aircraft has sequenced the</del>
262		<del>initial top-of-climb.</del>
263		
264		<del>@UNITS: Return Value T/F</del>
265		
266		
267		<del>@SPECIAL_CONSIDERATIONS: N/A</del>
268		
	152	
269	153	
270	154	begin
271	155	return Data_Storage.Prsegtoc;
272	156	end Prsegtoc;
273	157	
274	158	
275	159	procedure Put_Prsegtoc (Data : in Boolean) is
276		
277		<del>@DESCRIPTION: Stores the flag indicating if the aircraft has sequenced the</del>
278		<del>initial top-of-climb.</del>
279		
280		<del>@UNITS: Data T/F</del>
281		
282		
283		<del>@SPECIAL_CONSIDERATIONS: N/A</del>
284		
	160	
285	161	
286	162	begin
287	163	Data_Storage.Prsegtoc := Data;
288	164	end Put_Prsegtoc;
289	165	
290	166	

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_SYS\_PERF.STB (continued)

291	167	function Pcaltnpreds (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type) return Altn_And_Fuels_Tpkg.Altnerfrec is
292		<del>-----+-----</del>
293		<del>-----+-----@DESCRIPTION: Retrieves the alternate predictions data for the requested fpln.</del>
294		<del>-----+-----</del>
295		<del>-----+-----@UNITS: Fpln Active, Secondary, Secsecondary</del>
296		<del>-----+-----Return Value Record type, see type definition</del>
297		<del>-----+-----</del>
298		<del>-----+-----</del>
299		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>
300		<del>-----+-----</del>
	168	
301	169	begin
302	170	
303	171	return Data_Storage.Pcaltnpreds (Fpln);
304	172	end Pcaltnpreds;
305	173	
306	174	
307	175	procedure Put_Pcaltnpreds (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type; Data : in Altn_And_Fuels_Tpkg.Altnerfrec) is
308		<del>-----+-----</del>
309		<del>-----+-----@DESCRIPTION: Stores the alternate predictions data for the fpln that is passed in.</del>
310		<del>-----+-----</del>
311		<del>-----+-----@UNITS: Fpln Active, Secondary, Secsecondary</del>
312		<del>-----+-----Data Record type, see type definition</del>
313		<del>-----+-----</del>
314		<del>-----+-----</del>
315		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>
316		<del>-----+-----</del>
	176	
317	177	begin
318	178	
319	179	Data_Storage.Pcaltnpreds (Fpln) := Data;
320	180	Ctp_Perf_Bkgnd_Put_Bk_Data.Pcaltnpreds_Exec := True;
321	181	end Put_Pcaltnpreds;
322	182	
323	183	
324	184	function Pctriptime (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type) return Altn_And_Fuels_Tpkg.Triptimedata is
325		<del>-----+-----</del>
326		<del>-----+-----@DESCRIPTION: Retrieves the trip data for the requested fpln.</del>
327		<del>-----+-----</del>
328		<del>-----+-----@UNITS: Fpln Active, Secondary, Secsecondary</del>
329		<del>-----+-----Return Value Record type, see type definition</del>
330		<del>-----+-----</del>
331		<del>-----+-----</del>
332		<del>-----+-----@SPECIAL_CONSIDERATIONS: N/A</del>

File: CTP\_A340S1A\_PERF\_BND\_PUT\_BK\_DAT\_SYS\_PERF.STB (continued)

333		<del>-----+</del>
	185	
334	186	
335	187	begin
336	188	return Data_Storage.Pctriptime (Fpln);
337	189	end Pctriptime;
338	190	
339	191	
340	192	procedure Put_Pctriptime (Fpln : in Perf_Ext_Tpkg.Pred_Major_Fp_Type; Data : in Altn_And_Fuels_Tpkg.Triptimedata) is
341		<del>-----+</del>
342		<del>----- @DESCRIPTION: Stores the trip data for the fpln that is passed in.</del>
343		<del>-----+</del>
344		<del>----- @UNITS: Fpln   Active, Secondary, Secsecondary</del>
345		<del>-----           Data   Record type, see type definition</del>
346		<del>-----+</del>
347		<del>-----+</del>
348		<del>----- @SPECIAL_CONSIDERATIONS: N/A</del>
349		<del>-----+</del>
	193	
350	194	
351	195	begin
352	196	Data_Storage.Pctriptime (Fpln) := Data;
353	197	Ctp_Perf_Bkgnd_Put_Bk_Data.Pctriptime_Exec := True;
354	198	end Put_Pctriptime;
355	199	
356	200	end Sys_Perf_Interface_Dpkg;

```

1 *****
2 *
3 * TRACE FILENAME      :  CTP_A340S1A_PERF_BKGND_PUT_BK_DATA.TRT
4 *
5 * MODIFICATION HISTORY :
6 *
7 *          DATE          SCR #          AUTHOR          DESCRIPTION
8 *          =====          =====          =====          =====
9 *          August 18, 2010   52527.78   Zhihong Zhai   Initial Development for A340 S1A S1 plan.
10 *                                     1. Rollover from A320 S1A
11 *                                     CTP_A320_PERF_BKGND_PUT_BK_DATA(TRT;14).
12 *
13 *          Jul 9, 2013     55836.04     Chen Jixing     Update as per A340_55677_04.DRAT on build S1A120 for A
14 *                                     340
15 *                                     Peg 2
16 *                                     1. remove SDD anchor PERF_SDD_07059, PERF_SDD_07063
17 *                                     SRD anchro PERF_SRD_12280, PERF_SRD_12372_INT and
18 *                                     reorder anchors.
19 *          Aug 26, 2014     57231.93     Dun Qing       Update for A340 step2 CR1 on build ST2050.
20 *                                     1. Added PERF_SDD_09025 as per SCR 55961.36(FMS2000, A
21 *                                     3XX)
22 * *****
23 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_0421
24 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_07154
25 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_07394_INT
26 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_07467_INT
27 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_07468_INT
28 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_07469_INT
29 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_07470_INT
30 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_07471_INT
31 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_07472_INT
32 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_07473_INT
33 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_07474_INT
34 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_07475_INT
35 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_07476_INT
36 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_07477_INT
37 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_07479_INT
38 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_07480_INT
39 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_07481
40 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_07482
41 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_07527
42 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_1826
43 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_1831
44 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_2094_INT
45 A340    SDD    A340_PERF_TEST_2443    PERF_SDD_2095_INT

```

46	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_2096
47	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_2109_INT
48	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_2113_INT
49	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_2158_INT
50	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_2159_INT
51	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_2289
52	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_2407_INT
53	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_2414_INT
54	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_2417_INT
55	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_2436
56	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_2631_INT
57	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_2632_INT
58	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3027
59	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3052_INT
60	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3106_INT
61	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3107_INT
62	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3155_INT
63	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3392_INT
64	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3393_INT
65	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3500_INT
66	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3501_INT
67	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3511_INT
68	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3515_INT
69	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3516_INT
70	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3517_INT
71	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3518_INT
72	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3519_INT
73	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3520_INT
74	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3523_INT
75	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3680_INT
76	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3739_INT
77	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3752_INT
78	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_3968_INT
79	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_4220_INT
80	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_4543_INT
81	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_4544_INT
82	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_5587_INT
83	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_5614_DR
84	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_5617_INT
85	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_7018
86	A340	SDD	A340_PERF_TEST_2443	PERF_SDD_09025
87	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_10167_INT
88	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_10253
89	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_10333_INT
90	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_10869
91	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_12092
92	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_12093

93	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_12094
94	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_12095
95	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_1544_A3XX
96	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_2020
97	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_2045
98	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_2051
99	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_2071
100	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_2087_INT
101	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_23172_INT
102	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_23173_INT
103	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_7463
104	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_9993
105	A340	SRD	A340_PERF_TEST_2443	PERF_SRD_9994