HPC Assignment 4 Q3

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1 Question 3:

- Technology and Literature Survey: The goal was to understand the math and the existing literature on the given topic. In summary the math we have decided to proceed is as follows. The method we choose to follow is different from the traditional approached for matrix multiplication in that we employ a nonlinear processing function and reduce the problem to table lookups. This does not require any multiply-add operations instead we use vector quantization methods for similarity. In this way we have eliminated the multiply-ads by introducing a family of quantization functions.
- **Problem formulation:** Let us assume that we have two matrices $A \in \mathbb{R}^{N \times D}$ and $B \in \mathbb{R}^{N \times M}$ with $N >> D \geq M$. We have a time budget τ and now our task becomes to construct three functions g(.), h(.) and f(.) along with constants α , β such that: $\|\alpha f(g(A), h(B)) + \beta AB\|_F < \epsilon(\tau) \|AB\|_F$ for as small an error $\epsilon(\tau)$ as possible. The constants α and β are separate from f(.,.) so that it can produce low bitwidth outputs (for e.g in range [0,255]) even when the entries of AB do not fall in the range.
- **Methodology:** Here we assume the existence of a training set A, whose rows are selected from the same distribution as that of A. The first task that we achieved was product quantization. It consists of the following steps:
 - **Prototype learning:** Here we used offline training where we cluster the rows of \tilde{A} (training set) using K-means algorithm on each of C disjoint subspaces to create C sets of K prototypes.
 - **Encoding function**, g(a): Here we stored the most similar prototype to a as integer indices using $C \log_2(K)$ bits.
 - Table Construction, h(B): Then we precomputed and stored the dot products between b and each prototype in each subspace as C lookup tables of size K.

- **Aggregation,** f(.,.): Finally we used the indices and the tables constructed above to lookup the estimated partial a^Tb in each subspace then aggregate the results across all C subspaces using the sum operation.
- Hash Function Family, g(a): Currently, we are working in developing the hash function. We have constructed the basic hash function to map the vector x to a set of 4 indices using 4 arrays (v^1, v^2, v^3, v^4) with 4 split thresholds. We lookup the split threshold for every node at all the four levels. If the vector element is above the threshold we map it to the index using the equation i = 2i or 2i 1 based on whether it is assigned to left child or the right one in the hashing tree.
- Modifications in the CPU implementation: Used Quick sort for sorting the indices. Flattened the call heirarchy. Refactored out the parallelizable functions.
- Future Goals: Our next goals are to work on the algorithm and code to add next level to the hashing tree, work on optimizing the prototyping method further, using fast 8-bit aggregation for f(.,.). Also, currently, we have implemented the above algorithms in CPU, hence we are also working to scale these to GPU for efficient GPU training(accelerating the clustering process) and inferencing(matrix multiplication).