

# Shubham Gupta

Enrollment No.: IEC2012017

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## Profile

**Objective :** Seeking a challenging position which will enable me to continuously learn, create and innovate while contributing to short and long term goals of the organization effectively using technological and managerial skills.

## Education

<b>2012 to 2016</b>	<b>B.Tech Electronics &amp; Communication</b> Indian Institute of Information Technology Allahabad	<b>9.8 (upto 7<sup>th</sup> sem)</b>
<b>2011 to 2012</b>	<b>Senior Secondary Examination - Class XII</b> St.Paul's Convent Senior Secondary School Ujjain (CBSE)	<b>96%</b>
<b>2009 to 2010</b>	<b>Higher Secondary Examination - Class X</b> St.Paul's Convent Senior Secondary School Ujjain (CBSE)	<b>9.8(CGPA)</b>

## Work Experience

- Software Development Engineer Intern at Infurnia** *Jan'16 to Present*  
Responsible for development of furniture customization platform which will allow the users to instantly customize the living space according to their need. Platform is built mainly on Three.js along with use of HTML, CSS and Angular JS for providing the user interface.

## Accomplishments

- Ranked 1** out of 90 students in Department of Electronics and Communication, IIIT-Allahabad
- C.B.S.E 12<sup>th</sup> Board District Topper (Ujjain District)
- Recipient of **IIITA's Merit Award** and **Merit Incentive Scholarship**
- Qualified for the award of **Scholarship for Higher Education** by virtue of performance **within top 1% of CBSE 12<sup>th</sup> Board**.
- Developer** for website of Electronics Department '**ece.iiita.ac.in**'
- Stood '**First**' in the event '**P.S. I LOVE ELECTRONICS**' organized in 'Aparoksha' Tech-Fest IIITA
- Stood '**Third**' in the event '**A.I. POD**' organized in 'Aparoksha' Tech-Fest IIITA
- Scored a perfect **TEN SGPI** in 'FIRST SEMESTER'

## Projects

### 1. Sample and Hold Circuit Design with Bootstrapped Clock and Transmission Gate Switch using 180nm CMOS Technology (Team Size = 4, Aug'15 - Nov'15)

This project presents S/H circuit design that utilizes clock bootstrapping which allows sampling at peak voltages alongwith transmission gate switch that helps reduce the overall charge injection in design. The designing involves understanding the working of Sample and Hold Circuits and studying numerous S/H circuit designs.

### 2. 8-bit Processor Design and Modeling (Team Size = 5, Jan'15 - May'15)

In this project we worked on 8-bit processor designing using Verilog – HDL. Different modules (like ALU, Controller Unit, and Data Path Unit etc.) were written using Verilog – HDL. Then a top module was designed which consisted of all sub-modules and then results were observed.

### 3. PROTOTYPE OF SMART WHEEL CHAIR (TEAM SIZE = 5, JUL'13 - NOV'13)

We had made prototype of wheel chair which was controlled by an Android App. The person sitting on chair could control the movement and direction of Wheel Chair and there was an Alarm System which can be used in emergency.

- Hardware Used : Arduino Uno, Bluetooth Module ( HC – 06)
- Software Used: Arduino IDE, Android App for Bluetooth connection with Mobile or any other android device.

### 4. VOICE CONTROLLED ASSISTANT USING RASPBERRY PI (TEAM SIZE = 5, JUL'14 - NOV'14)

We had developed a voice controlled assistant using Raspberry Pi on JASPER platform. It had different modules like e-mail, weather, news etc. The system took speech as input and responded to it through speakers. For e.g. if user asked for News, then the system would read out the News Headlines. Similarly if user asked for emails, then it would read out the unread mails.

- Hardware Used: Raspberry Pi Model B+, Mic.
- Software Used: Wheezy Raspbian, Python IDE etc.

## Positions of Responsibilities

- **Vice-Chairperson** IEEE RAS Student Chapter IIIT-A (Apr'15 - present)
- **Core Committee Member** and **Technical Committee Head** for First Workshop on Robotics by IEEE RAS U.P. Section (Mar'15 – Apr'15)
- **Organizer – Riddlonics** in Aparoksha (Techfest) IIIT-A (Mar'15)
- **House Captain** and **Vice-Captain** (Red House) St. Pauls. Con.Sen. Sec School (Jul'10 – Apr' 12)

## Skill Set and Interests

- **Programming Languages** : C, C++, Java(GUI Interface), Javascript
- **Platform** : Windows
- **Web Development** : HTML, CSS, Bootstrap (library), Angular JS

- **Database Management** : MySQL (basic)
- **Software** : Matlab(basic), Arduino IDE, Proteus, Multisim, Ultiboard, Microsoft FrontPage(for website development), Xilinx ISE, CADFeko (Antenna Simulation)
- **Other Courses** : Digital Electronics, Microprocessors, VLSI Design, Embedded Systems, Image Processing

#### Personal Details

- **Date of birth** : 05/04/1994
- **Alternate Number**: 9479854117
- **Nationality** : Indian
- **Permanent Address**: 9 Brijdham Near Shivaji Park Colony, Freeganj, Ujjain (M.P)
- **Current Address** : Room No. 313 Glorious PG Sri Ram Temple Road Ejipura Bangalore 560047

#### DECLARATION:

I hereby declare that the details provided by me in this resume are correct and I have knowingly not omitted/ misrepresented any information. I am aware that the company can use this data for verification purposes and any material inconsistency identified between the details shared above versus actual information would have a bearing on my employment, based upon company policies.

Date:

Time:

Place: Bangalore

Signature: Shubham Gupta