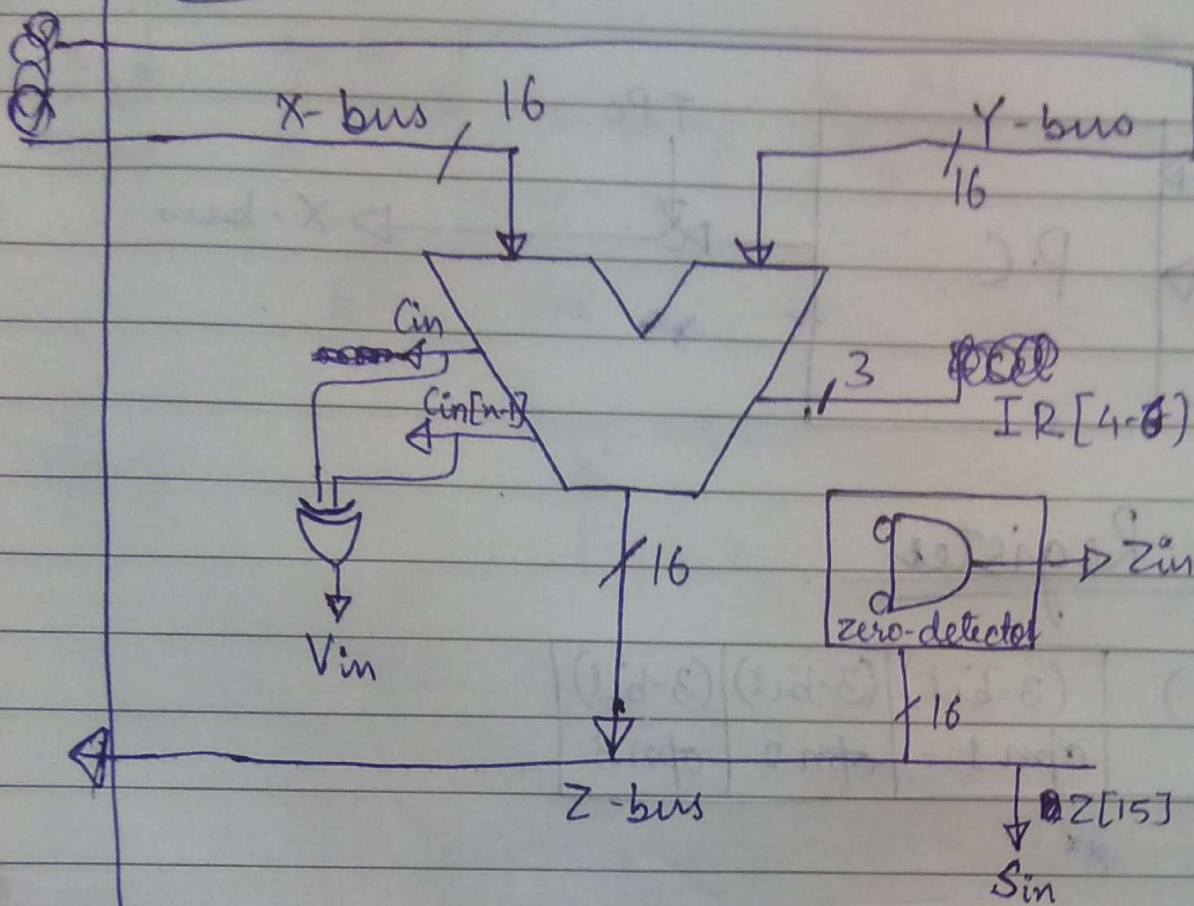
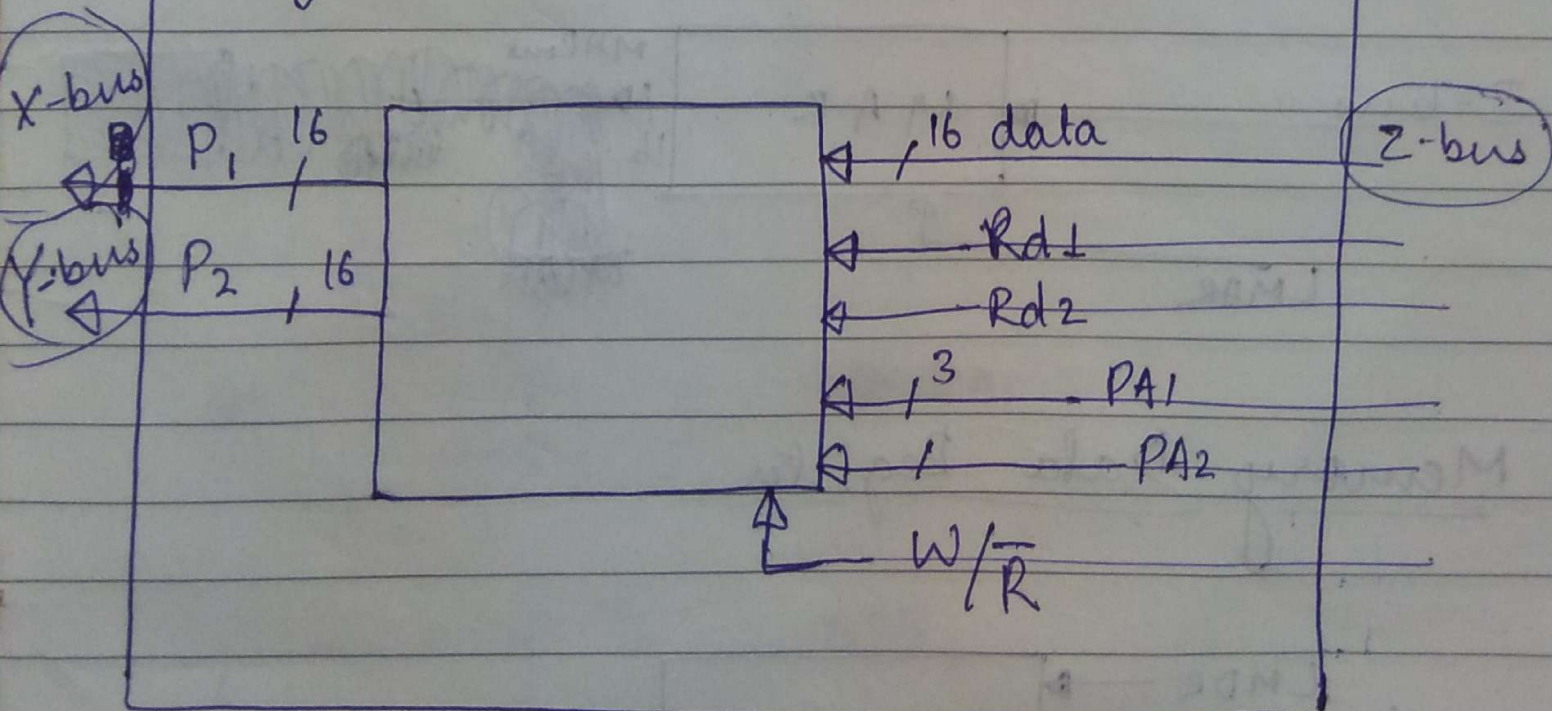


## ALU

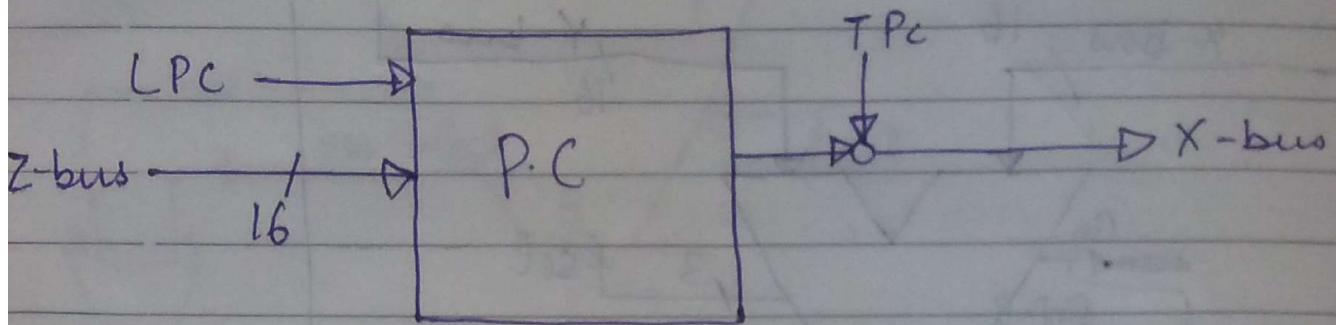


## Register Bank

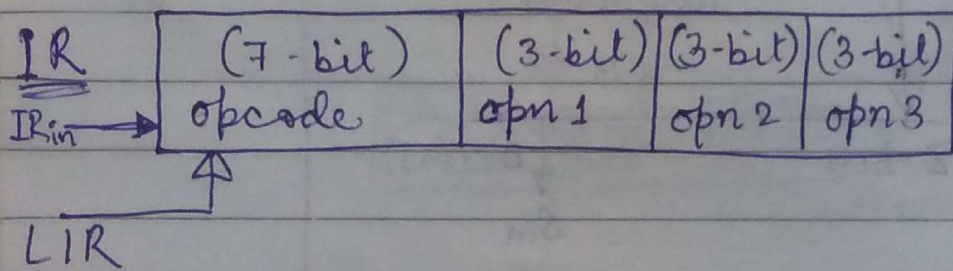




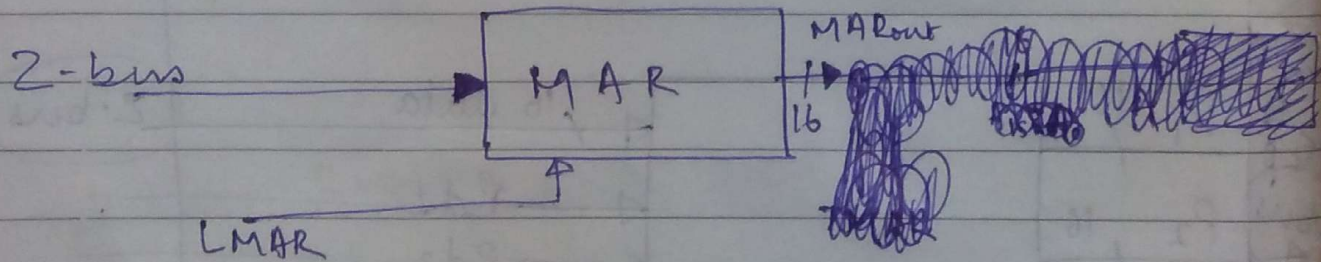
# Program Counter



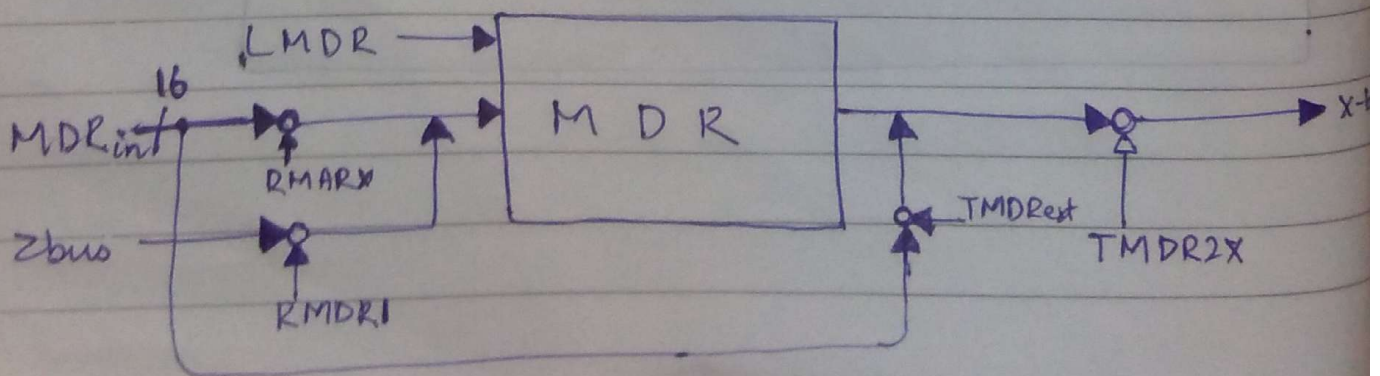
# Instruction Register



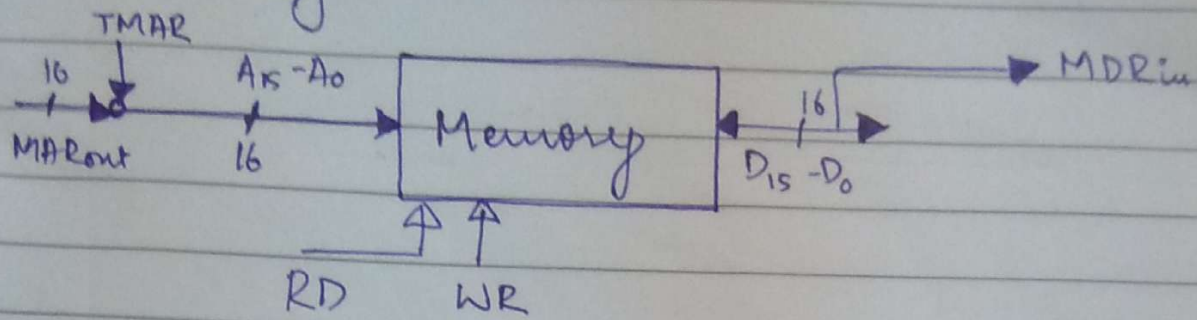
# Memory Address Register



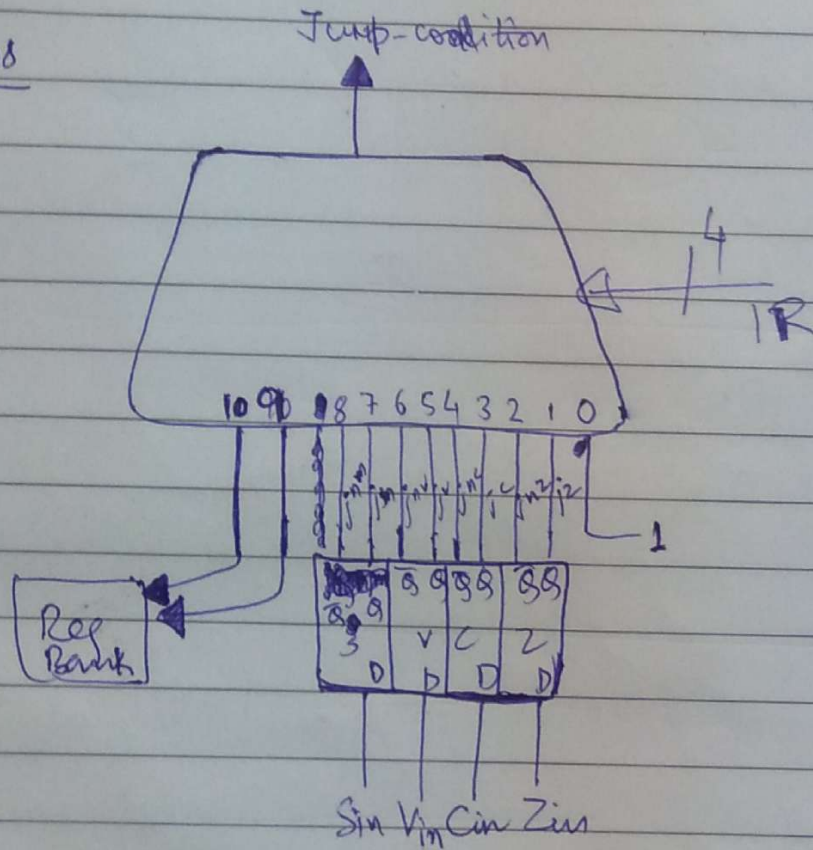
# Memory Data Register



## Memory



## Jumps



## Register MUXs

