## U18CO018

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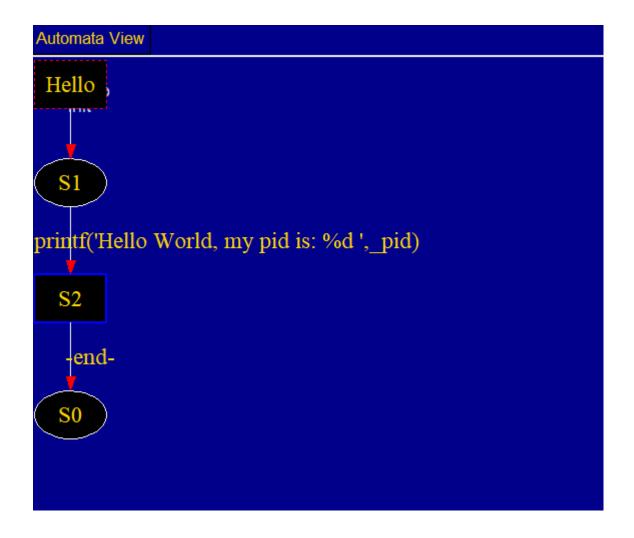
## Software engineering

## Lab Assignment 4

1. Write a program to create a process that prints "Hello World". Use run in init process to instantiate it and \_pid to print the ids of all create processes.

```
active proctype Hello() {
    printf("Hello World, my pid is: %d\n", _pid);
}
init {
    int lastpid;
    printf("init process, my pid is: %d\n", _pid);
    lastpid = run Hello();
    printf("last pid was: %d\n", lastpid);
}
```

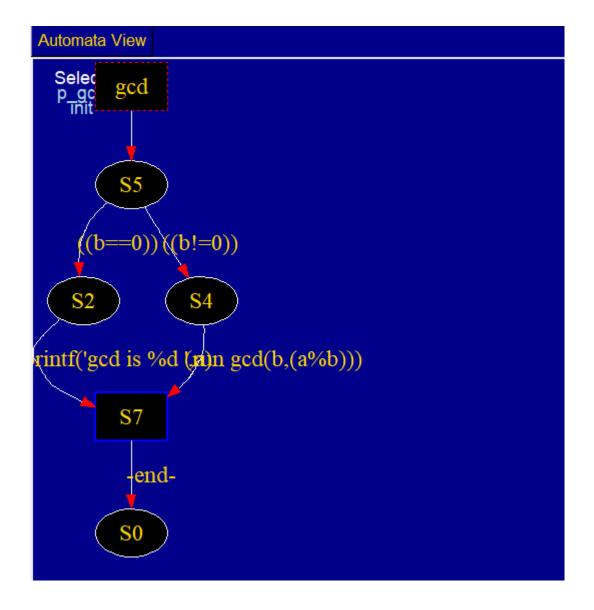
```
E:\Asem7\Software Engineering\Assignment4>spin prog1.pml
init process, my pid is: 1
Hello World, my pid is: 0
Hello World, my pid is: 2
last pid was: 2
3 processes created
```



2. Model Euclid's algorithm for Greatest Common Divisor.

```
proctype gcd(int a;int b) {
    if
      :: (b == 0) -> printf("gcd is %d\n", a)
      :: (b != 0) -> run gcd(b, a % b)
    fi
}
init {
        printf("gcd for 14 and 21\n");
      run gcd(14, 21);
}
```

```
E:\Asem7\Software Engineering\Assignment4>spin prog2.pml
gcd for 14 and 21
gcd is 7
5 processes created
```



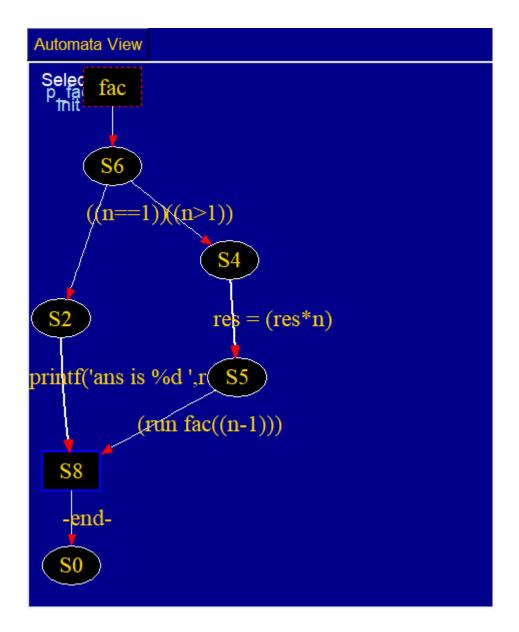
3. Create a process factorial(n, c) that recursively computes the factorial of a given non-negative integer "n".

```
int res = 1;

proctype fac(int n) {
    if
        :: (n == 1) -> printf("ans is %d\n", res)
        :: (n > 1) -> res = res * n; run fac(n-1)
        fi
}

init {
            printf("factorial of 5 is\n");
            run fac(5);
}
```

```
E:\Asem7\Software Engineering\Assignment4>spin prog3.pml
factorial of 5 is
ans is 120
6 processes created
```



4. Create a Promela model for producer-consumer problem with buffer size 5.

```
#define SIZE 5
chan c = [6] of \{byte\};
chan d = [true] of {bool};
byte fullness = 0;
active proctype producer() {
  byte data;
  do
  :: fullness < SIZE -> fullness = fullness + 1;
               c! data;
               data++;
               printf("item produced\n")
  :: d ? true;
  od
}
active proctype consumer() {
  byte data;
  do
  :: c?data; fullness = fullness - 1; d != true; printf("item consumed\n")
  od
}
active proctype monitor() {
  assert (fullness <= SIZE);</pre>
}
```

```
E:\Asem7\Software Engineering\Assignment4>spin prog4.pml
      item produced
      item produced
          item consumed
      item produced
          item consumed
          item consumed
      item produced
          item consumed
      item produced
          item consumed
      item produced
          item consumed
      item produced
          item consumed
          item consumed
      item produced
      item produced
          item consumed
      item produced
         item consumed
      item produced
          item consumed
      item produced
         item consumed
```

item consumed

