

About MNIT Jaipur

Malaviya National Institute of Technology Jaipur (Deemed University) is one of the premier NITs, designated with the status of "Institute of National Importance" by MHRD. The institute was established in 1963, and its campus spreads over 325 acres of lush green area in the central location of Jaipur city. The institute offers undergraduate and postgraduate courses (B.Tech., M.Tech. / MBA / M.Sc. & Ph.D.) to about 4500 students, in leading fields of engineering, technology, architecture, management & sciences. Through the internationally renowned faculty, laboratories with state of art equipments and excellent infrastructure, the institute is actively engaged in research, consultancy and developmental activities, besides imparting regular teaching.

Electrical Engineering Department

The Department is one of the oldest departments of the institute, offering a fine blend of experience and innovation in teaching. Presently offering under-graduate and post-graduate studies in Electrical Engineering and Power Systems Engineering respectively. The department is having around 30 research scholars, pursuing Ph.D. in varied fields of Electrical Engineering. The department provides a life-long learning experience, through its state of art laboratories, vast pool of courses, and industry-orientation. A strong collaborative framework with reputed universities in India and abroad, the department offers ample opportunities for individual growth. As a part of year-long Golden Jubilee Celebrations, various academic activities have been planned. The present programme is one of the events being organized by the department.

About Jaipur

Jaipur is well known as the Pink City and is the capital of Rajasthan. It is a famous heritage city, with numerous places of tourist's interest. Jaipur is well connected by road, rail and air services. It is about 250 Kms from Delhi. MNIT Jaipur is situated on Jawahar Lal Nehru Marg and is about 9 kms from

each from main railway station of Jaipur and from Central Bus Stand (Sindhi Camp). Airport (located at Sanganer) is about 3 kms away from the institute.

Eminent Speakers

Prof. Bhim Singh, IIT Delhi
Prof. S. N. Singh, IIT Kanpur
Prof. S. P. Singh, IIT Roorkee
Prof. Vishal Verma, DTU Delhi
Prof. Madhusudan Singh, DTU Delhi
Prof. R. A. Gupta, MNIT Jaipur
Prof. S. K. Jain, MANIT Bhopal
Dr. M. K. Pathak, IIT Roorkee
Dr. H. M. Suryawanshi, VNIT Nagpur
Dr. Sanjeev S. Chauhan, SLIET Longowal
Dr. Ashwani Chandel, NIT Hamirpur
Dr. Jagdish Kumar, PEC, Chandigarh
Dr. Subhash Dubey, GCET, Jammu

Accommodation

Limited accommodation is available in the Guest House / Hostels of the MNIT for outstation participants on nominal chargeable basis with an advance request. The participant will not be paid any TA/DA.

Dates to Remember

Last date of receiving completed Registration form	30 th April 2013
Confirmation of Selection by E-mail	2 nd May 2013
Course Duration	24 th -28 th June 2013

Address for Communication

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Assistant Professor

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Malaviya National Institute of Technology Jaipur

Faculty Development Programme

on

Real Time Implementation of Power Electronics Technologies

June 24th – 28th, 2013

Sponsored By

Department of Science & Technology (DST)

Govt. of Rajasthan,
The Institution of Engineers (India) &

TEQIP-Phase II



Patron

Prof. I. K. Bhat
Director MNIT Jaipur

Principal Coordinator

Prof. R. A. Gupta

Course Coordinators

Dr. Nitin Gupta

Dr. H. P. Tiwari and Dr. Rajesh Kumar

Organized By

Department of Electrical Engineering
Malaviya National Institute of Technology
Jaipur-302017

Objective of the Course

The role of power electronics is to process and control the flow of electrical energy by supplying voltages and currents in a form that optimally suit consumer loads. Power electronic based devices (Converters) are currently being used in electrical equipment in electrical distribution systems and more efficiently in operation with adjustable speed drives, traction, high-power factor converters, arc furnaces, HVDC systems, and renewable energy system, etc. They serve as an energy conditioner among various high-, medium-, and low-voltage consumers. Besides the advantage, power electronics based converters are also responsible for harmonic generation and draws reactive power from the supply system and thus cause the problem of power quality. Many new/modified converters and their topologies, algorithms are developed to overcome these issues with their implementation using high speed processors like DSPs, FPGAs, DSPACE. This course will offer a unique opportunity to the faculty members, researchers, engineers and research student working in the relevant topics of Power Electronics and Power Quality area to come closer through theoretical and algorithms implementation / demonstration sessions.

Course Contents

The major course contents of the program are:

- Converters- Principle, Modelling and Simulation Techniques using DSP/FPGA
- Power Quality Issues and Standards
- State-of-the-art of Active Power Filter, DSTATCOM, UPQC, PE Applications
- Power Electronic Converter Fed AC & DC Drives
- Smart Grids and Grid Connected PV
- Demonstration sessions (Simulation and Implementation) of Power Electronics based Algorithm using High Speed Controllers

The above contents of FDP will provide opportunity to faculty / researchers, engineers and utility/industrial personal to know the advancement, associated problems and measure to overcome them and possible area of future research.

Resource Persons

The various sessions of the FDP will be preceded by faculty members of MNIT Jaipur, faculty of other reputed institutions like IITs, NITs, and experts from industries. During the workshop, participants will have lectures from them and will have chance to interact with the external expert and local faculty working in power quality area.

Eligibility / Target Audience

Faculty members / Research students of Govt. / Govt. aided / self-financed engineering colleges, engineers from industries, Public Sector Undertaking and Utilities are eligible for attending this programme

How to Apply / Registration

Faculty of Academic Institutes	2500/-
Students / Research Scholars	1500/-
Persons from Industries	4000/-

Fee shall be paid by Demand Draft in favour of "Registrar, MNIT Jaipur" payable at Jaipur. Duly filled applications in the prescribed format and sponsored by the competent authority of the Institution may be sent to the Coordinator so as to reach on or before 30th April 2013. The applicant may also send an advance scanned copy of the application form and DD through E-mail. The registration fee will include registration kit, high tea, working lunch. Brochure and application form can be downloaded from the institute web site www.mnit.ac.in. Early registration is encouraged as the seats are limited. Registration charges are non-refundable for selected participants.

A City Tour may be arranged on 29th June on request of the participants

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&

TEQIP phase II, MNIT Jaipur

Sponsored

Faculty Development Programme

on

**"Real Time Implementation of
Power Electronics Technologies"**

June 24th – June 28th, 2013

Department of Electrical Engineering

Malaviya National Institute of Technology Jaipur

Registration Form

Name : _____

Designation: _____

Institute / Organization: _____

Qualification: _____

Specialization: _____

Mailing Address: _____

Phone (M) _____ (O) _____

E-mail: _____

Accommodation required? Yes / No

Registration fee details

Draft No. _____ Dt. _____

For Rs. _____ in favour of "Registrar, MNIT Jaipur" is enclosed.

Date: _____ Signature of Candidate

The applicant will be permitted to participate in the above programme, if selected.

Date: _____ Signature of Sponsoring Authority with Seal.