



Dhirubhai Ambani
Institute of Information and Communication Technology

EL467 - Digital Programming

-Prof. Yash Agarwal

- **Implementation of Elevator Control System using Verilog HDL**

By,

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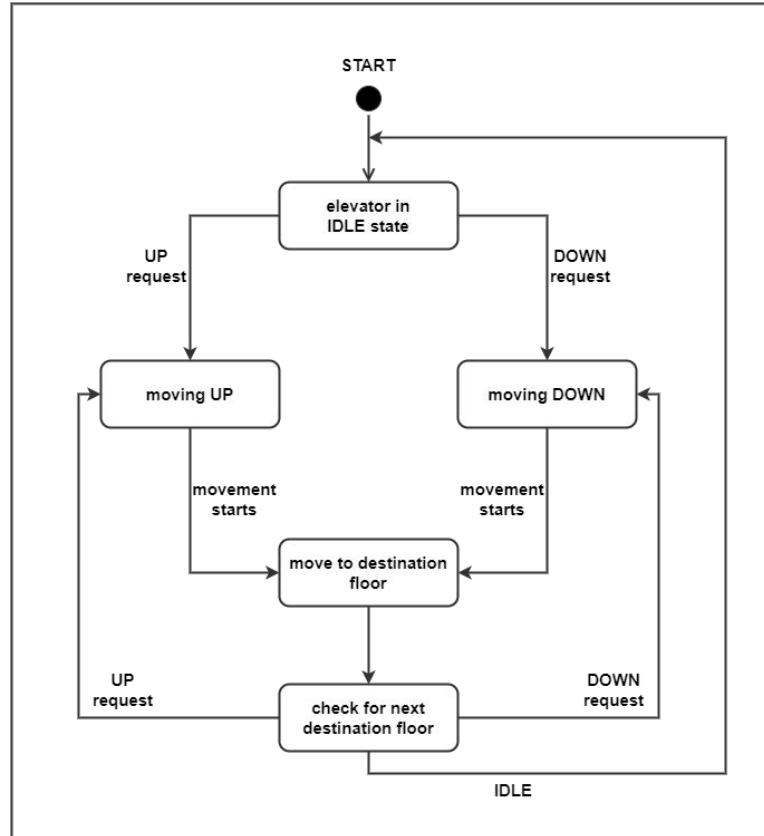


Abstract

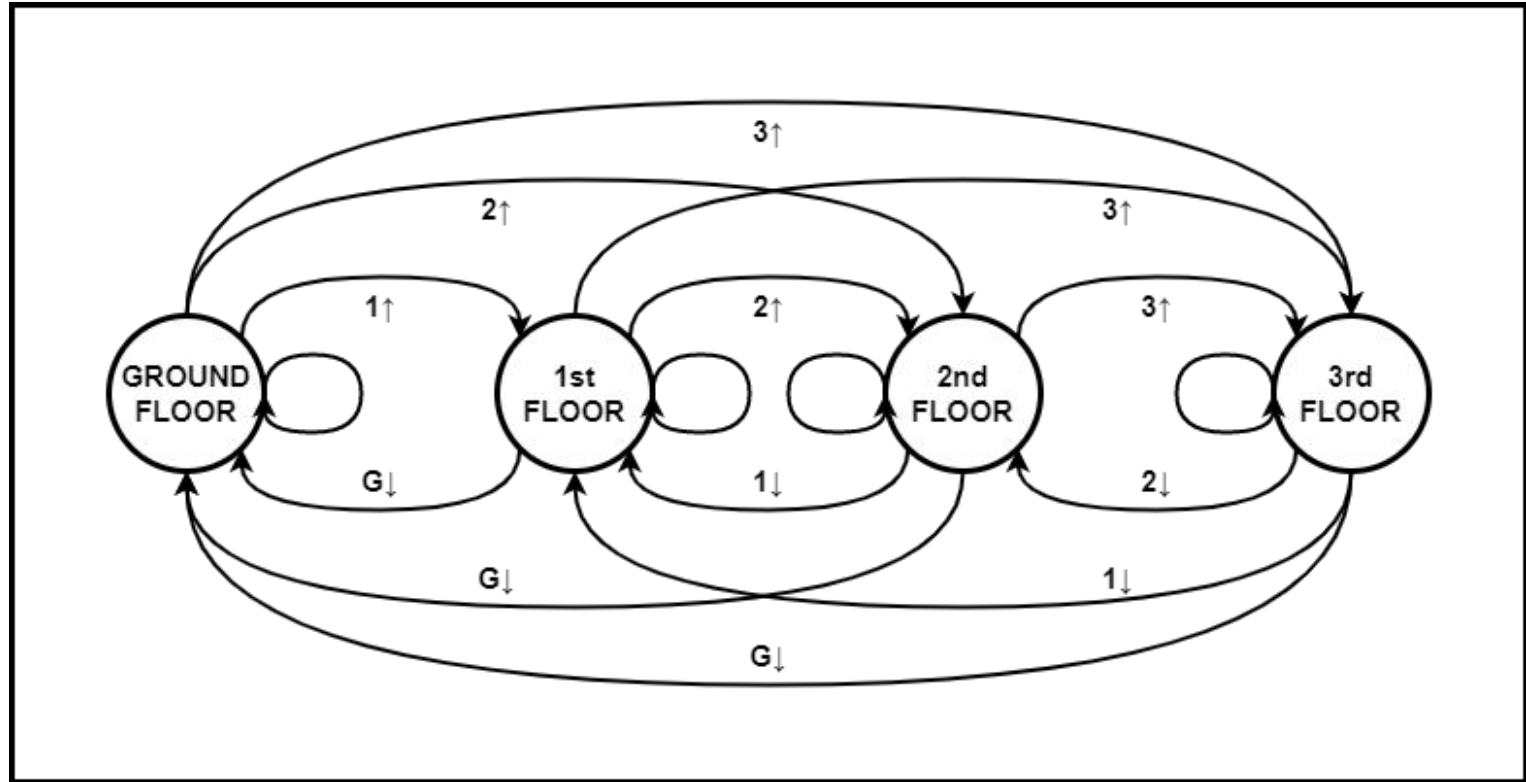


- Introduction
- Where it is used?
- How it works?

Working flow chart



State machine



Implementation code:



[Elevator.v](#)

[ElevatorTestbench.v](#)

Output waveform:

