## SHORT CV

CONTACT Information Department of Electronics & Communication, National Institute of Technology Meghalaya (NITM),

Bijni Complex, Laitumkhrah, Shillong, Meghalaya, India - 793003 Email: shubuit@gmail.com; shub@nitm.ac.in; Personal Website

Present Designation Assistant Professor at National Institute of Technology, Meghalaya (NITM)

KEY EXPERTISE

 $\label{thm:conductor} \textbf{Fabrication, Characterization, Simulation and Model III-N semiconductor devices; VLSI circuit;}$ 

Embedded Design.

HIGHEST DEGREE Doctor of Philosophy (PhD) in Micro-Electronics, Indian Institute of Technology, Kharagpur, 24<sup>th</sup>

November 2016

Publications Total of >30 referred scientific publications More than 45 citations giving an h-index of 4 (Web of

Science TM )

MENTORING Incubated Startup - Soil Agritech Pvt. Ltd.; 3 supervised B.Tech Theses (3 Ongoing).

TEACHING Taken lectures on various basic and advanced electronics and communication engineering courses

since 2016. Deliver invited lecture at the several Faculty Development Program & National seminars.

Management Presently, two projects of Department of Science and Technology under Device Development Pro-

gramme (150000 INR.) and Ministry of Earth Science under National Mission on Himalayan Studies

(NHMS) (**4470880 INR.**) are ongoing

EDUCATION INTERNATIONAL Coordinator and participant in international collaborative projects and networks, active research collaboration with more than 2 groups worldwide. Member of the international programme, advisory

or organizing committee of more than 10 international conferences

Reviewing Regular referee for numerous journals, including IEEE, Elseiver, Springer, Wiley etc and conferences.

Approximately 10 refereed articles / year.

RECOGNITION

- Received Marie Sklodowska-Curie Actions Seal of Excellence award from European Commission in 2018.
- Semifinalist for the Cisco Global Problem Solver Challenge in 2018.
- Received National Postdoctoral (NPDF) Fellowship in 2017.
- Received Italian Fellowship for Postdoctoral in 2017.
- TI IICDC 2016 (Website | Technical Video | Buisness Video)

Mentor of the Incubated Company – Successfully become top 10 teams in Texas Instruments Inc. India Innovation Challenge Design Contest 2016 with over 11000 participants from 624 colleges of India. Got Incubation of **INR 2500000/-** from Department of Science and Technology (DST), Texas Instruments, and anchored by NSRCEL of IIM Banglore for this project.

- International Travel Grant from Ministry of Human Resource and Development (MHRD) India for EMRS Fall Meeting, 2015, Warsaw Poland
- Received IIT KGP Institute Scholarship in 2012 for pursuing PhD from MHRD, Government of India.
- Received GATE Scholarship in 2010 for pursuing M.Tech from MHRD, Government of India.