Shubh Goel

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Education

- **ETH Zurich [5.835/6]** *2024 - 2026*
  MSc. in Computer Science, Major: Machine Intelligence, Minor: Data Management Systems Zurich, Switzerland
- **Indian Institute of Technology Delhi [9.154/10]** *2020 - 2024*
  B.Tech. in Electrical Engineering, Minor: Computer Science Delhi, India
- **Mount Carmel School Dwarka [97.8 %]** *2020*
  Central Board of Secondary Education (CBSE) (Grade XII); Delhi, India
- **Mount Carmel School Dwarka [95.4 %]** *2018*
  Central Board of Secondary Education (CBSE) (Grade X) Delhi, India

Scholastic Achievements

- **IIT Delhi Semester Merit Award:** Made it to the **top 7% (among 1200+ students)** in **6 out of 8** semesters
- **Department Rank:** Made it to the **top 3 out of 128** students in the Electrical Engineering undergraduate program at IIT Delhi
- **Joint Entrance Exam (JEE):** Secured an All India Rank (General) of **2209** (JEE Mains) & **1323** (JEE Advanced) among 1.1 M candidates
- Awarded **CBSE Merit Certificate** in grade X for standing in the **top 0.1%** students nationally in Science
- Awarded **CBSE Merit Certificate** in grade XII for standing in the **top 0.1%** students nationally in Math, Physics, and CS
- **National Science Olympiad 2018:** Won the **silver medal** for securing a **Zonal Rank 2** and an **International Rank 27**

Internships

- **JP Morgan Chase and Co., Mumbai/QR-Counterparty Credit Risk** *May, 2023 - Jun, 2023*
  IMM Back-testing Failure Analysis and Remediation
  – Improved **equity market factor back-testing failures by 40%** by proposing a new outlier removal algorithm for historical vol calibration
  – Worked on the analysis of **MtM** differences between **Commodity Swap/Index Swap** pricing models and FO models
  – **Automated** the process of Onboarding and Off-boarding of counterparties to/from **exposure back-testing** portfolio
- **Implementation of Contour Tracing Algorithms on an FPGA board | Prof. Subrat Kar** *Jun 2022-Nov 2023*
  Global Internship Program in Engineering Design and Innovation, IIT Delhi
  – Implemented Adapted and Segmented(AnS) **Pixel-Following, Vertex-Following and Run-Data-Base-Following algorithms** in Verilog
  – Implemented a novel hardware accelerator for contour tracing in image analysis and CV using the AnS algorithms on **Xilinx-7 FPGA platform**
  – Achieved a **speedup of 55x** compared to existing methods, making it ideal for parallel processing arrays and mesh-connected networks

Publications

- **MMA-Net: Multi-Modal Attention Network for 2-D Object Detection in Autonomous Driving [DOI]** *Sept 2024*
  Abhilash Gaur, Shubh Goel*, Kanishk Goel*, Seshan Srirangarajan, Po-Hsuan Tseng, Kai-Ten Feng* ICASSP 2025
- **A Hardware Accelerator for Contour Tracing in Real-Time Imaging [DOI]** *Nov 2023*
  Sonal Gupta, Shubh Goel, Ayush Kumar, and Subrat Kar, Senior Member, IEEE IEEE Sensors journal 2024

Academic Service

- **Graduate Teaching Assistant** *Sep 2025 - Jan 2026*
  263-5902-00 S: Computer Vision
- **Undergraduate Teaching Assistant** *Jan 2024 - May 2024*
  COL100: Introduction to Computer Science
- **Undergraduate Teaching Assistant** *Aug 2023 - Nov 2023*
  ELL101: Introduction to Electrical Engineering

Research Experience

- **Safe Guaranteed Domain Exploration with Autonomous Robots** *Mar 2025 - Sep 2025*
  Prof. Andreas Krause LAS, ETH Zurich
  – Helped in extending a theoretical safe exploration framework to real-world autonomous navigation applications in Isaac Sim
  – Improved robot trajectory smoothness and time taken by it to reach the goal, and implemented baselines for comparison
  – Built a scalable experimentation pipeline using Docker and Weights & Biases; conducted detailed timing analysis across distributed ROS 2 nodes
- **Evaluation of Visual Encoders on Dexterous Manipulation Tasks in Simulation [Preprint]** *Sep 2024 - Nov 2024*
  Prof. Marc Pollefeys CVG, ETH Zurich
  – Benchmarked SOTA visual encoders (DINO, R3M, HRP, etc.) on robotic manipulation evaluation suites like Franka Kitchen, Metaworld, etc
  – Recorded expert demonstrations using Rokoko Smartgloves for behaviour cloning on a novel benchmark with four dexterous manipulation tasks
- **Multi-Modal Sensor Fusion Model for Autonomous Driving[Bachelor's Thesis]** *Aug 2023 - May 2024*
  Prof. Seshan Srirangarajan IIT Delhi
  – Designed an **attention-based cross-modal feature learning** model having robustness to sensor failure, to fuse multi-modal sensor data
  – Utilized **CARLA**, an open-source simulator tailored for autonomous driving research, to create realistic urban environments
  – Tested the fusion model on object detection task, achieving a high Mean Average Precision of **80.09%** on the generated data

Course Projects

- **Table Cell Classification for Question Answering** *Mar 2024*
  Prof. Mausam IIT Delhi
  – Built a **Bi-LSTM** based model for predicting correct column and row for the given question and the corresponding table
  – Utilized **nltk** library for tokenization and initialized the token embeddings with pre-trained **Fasttext embeddings**
  – Achieved a high column accuracy of **89 %**, indicating the effectiveness of the developed model in correct column prediction
- **Planning in Markov Decision Process and Deep Q-Learning** *Mar 2024*
  Prof. Rohan Paul IIT Delhi
  – Implemented **value iteration**, **policy iteration**, and their respective variants to find an optimal policy for a given MDP
  – Implemented Q-learning using **OpenAI Gym** and **PyTorch** to learn a state-action value function for a **model-free** setting
- **Imitation Learning and Policy Gradients** *Apr 2024*
  Prof. Rohan Paul IIT Delhi
  – Implemented **DAGGER** and **Reinforce** to learn an optimal policy in **Hopper-v4** and **Ant-v4** environments from OpenAI Gym
  – Utilized **SAC algorithm** with **Hindsight Experience Replay** to train a goal-conditioned policy for the **PandaPush-v3** environment
- **Rollerball: A Chess variant** *Nov 2023*
  Prof. Mausam IIT Delhi
  – Engineered a Rollerball playing AI agent using **minimax** algorithm with **alpha-beta** pruning and early cut-off
  – Improved the agent's performance using **quiescence search, heatmaps, transposition tables and opening book**
- **Graph Neural Networks** *Nov 2023*
  Prof. Sayan Ranu IIT Delhi
  – Designed a **GIN** based architecture using **Pytorch Geometric** to predict whether a molecule inhibits HIV virus replication or not
  – Achieved a high ROC-AUC of **0.76** on the test data by introducing skip connections, dropout layers and weight decay

Technical Skills

Programming Languages(Proficiency Level)	JAVA/C/C++/Python(Advanced), MATLAB(Basic)
Software/Libraries/Frameworks	IsaacSim, ROS 2, Pandas, Numpy, Scikit-Learn, TensorFlow, PyTorch, Gensim, NLTK, HuggingFace, OpenAI Gym, Vivado, LaTeX

Key courses taken

Computer Science	Data Structure & Algorithms, Discrete Math, Analysis & Design of Algorithms, Computer Architecture, Machine Intelligence & Learning, Data Mining, Principles of Autonomous Systems, NLP, Deep Learning, Probabilistic AI, Computer Vision, Advanced OS, Foundations of Reinforcement Learning, Design of Parallel and High-Performance Computing (Ongoing)
Mathematics and Statistics	Probability & Stochastic Processes, Linear Algebra & Differential Equations, Calculus

Positions Of Responsibility

- **Core Team Member** *March 2021 - May 2024*
  Algorithms and Coding Club(ANCC) IIT Delhi
  – Assisted my team in launching the **Summer of Competitive Programming(SoCP)** program for the first time in July 2021
  – Led my team to **organize inter-college tournament**, a Competitive Programming knock out tournament **for the first time** in Tryst'23
  – Worked with my team in planning and organizing activities that helped the club to attain official status in 2023