Shubh Goel

Fourth Year Undergraduate
B.Tech in Electrical Engineering
(Minor in Computer Science)
Indian Institute Of Technology, Delhi

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Education

Degree/Certificate	Institute/Board	CGPA/Percentage	Year
B.Tech.	Indian Institute of Technology, Delhi	9.03*	2020-Present
CBSE/SSCE	Mount Carmel School Dwarka	97.8%	2020
CBSE/AISSE	Mount Carmel School Dwarka	95.4%	2018

*9.098 post Fall'23 semester(transcript awaited)

Scholastic Achievements

- **IIT Delhi Semester Merit Award:** Made it to the **top 7% (among 1200+ students)** in the semesters I, II, IV and V
- **Department Change:** Got selected for department change based on academic excellence in the first year at IIT Delhi
- **Department Rank:** Made it to the **top 4 out of 128** students in the Electrical department after completion of six semesters
- **JEE(Mains and Advanced):** Secured **AIR 2209(GE)** in JEE Mains and **AIR 1323(GE)** in JEE Advanced among 1.1 M candidates
- Awarded **CBSE Merit Certificate** in grade X for standing in the **top 0.1%** students nationally in Science
- Awarded **CBSE Merit Certificate** in grade XII for standing in the **top 0.1%** students nationally in Math, Physics, and CS
- **National Science Olympiad 2018:** Won the **silver medal** for securing a **Zonal Rank 2** and an **International Rank 27**

Internships

- **JP Morgan Chase and Co., Mumbai/QR-Counterparty Credit Risk** *May, 2023 - Jun, 2023*
  IMM Back-testing Failure Analysis and Remediation
  – Improved **equity market factor back-testing failures by 40%** by proposing a new outlier removal algorithm for historical vol calibration
  – Worked on the analysis of **MtM** diferences between **Commodity Swap/Index Swap** pricing models and FO models
  – **Automated** the process of Onboarding and Ofboarding of counterparties to/from **exposure back-testing** portfolio

- **Implementation of Contour Tracing Algorithms on an FPGA board | Prof. Subrat Kar** *Jun 2022-Nov 2023*
  Global Internship Program in Engineering Design and Innovation, IIT Delhi
  – Implemented adapted and segmented(AnS) **Pixel-Following, Vertex-Following and Run-Data-Base-Following algorithms** in Verilog
  – Implemented a novel hardware accelerator for contour tracing in image analysis and CV using the AnS algorithms on **Xilinx-7 FPGA platform**
  – Achieved a **speedup of 55x** compared to existing methods, making it ideal for parallel processing arrays and mesh-connected networks

Publications

- **A Hardware Accelerator for Contour Tracing in Real-Time Imaging(under review)** *Nov 2023*
  Sonal Gupta, Shubh Goel, Ayush Kumar, and Subrat Kar, Senior Member, IEEE IEEE Sensors journal

Projects

- **Multi-Modal Sensor Fusion Model for Autonomous Driving(Bachelor's Thesis - Ongoing)** *Aug 2023 - May 2024*
  Prof. Seshan Srirangarajan IIT Delhi
  – Designed an **attention-based cross-modal feature learning** model having robustness to sensor failure
  – Proposed a **Transformer-based** model to efficiently convert sensor features into **Bird's Eye View Representation**
  – Working on a planning model which would take the learned sensor features and output the required vehicle control

- **Denoising EEG Signals Using Deep Learning** *Jun 2022 - Dec 2022*
  Prof. Lalan Kumar IIT Delhi
  – Built **MLP**, **CNN-LSTM**, **LSTM-Resnet** based Deep Learning models to remove various **artifacts** from a raw EEG signal
  – Prepared the dataset for training by performing **Independent Component Analysis(ICA)** on the raw EEG Signals
  – Achieved a high PCC of **0.933**, indicating the efectiveness of the developed models in artifact removal and signal enhancement

- **Rollerball: A Chess variant** *Nov 2023*
  Prof. Mausam IIT Delhi
  – Engineered a Rollerball playing AI agent using **minimax** algorithm with **alpha-beta** pruning and early cut-off
  – Improved the agent's performance using **quiescence search, heatmaps, transposition tables and opening book**

- **Graph Neural Networks** *Nov 2023*
  Prof. Sayan Ranu IIT Delhi
  – Designed a **GIN** based architecture using **Pytorch Geometric** to predict whether a molecule inhibits HIV virus replication or not
  – Achieved a high ROC-AUC of **0.76** on the test data by introducing skip connections, dropout layers and weight decay

- **Medical Diagnosis** *Oct 2023*
  Prof. Mausam IIT Delhi
  – Implemented **Expectation Maximization** algorithm to learn a Bayesian network modelling eight diseases

- **Sports Complex Planning** *Aug 2023*
  Prof. Mausam IIT Delhi
  – Implemented **local search algorithm** for optimal sports zone placement to reduce the time spent walking in a day
  – Significantly improved the quality of the algorithm using random walks, random restarts and **backtracking with tabu list**

- **Hidden Organizations Finder** *Sep 2023*
  Prof. Mausam IIT Delhi
  – Developed an algorithm to convert the largest clique in a graph problem into its corresponding **SAT CNF form**
  – Reduced the number of literals and clauses by implementing a **parallel counter** using divide and conquer algorithm

- **Transactional Data Compression** *Aug 2023*
  Prof. Sayan Ranu IIT Delhi
  – Implemented the **FP-growth** algorithm efficiently to mine frequent item sets in the provided dataset, achieving a compression ratio of **11.862%**
  – Used heuristics such as performing the mining process several times with decreasing support values to improve the compression ratio

- **Creating a New Cryptocurrency** *Sep 2021 - Nov 2021*
  Prof. Venkata Koppula IIT Delhi
  – Built a nearly complete, **counterfeit-resistant**, **buyer-seller-miner** based cryptocurrency **"DSCoin"** using **blockchains**
  – Used **CRF(sha-256)** for encryption; data structures like **Merkle Trees, Linked Lists, Queues** for implementing **transaction blocks**
  – Handled **malicious miners** using incentive engineering; modified the blockchain structure from a Linked list to a tree-like structure

Technical Skills

Programming Languages(Proficiency Level)	JAVA/C/C++/Python(Advanced), MATLAB(Basic)
Software/Libraries/Frameworks	LaTeX, Vivado, Pandas, Numpy, Scikit-Learn, TensorFlow, PyTorch
Hardware Description Language	Verilog

Key courses taken

Computer Science	Data Structure & Algorithms, Discrete Math, Analysis & Design of Algorithms, Computer Architecture, Machine Intelligence & Learning, Artificial Intelligence (*fall'23 Semester*), Data Mining (*fall'23 Semester*)
Mathematics and Statistics	Probability & Stochastic Processes, Linear Algebra & Differential Equations, Calculus
Electrical	Signals & Systems, Circuit Theory, Digital Electronics, Embedded Systems, Control Engineering, Communication Engineering

Academic Service

- **Undergraduate Teaching Assistant** *Aug 2023 - Nov 2023*
  ELL101: Introduction to Electrical Engineering

Positions Of Responsibility

- **Core Team Member** *March, 2021 - Present*
  Algorithms and Coding Club(ANCC) IITD
  – Assisted my team in launching the **Summer of Competitive Programming(SoCP)** program for the frst time in July 2021
  – Led my team to **organize inter-college tournament**, a Competitive Programming knock out tournament **for the frst time** in Tryst'23
  – Worked with my team in planning and organising activities that helped the club to attain official status in 2023