## **REPORT -STATISTICS**

Number of instructions executed Number	of Number of Operand wrong fetch stalls branch entrance
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## Lab 4 test cases

evenorodd. out	6	28	4	0
descending.	337	1001	130	88

## Lab 1 codes

1-even	44	143	21	7
2-prime	8	27	3	1
3-descendin g	273	761	101	31
4-histogram	165	813	102	21
5-fibonnaci	100	355	45	9