

# **REPORT**

## **(LAB-5)**

<b>PROGRAM NAME</b>	<b>CYCLE COUNT</b>	<b>IPC</b>
prime.out	295	0.02372
histogram.out	8614	0.01671
fibonacci.out	4184	0.02174
even.out	1773	0.02086
descending.out	8928	0.02374
arithmetic.out	1837	0.02395
(lab-5)evenorodd.out	259	0.02316
(lab-5)descending.out	12991	0.02132

### **OBSERVATIONS**

.\_IPC decreases when there are more memory operations , we can see that in the case of histogram.out program.

. IPC with higher number of hazards have more number of iterations , thus leading to higher number of cycles .

Due to high latency of main memory , data hazards didn't happen as the next instruction is fetched from memory previous is already executed.

.The IPC observed was between 0.01671 to 0.02395.

.One cycle is equivalent to 40 or more cycles.