

Implementation of omega-automata simplification techniques in "Spot", a model checking library.

When "Spot" SAT-based minimization meets incremental solving.

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Part I

Report

Chapter 1

LRDE Presentation

1.1 Line of business

The LRDE (Research and Development Laboratory of EPITA) is focused on fundamental research and development in computer science. Its main areas of expertise are:

- Image processing and pattern recognition
- Automata and verification
- Performance and genericity

Building on its solid scientific production and academic collaborations, the laboratory has industrial contracts, conducts internal research projects and participates in collaborative academic research projects.

Its members also give classes to students at EPITA from the first year of engineering.

1.2 The Laboratory

The LRDE (<https://www.lrde.epita.fr/wiki/Home>) was created in February 1998 to promote the research activity at EPITA and to allow students to be involved into important research projects.

The research activity at LRDE is focusing on subjects related to the school with the aim of getting recognition in the scientific domain through publications and by working together with other research centers.

One particularity of the LRDE is the will to create a bond between traditional teaching given to EPITA students and teaching through research. The point of this is to:

- participate to the production of knowledge in computer science and to promote the image of EPITA in scientific domain.
- develop LRDE student's formation through research and allow them to access a third cycle formation.

1.3 Members

The laboratory is currently composed of thirteen permanent members, including teacher-researchers, engineers and administration.

In addition to permanent staff, the LRDE also hosts PhD students. Currently, there are five of them. During the whole duration of their doctoral studies, they work with two advisor researchers, one of the LRDE and one of another university (joint supervision in partnership).

Each year, the permanent members recruit third year students from EPITA, whom will stay until the end of their studies, following a dedicated study specialisation at EPITA . Hence, the laboratory hosts two generations of students that can grow to a number between ten to fifteen.

1.4 Services

The LRDE is working on four different axis:

1.4.1 Image Processing

Olena



The Olena project (<https://olena.lrde.epita.fr>) consists of a generic image processing library. Its objective is to implement a platform of numerical scientific computations dedicated to image processing, pattern recognition and computer vision. This environment is composed of a generic and efficient library (Milena), a set of tools for shell scripts and a visual programming interface. The project aims at offering an interpreted environment like MatLab or Mathematica. It provides many ready-to-use image data structures (regular 1D, 2D, 3D images, graph-based images, etc.) and algorithms. Milena's algorithms are built upon classical entities from the image processing field (images, points/sites, domains, neighborhoods, etc.).

Each of these parts imply its own difficulties and require the development of new solutions. For example, the library, which require the entirety of low level features on which it relies on to be both efficient and generic — two objectives that are hard to meet at the same time in programmation. Fortunately, the object oriented programming eases this problem if we avoid the classical object modeling with inheritance and polymorphism. Hence, this genericity allows the development of efficient and re-usable code - i.e. developers or practitioners can easily understand, modify, develop and extend new algorithms while retaining the core traits of Milena: genericity and efficiency. The Olena platform uses this paradigm. The project already addressed the problem of the diversity of data and data structures.

Furthermore, the people working on this project were able to put in light the existence of conception models related to generic programming. Olena is an open source project under General Public License (GPL) version 2.

Climb

The Climb team of the laboratory has chosen to focus on the persistent question of performance and genericity, only from a different point of view.

The purpose of this research is to examine the solutions offered by languages other than C++, dynamic languages notably, and Lisp in particular. C++ has its drawbacks, it is a heavy language with an extremely complex and ambiguous syntax, the template system is actually a completely different language from standard C++ and finally it is a static language. This last point has significant implications on the application, insofar as it imposes a strict chain of Compilation \rightarrow Development \rightarrow Run \rightarrow Debug, making for example rapid prototyping or human-machine interfacing activities difficult. It becomes therefore essential to equip the involved projects with a third language infrastructure that is rather based on scripting languages.

The Climb project aims at investigating the same domain as Olena, but starting from an opposite view. It express the same issues following an axis of dynamic genericity and compares the performance obtained by some Common Lisp compilers with those of equivalent programs written in C or C++.

1.4.2 Finite state machine manipulation

Vcsn



The VCSN project (<https://vcsn.lrde.epita.fr>) is a finite state machine manipulation platform developed in collaboration with the ENST. Finite state machines, also called automata, are useful for language treatment and task automation. In the past, such platforms, like "FSM", were supposed to work for problems of industrial scale. Hence, for efficiency reasons, they were specialized in letter automata. On the other hand, platforms like "FSA" were based on a more abstract approach. VCSN tries to answer both of these issues by using techniques of static and generic programming in C++.

VCSN can then support the entirety of automata with multiplicity in any kind of semiring. Thanks to generic programming techniques, it is not necessary to code a single algorithm once for each type of automata anymore. A single abstract version is sufficient, and this without losing efficiency. It is not necessary to handle C++ perfectly to be able to use the platform thanks to an interpreter conceived to highlight all of the system's potential. This environment should allow researchers

to experiment their ideas and beginners to practice with an intuitive interface.

VCSN is an open source project under GPL license.

1.4.3 Model checking

Spot



Spot (<https://spot.lrde.epita.fr/>) is a library of algorithms for "model checking", which is a way to check that every possible behavior of a system satisfy its given properties. Spot allows to express those properties using linear-time temporal logic (LTL). It corresponds to classical propositional calculus (with its "or", "and" and "not" operators) equipped with temporal operators to express things such as "in a future time" or "anytime since now". Spot also supports arbitrary acceptance condition, transition-based acceptance and four different representation formats of ω -automata (HOA, never claims, LBTT, DSTAR). All those terms will be explained in the 'basic concepts' section.

Such formulas seen above (LTL formulas) can be translated to automata (Spot implements different algorithms), such that verifying that the behavior of a model satisfy a formula can be reduced to operations between two automata (here again Spot implements different algorithms). This approach can be applied to different kind of systems: communication protocols, electronic circuits, programs...

This project was born in the MoVe team at LIP6, but since 2007 it is mainly developped by the LRDE, with some occasional collaborations with LIP6. It is distributed under a GNU GPL version 3 license.

1.4.4 Speaker recognition

Speaker ID

The Speaker Recognition team is working on Machine Learning solutions applied to Speaker Recognition tasks. They propose statistical representations of speech signal which are more robust to the problem of session and channel variabilities.

A speaker must always be identified, whether he is ill, suffering from sore throats, or his current emotions bring change to his voice. To do this, all the characteristics of a voice that can change depending on any external parameter must be ignored. This is one of the issues the Speaker ID team is facing.

They participated in the evaluation campaign of speaker verification systems organized by NIST (the National Institute of Standards and Technology) which organizes competitions in various fields, both to stimulate research and to define new

standards since the beginning of the project.

The work of LRDE Speaker ID team is conducted in collaboration with the Spoken Language Systems Group of the MIT Computer Science and Artificial Intelligence Laboratory (<http://groups.csail.mit.edu/sls/>).

1.5 The internship in the company's work

This internship took place within the team of model checking. It was essentially focused on the improvement of the SAT-based minimization of ω -automata. It covers one of the many features of the Spot library.

Chapter 2

Spot

Spot was first presented in 2004 [4]. It was purely a library until Spot 1.0 [3], when command-line tools for LTL manipulation and translation of LTL to some generalizations of Büchi Automata have started to be distributed. Today, Spot 2.0 supports more tools with arbitrary acceptance conditions as described in the Hanoi Omega Automata format (HOA) [7] and python bindings usable in interactive environments such as IPython/Jupyter [5].

2.1 Structure

The Spot project can be broken down into several parts, as shown in Figure 2.1. Orange boxes are C/C++ libraries. Red boxes are command-line program. Blue boxes are Python-related.

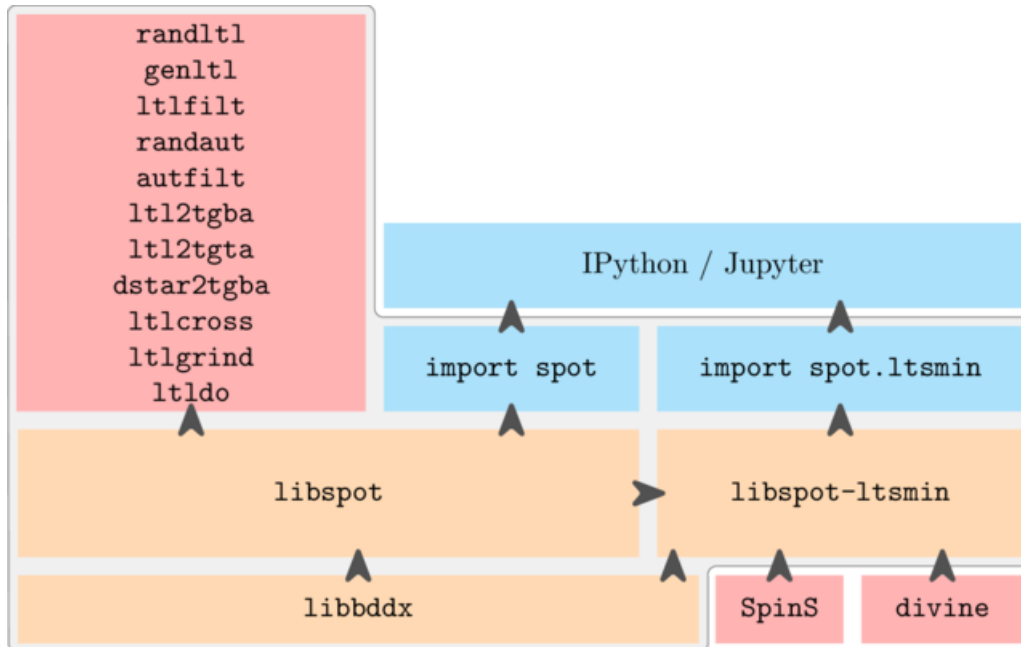


Figure 2.1: Architecture of Spot

Spot is actually split in three libraries:

- `libbddx` is a customized version of BuDDy for representing Binary Decision Diagrams which we use to label transitions in automata, and to implement a

few algorithms.

- libspot is the main library containing all data structures and algorithms.
- libspot-ltsmin contains code to interface with state-spaces generated as shared libraries by LTSmin.

2.2 Command-line tools

Spot 2.0 installs the following eleven command-line tools, that are designed to be combined as traditional Unix tools.

SPOT 1.0	randltl	Generates random LTL/PSL formulas
	genltl	Generates LTL formulas from scalable patterns
	ltlflt	Filter, converts, and transforms LTL/PSL formulas
	ltl2tgba	Translates LTL/PSL formulas into generalized Büchi automata[1], or deterministic parity automata (new in 2.0)
	ltl2tgta	Translates LTL/PSL formulas into Testing automata[2]
	ltlcross	Cross-compares LTL/PSL-to-automata translators to find bugs (works with arbitrary acceptance conditions since Spot 2.0)
	ltlgrind	mutates LTL/PSL formulas to help reproduce bugs on smaller ones
	dstar2tgba	converts ltl2dstar automata into Generalized Büchi automata[6]
	randaut	generates random ω -automata
	autfilt	filters, converts and transforms ω -automata
	ltldo	runs LTL/PSL formulas through other translators, providing uniform input and output interfaces

Figure 2.2: Spot tools description

As you see, the first six tools were introduced in Spot 1.0 [3], and have since received several updates. The other tools were introduced after.

2.3 The Python Interface

Similar tasks can be performed in a more "algorithm-friendly" environment using the Python interface. Combined with the IPython/Jupyter notebook [5] (a web application for interactive programming), this provides a nice environment for experiments, where automata and formulas are automatically displayed.

2.4 Workflow

Working on any project of the LRDE implies to follow some rules. That allows a better integration of each member. Once a patch is ready, any member of the model checking team can re-read the patch and make suggestions.

2.4.1 Coding conventions

As Spot is a free software, uniformity of the code matters a lot. Some coding conventions are used so that the code looks homogeneous. Here are some points:

- UTF-8 is used for non-ASCII characters.
- tabs are not used for indentation in c++ files, only spaces, in order to prevent issues with people assuming different tab widths.
- `#include` with angle-brackets refers to public Spot headers (i.e those that will be installed, or system headers that are already installed).
- `#include` with double quotes refer to private headers that are distributed with Spot.
- ... (see <https://gitlab.lrde.epita.fr/spot/spot/blob/master/HACKING> for more details)

2.4.2 Git Versionning Tool

The versionning tool used in Spot is Git. All development branches except 'master' and 'next' follows a particular naming convention: {initials}/{subject of work}). This allows a quick glance to identify who works on which branch and on what.

Concerning commits, large commits introducing a feature are preferred to many small commits covering the same feature. Suppose that a new feature must be implemented and needs 3 key steps. Even if each step is done in many commits during the development, at the end, it's better to squash commits so as to have only 3 large commits representing the 3 key steps.

Also, if at any moment it turns out that a previous work could have been done otherwise, any update must be applied directly to the commit concerned - each commit must actually insert code in its final form.

2.4.3 Adding Tests

Any implementation done must be tested. For the purpose on one hand to avoid regression and on the other hand to ensure the code runs as expected. All tests are located in the 'tests' folders. Most of them are written in Python (using the python bindings) or shell script.

Chapter 3

Basic concepts

Chapter 4

Completed work

4.1 Specifications

4.1.1 Overall goal

4.1.2 Detailed explanation of the results to be obtained

4.2 Activity report

4.2.1 Selected areas of study and research

4.2.2 Conduct of studies

4.3 Interpretation and critique of results

Chapter 5

Bibliography and glossary

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Part II

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Appendix A

Company documentation

Appendix B

Hardware / Software Documentation

Appendix C

Gross results